

101529C

LR1000-500-1.5 Multiple Channel Delay Line Module

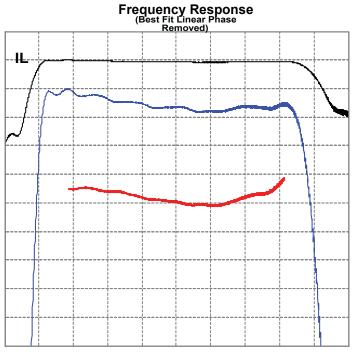
Specifications

| Parameter | Symbol | Unit | |
|----------------------------------|-----------------|------------------------|--------|
| Component | | | 101528 |
| Center Frequency | F ₀ | MHz defined | 1000 |
| Bandwidth | В | MHz defined | 550 |
| -3 dB Bandwidth | B ₃ | MHz min | 617.5 |
| -40 dB Bandwidth | B ₄₀ | MHz max | 685 |
| Delay | T ₀ | µsec typ | 1.502 |
| Amplitude Ripple | | dB _{P-P} max | 2 |
| Phase Ripple | | deg _{P-P} max | 10 |
| Rejection | | dB max | -46 |
| Spurious for $ t - T_0 > .9T_0$ | | dB max | 9 |
| Gain | | dB min | -6 |
| Power In for -1 dB Comp | | dBm min | 16 |
| Noise Figure | | dB max | 10 |
| Return Loss | | dB min | 10 |
| DC Power | 15V@1A | | |

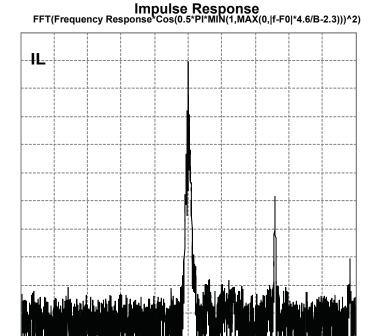
Notes

- 1. Multiple Channel Delay Line Module. Unit consists of four independent identical channels.
- 2. Center Frequency (F₀) and Bandwidth (B) are defined, not measured.
- 3. Gain is the maximum gain for $|f F_0| < .5B$
- 4. Ripple spec applies to $|f F_0| < .4B$, and is doubled for $.4B < |f F_0| < .5B$
- 5. Specifications are at 22 °C only. Unit will operate undamaged from -54 °C to 125 °C with shifts $dF_0 = -x * F_0$, $dT_0 = x * T_0$, where x = 94E-6 * (temperature 22 °C)

Typical Performance



10 dB/div, 1 dB/div, 10 deg/div, 79.000 MHz/div



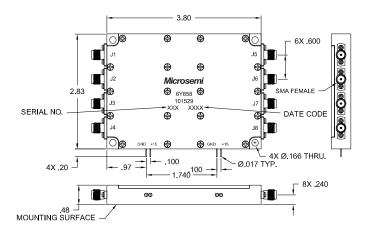
10 dB/div, 0.203 us/div



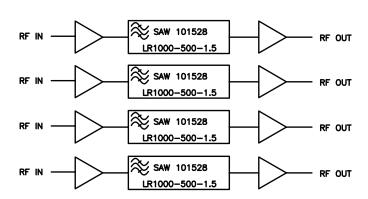
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Package Outline



System Block Diagram





Microsemi Corporate Headquarters

One Enterprise, Aliso Viejo, CA 92656 USA Within the USA: +1 (800) 713-4113 Outside the USA: +1 (949) 380-6100 Fax: +1 (949) 215-4996 Email: sales.support@microsemi.com www.microsemi.com

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