Microsemi Corporation
May 19, 2017

Customer Notification No: CN17012
Addendum

Customer Advisory Notice (CAN)

Subject: Changes to Libero License Offerings

Summary of Changes:
This document is an addendum to CN17012. The following is a list of the changes:
- Clarified the types of IGLOO2 and SmartFusion2 devices covered under the Free Libero Silver License.
- Added ProASIC3 and Fusion to the Libero License Requirement table.
- Corrected the license types available for IP DirectCores.

Description

Microsemi has made the following changes to the Libero license options for Libero SoC and Libero IDE that are FPGA design tools used with Microsemi FPGAs and SoC FPGAs. These changes will be rolled out with Libero SoC v11.8 in the first week of March 2017 and are applied with the next renewal of your license. The following sections list out the features of all the offered Libero licenses.

Introducing Free Libero Evaluation License:

- Supports all ProASIC®3, IGLOO®, SmartFusion®, Fusion®, SmartFusion®2, IGLOO®2, and RTG4™ families, RT3PE600L and RT3PE3000L devices
- Includes ModelSim Pro with mixed-language simulation
- Programming and debug functions are not included
- See the IP Cores section for IP offerings and features
- This license does not support Libero IDE families; ProASIC®, ProASIC PLUS®, antifuse, and RT antifuse families

Introducing Free Libero Silver License:

- Supports all SmartFusion, ProASIC3, IGLOO, and Fusion devices. Supports IGLOO2 and SmartFusion2 devices with up to 25K logic elements
- Supports single language simulation
- See the IP Cores section for IP offerings and features
- IGLOO2 and SmartFusion2 security devices, radiation-tolerant and antifuse devices are not supported by this license
- This license does not support Libero IDE product families

Libero Gold License Updates:

- Libero gold license is now a paid license. The supported devices are listed in the table in Libero License Requirement section
- Includes all SmartFusion2 and IGLOO2 devices
- Includes ModelSim Pro with mixed-language simulation
- See the IP Cores section for IP offerings and features

Libero Standalone Archival License Updates:

- License validity period extended from 30 years to perpetual. The supported devices are listed in the table in Libero License Requirement section
- See the IP Cores section for IP offerings and features

Libero License Requirement

The following table gives details of the license types required for various software types, product families, and respective devices.

Product Family	Device	Software		License Type			
		Libero IDE	Libero SoC	Evaluation (Free)	Silver (Free)	Gold	Platinum/ Standalone
RTG4	RT4G150		✓	✓			✓
SmartFusion2, IGLOO2	M2S005, M2S010, M2S025 (T devices included) M2GL005, M2GL010, M2GL025 (T devices included)		✓	✓	✓	✓	✓
	All SmartFusion2 and IGLOO2 devices including S devices		✓	✓		✓	✓
SmartFusion, IGLOO, ProASIC3, Fusion	All Devices		✓	✓	✓	✓	✓
ProASIC and ProASIC^PLUS	All Devices	✓				✓	✓
RTAX-S	RTAX250S, RTAX1000S	✓				✓	✓
	RTAX2000S, RTAX4000S	✓					✓
RTAX-DSP	RTAX2000D, RTAX4000D	✓					✓
RT ProASIC3	RT3PE600L		✓	✓		✓	✓
	RT3PE3000L		✓	✓			✓
RTSX-SU	All Devices	✓				✓	✓
Axcelerator	AX125, AX250, AX500, AX1000	✓				✓	✓
	AX2000	✓					✓
SX-A, eX, MX	All Devices	✓				✓	✓

License Features

The following table lists various features of the available licenses.

	Evaluation	Silver	Gold	Platinum	Platinum Archival	Standalone Archival	Standalone (1 Year)
Validity	60 days	1 Year	1 Year	1 Year	20 Years (No Upgrades)	Permanent (No Upgrades)	1 Year
Licensing Requirements per Device	See Licensing Requirements Table			All	All	All	All
IP DirectCores	Libero IP bundle obfuscated and selected RTL IPs	RTL for Libero IP bundle cores					
SoftConsole	✓	✓	✓	✓	✓	✓	✓

FlashPro Software	✓	✓	✓	✓	✓	✓	✓
Simulation	Mixed language	Single language	Mixed language	Mixed language	Mixed language	X	X
Synthesis (Synplify Pro)	✓	✓	✓	✓	✓	X	X
Programming	X	✓	✓	✓	✓	✓	✓
Debug	X	Identify and SmartDebug*	Identify and SmartDebug*	Identify and SmartDebug*	Identify and SmartDebug*	SmartDebug*	SmartDebug*
Price / Renewal	$0(Node Locked, and Floating)	$0 (Node Locked, and Floating)	$995 / $695 (Node Locked and Floating)	$2995 / $2495 (Node Locked) $3495 / $2995 (Floating)	$6995 (USB Node Locked)	$6995 (Floating)	$1995 / $1495 (Node Locked and Floating)

Note: *SmartDebug is supported for SmartFusion2, IGLOO2, and RTG4 families only.*

The ordering information for Gold and Platinum licenses is available on Microsemi website at:
http://www.microsemi.com/products/fpga-soc/design-resources/licensing#ordering

Changes to the Kit License Requirements

The following table lists the changes in the license types for various kits.

Kit Part Number	Kit Description	Device on Kit	Prior License Type	New License Type
M2S150-ADV-DEV-KIT	SmartFusion2 SoC FPGA Advanced Development Kit	M2S150TS	Platinum	Gold
M2S090TS-EVAL-KIT	SmartFusion2 SoC FPGA Security Evaluation Kit	M2S090TS	Platinum	Gold
SF2060-STARTER-KIT	SmartFusion2 Starter Kit	M2S060	Gold	Gold
SF2-STARTER-KIT	SmartFusion2 Starter Kit	M2S050	Gold	Gold
SF2-484-STARTER-KIT	SmartFusion2 Starter Kit	M2S010	Gold	Silver
M2GL-EVAL-KIT	IGLOO2 FPGA Evaluation Kit	M2GL010T	Gold	Silver
VIDEO-DC-PRL	Imaging and Video Solution Kit	M2S150TS	Platinum	Gold
SF2-MC-STARTER-KIT	Motor Control kit	M2S010	Gold	Silver
RTG4-DEV-KIT	RTG4 Development Kit	RT4G150	Platinum	Platinum
AGLN-NANO-KIT	IGLOO nano Starter Kit	AGLN250	Gold	Silver
AGLP-EVAL-KIT	IGLOO PLUS Starter Kit	AGLP125	Gold	Silver
M1AGL1000-DEV-KIT	ARM Cortex-M1-Enabled IGLOO Development Kit	M1AGL1000	Gold	Silver
A3PE-STARTER-KIT-2	ProASIC3/E Starter Kit	A3PE1500	Gold	Silver
M1A3PL-DEV-KIT	Cortex-M1-enabled ProASIC3L Development Kit	M1A3P1000L	Gold	Silver
A2F500-DEV-KIT-2	SmartFusion Development Kit	A2F500	Gold	Silver
A2F-EVAL-KIT-2	SmartFusion Evaluation Kit	A2F200	Gold	Silver
MIXED-SIGNAL-DC	Mixed Signal Daughter Card	N/A	Gold	Silver
DMPM-DC-KIT	DMPM Daughter Card Kit	N/A	Gold	Silver

AFS-EVAL-KIT	Fusion starter kit	AFS600	Gold	Silver
M1AFS-ADV-DEV-KIT-PWR-2	Fusion Advanced Development Kit	M1AFS1500	Gold	Silver
M1AFS-EMBEDDED-KIT-2	Fusion Embedded Development Kit	M1AFS1500	Gold	Silver
CORE1553-DEV-KIT-2	Core1553 Development Kit	M1AFS1500	Gold	Silver
CORE429-DEV-KIT-3	Core429 Development Kit	M1AFS1500	Gold	Silver

IP Cores

The following tables list the IP cores for various Libero licenses.

IP Core Name	Libero IP Core Bundle						
	Evaluation	Silver	Gold	Platinum	Platinum Archival	Standalone Archival	Standalone Platinum
CoreJESD204BRX	Not Supported		RTL source				
CoreJESD204BTX	Not Supported		RTL source				
CoreQDR	Obfuscated		RTL source generator				
CoreRGMII	Obfuscated		RTL source				
CoreRSDEC	Not Supported		RTL source generator				
CoreRSENC	Not Supported		RTL source generator				

IP Core Name	Libero IP Core Bundle						
	Evaluation	Silver	Gold	Platinum	Platinum Archival	Standalone Archival	Standalone Platinum
Core10/100	RTL source						
Core10/100_AHBAPB	RTL source						
Core1588	RTL source						
Core16550	RTL source						
Core3DES	RTL source						
Core8051s	RTL source						
CoreABC	RTL source						
CoreAES128	RTL source						
CoreAHB	RTL source						
CoreAHB2APB	RTL source						
CoreAHBLite	RTL source						
CoreAHBLSRAM	RTL source						
CoreAHBLtoAXI	RTL source						
CoreAhbNvm	RTL source						
CoreAhbSram	RTL source						

CoreAHBtoAPB3	RTL source
CoreAI	RTL source
CoreAPB	RTL source
CoreAPB3	RTL source
CoreAPBLSRAM	RTL source
CoreApbNvm	RTL source
CoreAPBSRAM	RTL source
CoreAXI	RTL source
CoreAXItoAHBL	RTL source
CoreCFI	RTL source
CoreConfigMaster	RTL source
CoreConfigP	RTL source
CoreCORDIC	RTL source generator
CoreDES	RTL source
CoreDDR	RTL source
CoreEDAC	RTL source generator
CoreFIFO	RTL source generator
CoreFIR	RTL source generator
CoreFMEE	RTL source
CoreFROM	RTL source
CoreGPIO	RTL source
CoreHPDMACtrl	RTL source
CoreI2C	RTL source
CoreInterrupt	RTL source
CoreLPC	RTL source
CoreMACFilter	RTL source
CoreMBX	RTL source
CoreMDIO_APB	RTL source
CoreMemCtrl	RTL source
CoreMMC	RTL source
CorePCS	RTL source
CorePWM	RTL source
CoreQEI	RTL source generator
CoreRemap	RTL source
CoreResetP	RTL source
CoreRMII	RTL source
CoreSDLC	RTL source
CoreSDR	RTL source
CoreSDR_AHB	RTL source
CoreSDR_AXI	RTL source
CoreSF2Config	RTL source
CoreSF2Reset	RTL source

CoreSMIP	Obfuscated
CoreSPI	RTL source
CoreSysServices	RTL source
CoreTBItoEPCS	RTL source
CoreTimer	RTL source
CoreUART	RTL source
CoreUART_APB	RTL source
CoreWatchdog	RTL source
Cortex-M1	Pre-placed design block

Contact Information

For more information, contact Microsemi's Technical Support at soc_tech@microsemi.com.

The part numbers for Gold and Platinum licenses are available at: http://www.microsemi.com/products/fpga-soc/design-resources/licensing#ordering

To purchase a license, contact your local Microsemi sales representative.

Regards,

Microsemi Corporation

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