

Microsemi Space Forum India

August 11, 2015 Courtyard Marriott Ahmedabad, India

August 13, 2015 The Leela Palace Bangalore, India



Program

Welcome to the 2015 Microsemi Space Forum!

Microsemi has been developing space solutions for almost six decades and has played an important role in a wide variety of space programs globally.

As part of our ongoing commitment to the space industry, this forum is a unique opportunity to see Microsemi's technical experts, along with our partners, presenting leading space-related products, capabilities, technology roadmaps and solutions, including the newly introduced RTG4™ radiation-tolerant field programmable gate arrays (FPGAs) and the LX7730 telemetry controller, as well as a variety of its radiation-hardened DC-to-DC converters, precision time and frequency solutions, linear and point of load (POL) hybrids, custom hybrid solutions and radiation-hardened discretes—including the broadest portfolio of JANS Class diodes and bipolar transistor products.

This Space Forum is specifically designed for space industry system-level architects, research and development engineers, design and component engineers, and other space industry professionals seeking the latest innovations in this unique market. The Digital Track and Timing, Mixed-Signal and Power Track will run simultaneously after the General Session.

Space Forum Partners

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General Session

- 8.15-9.15 AM **Registration**
- 9.15-9.20 AM **Welcome**
Parag Kulkarni, India Country Manager, Corporate Sales
- 9.20-9.35 AM **Opening Remarks**
Siobhan Dolan Clancy, VP Worldwide Business Development, Aerospace
- 09.35-10.00 AM **Microsemi Space Technology Journey**
Mathieu Sureau, Director IC Engineering
- 10:00-10.15 AM **Break**

Digital Track

- 10.15-10.45 AM **RTG4 Product Overview**
Ken O'Neill, Director of Marketing, Space and Aviation
- 10.45-11.15 AM **RTG4 Architecture Details**
Minh Nguyen, Senior Marketing Manager, Space and Aviation
- 11.15-11.35 AM **RTG4 Reliability and Qualification**
Prabhu Mohan, Senior Manager, Engineering
- 11.35-11.55 AM **RTG4 Radiation Test Results and Test Plans**
Dr J. J. Wang, Senior Principal Engineer
- 11.55-12.55 PM **Lunch**
- 12.55-1.15 PM **Building High Reliability into Microsemi Designs, the Synplify Way**
Sharath Duraiswam, Senior Corporate Applications Engineer (Synopsys)
- 1.15-1.45 PM **RTG4 Package Technology**
Raymond Kuang, Director of Package Engineering
- 1.45-2.00 PM **Mixed Signal ICs for Space**
Dorian Johnson Product Marketing Manager
- 2.00-2.20 PM **SpaceWire and SpaceFibre**
Prof. Steve Parkes, Managing Director (STAR-Dundee)
- 2.20-2.50 PM **RTG4 Software Features / Board Level Considerations**
Harish Mettu, Field Applications Engineer
- 2.50-3.05 PM **Break**

Microsemi Space Forum

3.05–3.20 PM	Microsemi Precise Satellite Timing Module for Next Generation Satellite Bus <i>Peter Cash, Director of Space, Defense and Advanced Technology</i>
3.20-4.00 PM	Space System Managers – Architecture and Performance <i>Mathieu Sureau, Director IC Engineering</i>
4.00-4.15 PM	Using RTG4 for Motor Control Applications in Space (Demo) <i>Prakash Reddy, Staff Engineer</i>
4.15-4.20 PM	Lucky Draw
4.20 PM	Questions/Answers and Wrap-Up

Timing, Mixed Signal & Power Track

10:15-10.55 AM	Time & Frequency Product Overview & Key Performance Features <i>Peter Cash, Director of Space, Defense and Advanced Technology</i>
10.55-11.25 AM	Next Generation Power Solutions; Solving Real World Interface Issues <i>Kent Brooten, National Sales Manager</i>
11:25-11.55 AM	Improving the Performance of a DC-DC Forward Converter using I²MOS™ MOSFET Technology - <i>Al Ortega, Marketing Manager</i>
11.55-12.55 PM	Lunch
12.55-1.35 PM	Space System Managers – Architecture and Performance <i>Mathieu Sureau, Director IC Engineering</i>
1.35-1.50 PM	Mixed Signal ICs for Space <i>Dorian Johnson Product Marketing Manager</i>
1.50-2.10 PM	Space Power Development – Expanding Heritage with New Technology <i>Kent Brooten, National Sales Manager</i>
2.10-2.35 PM	I²MOS™ Technology Overview - A Truly Rad-Hard MOSFET <i>Brian Wilkinson, Senior Director Technical and Strategic Marketing and Applications</i>
2.35-3.00 PM	TID versus ELDRS on Transistors - The Whole Picture <i>Al Ortega, Marketing Manager</i>
3.00-3.15 PM	Break
3.15-3.45 PM	Microsemi Precise Satellite Timing Module for Next Generation Satellite Bus <i>Peter Cash, Director of Space, Defense and Advanced Technology</i>
3.45-4.15 PM	Switching High Voltages & High Currents in Space <i>Brian Wilkinson, Senior Director Technical and Strategic Marketing and Applications</i> <i>Kent Brooten, National Sales Manager</i>
4.15-4.20 PM	Lucky Draw
4.20 PM	Questions/Answers and Wrap-Up

Notes

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<u>Program at a Glance</u>
8:15 Registration
General Session 9:15 – 10:00 Welcome Microsemi Space Technology Journey
10:00 Break
Spilt into Two Tracks 10:15 Morning Presentations
11:55 Lunch (Hosted by Microsemi)
12:55 Afternoon Presentations
(2:50 – 3:05 Digital Track) & (3:00 – 3:15 Timing, Mixed Signal & Power Track) Breaks
3:05 & 3:15 Afternoon Presentations (continued)
4:20 Questions/Answers and Wrap-up

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