

Join us at Space Forum Europe 2013

November 19, 2013 • The Palace Hotel • Noordwijk, Netherlands



You are cordially invited to attend the Microsemi Space Forum. In this one-day technology conference, Microsemi semiconductor experts in space-related products will present information on the company's latest and most innovative space solutions, technology roadmaps, advanced packaging techniques, solutions to combat the effects of radiation, and more. In addition, applications engineers will discuss system-level challenges and potential solutions.

Due to the breadth and depth of Microsemi's space products portfolio, there will be two concurrent session tracks following the general session. One track will focus on power and mixed-signal and the other track will cover digital solutions.

Please note your preferred track when you register online. The conference is on November 19 at the Palace Hotel in Noordwijk, Netherlands.

Who Should Attend:

The Microsemi Space Forum is an ideal technology event for component engineers, design engineers, system-level architects, R&D engineers and other space industry professionals interested in the latest space-related solutions, system performance, reliability, packaging, radiation, and new digital and analog components for space applications.

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Space Forum Europe 2013

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Track 1: Digital

07:45 - 08:45	Registration
09:00 - 09:15	Kick off – Welcome
09:15 - 9:30	Key Note Address
09:30 - 10:00	Microsemi – Your Partner for Space System Solutions
10:00 - 10:15	Tea / Coffee break
10:15 - 11:00	FPGA Products Update
11:00 - 11:30	Prototyping Solutions for Microsemi Radiation Tolerant FPGAs
11:30 - 12:15	Technology Roadmap for Digital Space Flight Products
12:15 - 12:45	Fault Tolerant LEON3 Processor & SpaceWire Router Standard Products
12:45 - 13:45	Lunch
13:45 - 14:15	Integration of Common Spacecraft Interfaces & Support Functions
14:15 - 14:45	Next Generation Packaging Technology for Space Applications
14:45 - 15:15	Distribution Issues between Central Power and POL Architectures
15:15 - 15:45	Radiation Characterization & Mitigation Techniques
15:45 - 16:00	Tea/Coffee Break
16:00 - 16:30	Programming Software Qualification for Space FPGAs
16:30 - 17:00	Design Techniques for Implementing High-Reliable Designs using Microsemi Space FPGAs
17:00 - 17:30	Implementing Designs into Today's Leading Edge Space Qualified FPGAs
17:30 - 17:45	Q&A, Wrap-Up

Track 2: Power & Mixed Signal

07:45 - 08:45	Registration
09:00 - 09:15	Kick Off – Welcome
09:15 - 9:30	Key Note Address
09:30 - 10:00	Microsemi – Your Partner for Space System Solutions
10:00 - 10:15	Tea / Coffee break
10:15 - 10:45	Microsemi Leadership in Discrete Products for Space Applications
10:45 - 11:30	Radiation Hardened Discrete Products & Hybrids Roadmap
11:30 - 12:00	Analog Mixed Signal Space Product Update
12:00 - 12:45	Microsemi Standard Isolated DC/DC Portfolio & Roadmap
12:45 - 13:45	Lunch
13:45 - 14:15	Distribution Issues between Central Power and POL Architectures
14:15 - 14:45	SSPA Solutions and Space Relays
14:45 - 15:45	Integration of Common Spacecraft Interfaces & Support Functions
15:45 - 16:00	Tea / Coffee Break
16:00 - 16:45	Gallium Nitride Applications & Reference Designs
16:45 - 17:30	RF & Microwave Capability & Roadmap (GaAs and GaN & MMICs)
17:30 - 17:45	Q&A, Wrap-Up

This event is by invitation only.

Space is limited, so please register now at:

Space Forum Europe: www.microsemi.com/form/31-microsemi-space-forum-europe-2013

For questions, please contact spaceforum@microsemi.com.

Propelling Space Innovation for More Than a Half Century



Launchers



Satellites



Landers