

AXSK

REV2

ACTEL AXCELERATOR STARTER KIT BOARD

BOARD NAME :	AXCELERATOR STARTER KIT BOARD
BOARD PART NUMBER :	AX-EVAL-BRD250
BOARD REVISION :	2



MemecBoard
Calgary, Alberta
Canada

BOARD TITLE

ACTEL AXCELERATOR START KIT BOARD

PAGE TITLE

TITLE PAGE

LAST MODIFIED

Tuesday, September 28, 2004

SIZE

C

DESIGNER

JMK

REVISION

2

SHEET

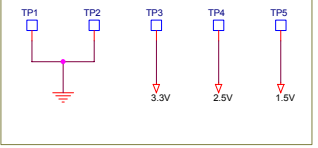
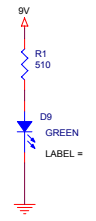
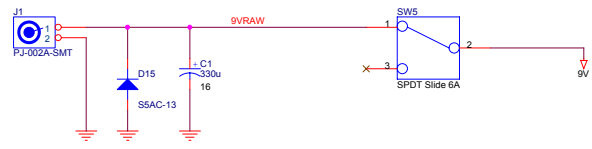
1 of 7

VOLTAGE REGULATION

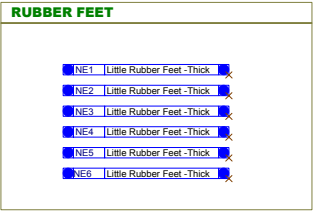
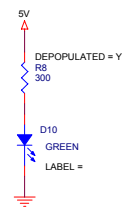
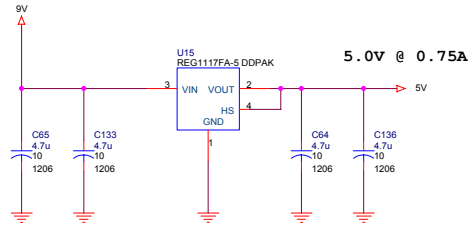
LEDs

TEST POINTS

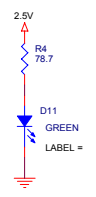
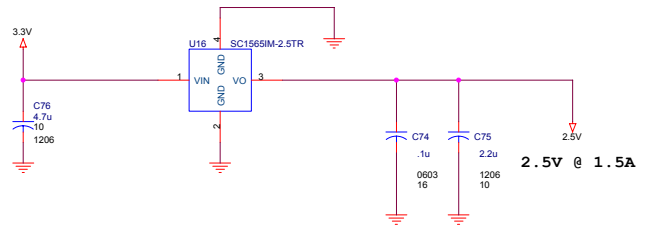
9V



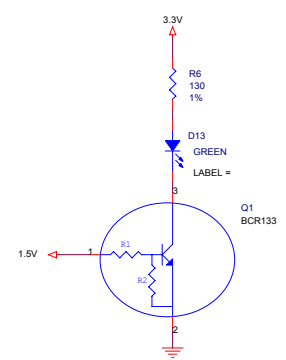
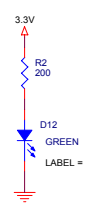
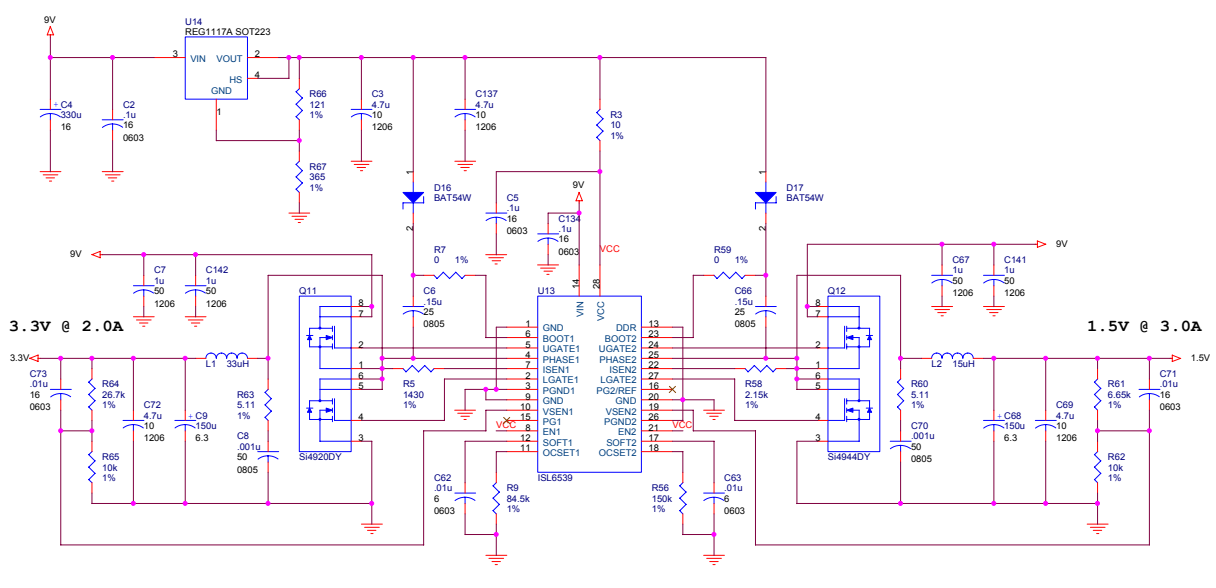
5V

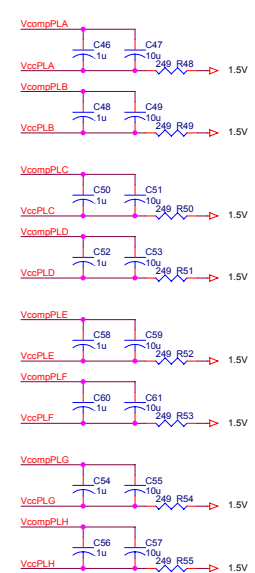
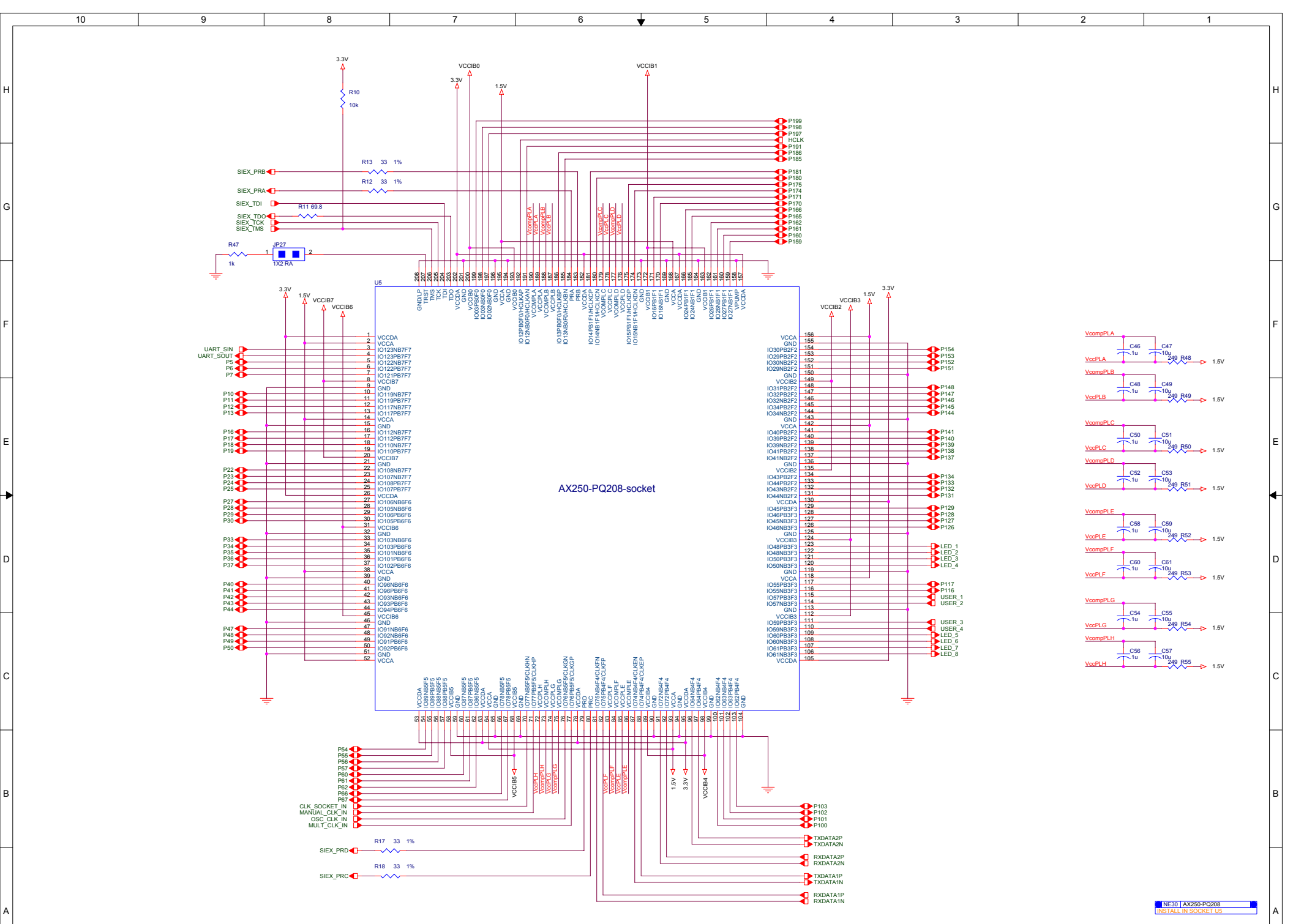


2.5V



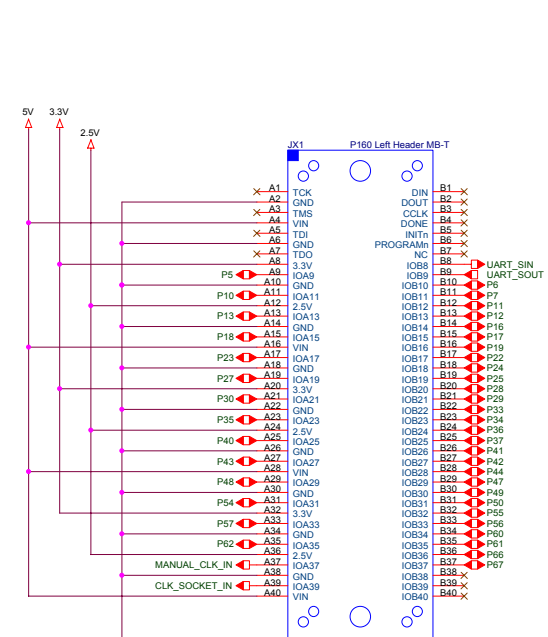
3.3V & 1.5V



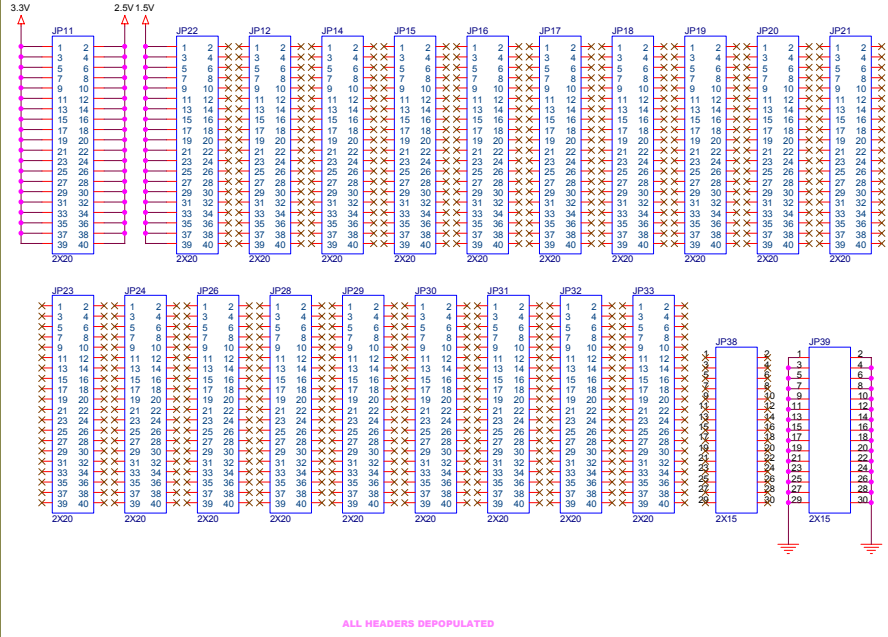


NE30 | AX250-PQ208
INSTALL IN SOCKET U5

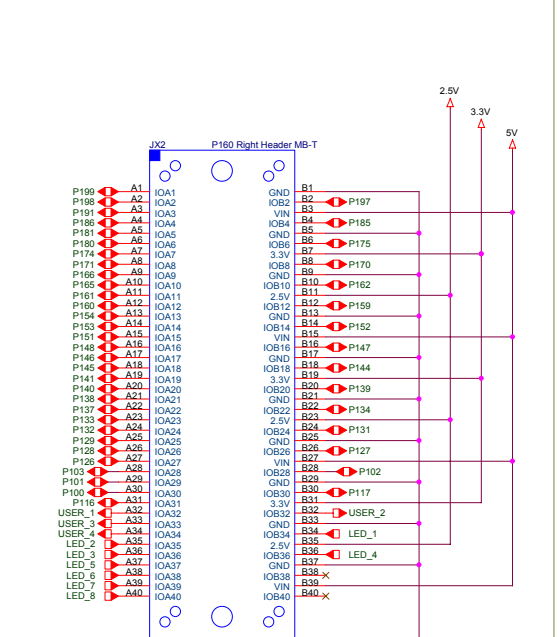
P160 LEFT HEADER



PROTOTYPE AREA

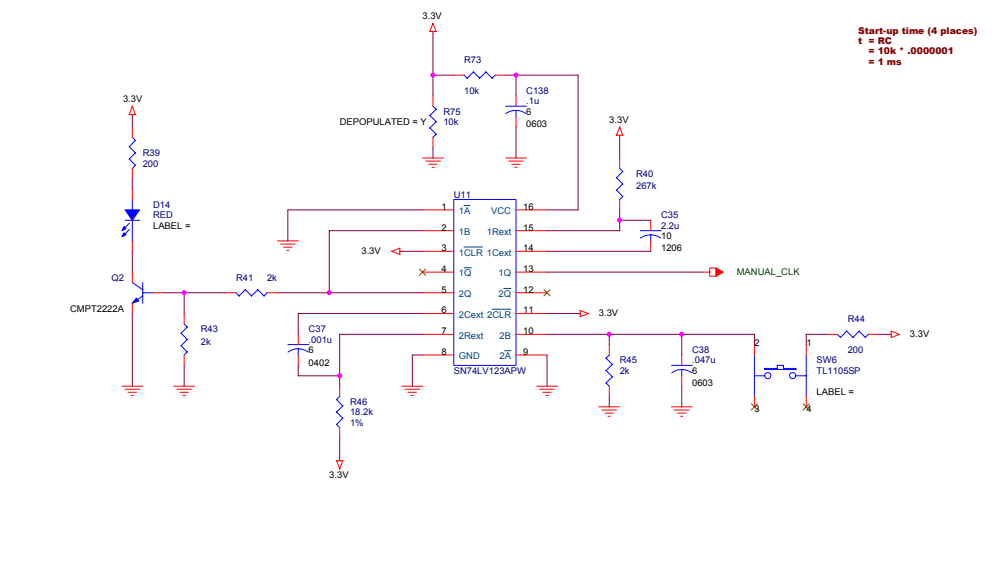


P160 RIGHT HEADER

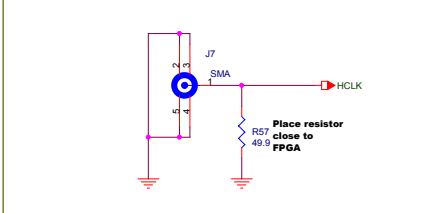


Small 2" x 4" (or similar) prototyping area with plated through holes

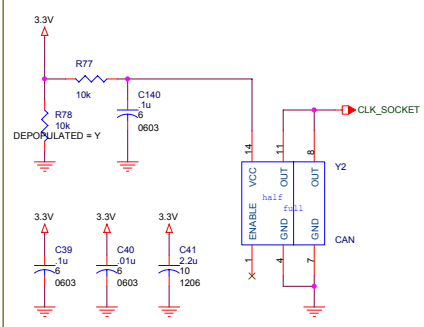
MANUAL CLOCK



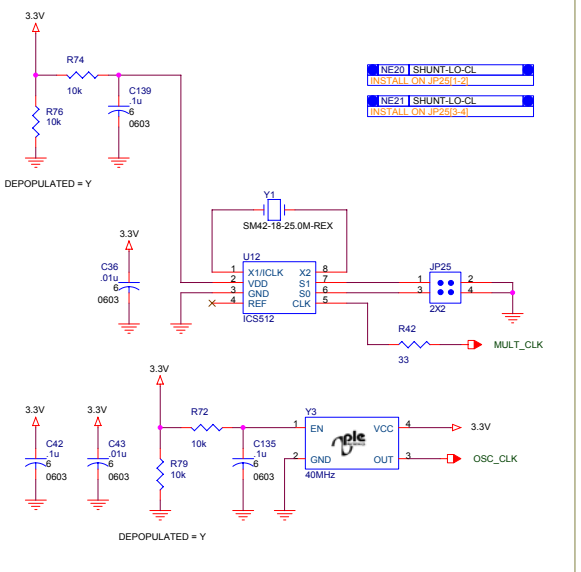
EXTERNAL CLOCK



CLOCK SOCKET

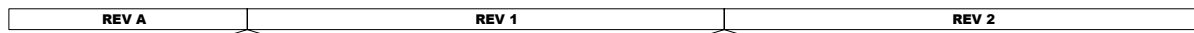


ON-BOARD CLOCKS



H

H



CHANGED R44, R23, R28, R32, R36 TO 200 ohms	DEPOPULATED C31, C32, C33 and C34
CHANGED R45, R25, R29, R33, R38 TO 2k ohms	CORRECTED U14 PART NUMBER (3.3V TO ADJ)
ADDED FOUR 47uF CAPACITORS TO P160	CHANGED PACKAGE OF U11 FROM SOIC TO TSSOP
CHANGED JX CONNECTORS TO TALLER VERSION	
UN-DEPOPULATED J3, J4, J5, J6	
CHANGED SOCKET PART NUMBER	
SWAPPED SOCKET AND FPGA DESIGNATORS	
MOVED LED PIN LOCATIONS ON FPGA	
REPLACED TWO REGULATORS WITH TI	
CORRECTED CLOCK PART NUMBER	
ADDED ALTERNATE PART NUMBERS	

G

G

F

F

E

E

D

D

C

C

B

B

A

A