CoreSMIP v2.0 Release Notes

These release notes accompany the production release of CoreSMIP v2.0. This document provides details about the features, enhancements, system requirements, supported families, implementations, and known issues and workarounds.

Features

CoreSMIP (SMIP = Security Monitor Intellectual Property) can be used to enhance the security of a design implemented in a SmartFusion2 or IGLOO2 device. CoreSMIP raises an alarm flag on tamper detection and can initiate erasure (or zeroization) of the device.

Delivery Types

CoreSMIP is delivered in the form of a netlist. Verilog and VHDL netlists are included. A license is required to use the core.

Supported Families

- SmartFusion2
- IGLOO2

Supported Tool Flows

Use Libero v11.4.1 software or later with CoreSMIP v2.0 release.

Installation Instructions

CoreSMIP is available through the Libero SoC IP Catalog. The core can be downloaded from a remote web-based repository to the local vault. Once installed in Libero SoC, the core can be instantiated, configured, connected, and generated using the SmartDesign tool.

Known Issues and Workarounds

There are no known issues in this release.

Release History

Table 1 provides the release history of CoreSMIP.

Table 1. Release History of CoreSMIP

Version	Date	Changes
2.0	August 2014	First production release.

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