MSCSM70TAM05TPAG Datasheet Triple Phase Leg SiC MOSFET Power Module

April 2020





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1 Revision History

The revision history describes the changes that were implemented in the document. The changes are listed by revision, starting with the most current publication.

1.1 Revision 1.0

Revision 1.0 was published in April 2020. It is the first publication of this document.



2 Product Overview

The MSCSM70TAM05TPAG device is a triple phase leg 700 V/349 A full silicon carbide (SiC) power module.

Figure 1 • MSCSM70TAM05TPAG Electric Schematic

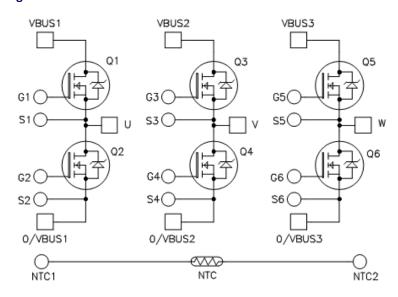
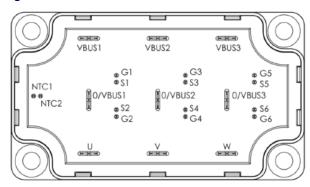


Figure 2 • MSCSM70TAM05TPAG Pinout Location



All ratings at $T_J = 25$ °C, unless otherwise specified.

Caution: These devices are sensitive to electrostatic discharge. Proper handling procedures should be followed.



2.1 Features

The following are key features of the MSCSM70TAM05TPAG device:

- SiC Power MOSFET
 - Low R_{DS(on)}
 - High-speed switching
 - Ultra low loss
- · Very low stray inductance
- · Kelvin source for easy drive
- Internal thermistor for temperature monitoring
- Aluminum nitride (AIN) substrate for improved thermal performance

2.2 Benefits

The following are benefits of the MSCSM70TAM05TPAG device:

- High-efficiency converter
- Outstanding performance at high-frequency operation
- Direct mounting to heatsink (isolated package)
- Low junction-to-case thermal resistance
- Solderable terminals for power and signal, for easy PCB mounting
- Low profile
- · RoHS compliant

2.3 Applications

The MSCSM70TAM05TPAG device is designed for the following applications:

- Uninterruptible power supplies
- Switched mode power supplies
- · EV motor and traction drive
- · Welding converters



3 Electrical Specifications

This section shows the electrical specifications of the MSCSM70TAM05TPAG device.

3.1 SiC MOSFET Characteristics (Per MOSFET)

The following table shows the absolute maximum ratings per SiC MOSFET of the MSCSM70TAM05TPAG device.

Table 1 • Absolute Maximum Ratings

Symbol	Parameter	Max Ratings	Unit	
V _{DSS}	Drain-source voltage			V
I _D	Continuous drain current	349 ¹	Α	
		T _C = 80 °C	278 ¹	
I _{DM}	Pulsed drain current		700	
V _{GS}	Gate-source voltage		-10/25	V
R _{DSon}	Drain-source ON resistance	6.4	mΩ	
P _D	Power dissipation	T _C = 25 °C	966	w

Note:

1. Specification of the SiC MOSFET device, but output current must be limited due to size of power connectors.

The following table shows the electrical characteristics per SiC MOSFET of the MSCSM70TAM05TPAG device.

Table 2 • Electrical Characteristics

Symbol	Characteristic	Test Conditions		Min	Тур	Max	Unit
I _{DSS}	Zero gate voltage drain current	V _{GS} = 0 V; V _{DS} = 700 V				300	μΑ
R _{DS(on)}	Drain-source on resistance	I _D = 120 A	T _J = 25 °C		5	6.4	mΩ
			T _J = 175 °C		6.3		
V _{GS(th)}	Gate threshold voltage	$V_{GS} = V_{DS}$, $I_D = 12 \text{ mA}$		1.9	2.4		V
I _{GSS}	Gate-source leakage current	V _{GS} = 20 V, V _{DS} = 0 V				300	nA



The following table shows the dynamic characteristics per SiC MOSFET of the MSCSM70TAM05TPAG device.

Table 3 • Dynamic Characteristics

Symbol	Characteristic	Test Conditions		Min	Тур	Max	Unit		
C _{iss}	Input capacitance	V _{GS} = 0 V V _{DS} = 700 V f = 1 MHz			13.5		nF		
C _{oss}	Output capacitance					1			
C _{rss}	Reverse transfer capacitance				0.09				
Qg	Total gate charge	V _{GS} = -5 V/20 V			645		nC		
Q_{gs}	Gate-source charge	V _{Bus} = 470 V I _D = 120 A			174				
Q_{gd}	Gate-drain charge				105				
T _{d(on)}	Turn-on delay time	V _{GS} = -5 V/20 V			40		ns		
T _r	Rise time	V _{Bus} = 400 V I _D = 240 A; T _J = 150 °C			35				
T _{d(off)}	Turn-off delay time	$R_{Gon} = 9 \Omega$; $R_{Goff} = 1.6 \Omega$			50				
T _f	Fall time				20				
E _{on}	Turn on energy	Inductive switching	T _J = 150 °C		1.96		mJ		
E _{off}	Turn off energy	$V_{GS} = -5 \text{ V}/20 \text{ V}$ $V_{Bus} = 400 \text{ V}$ $I_D = 160 \text{ A}$ $R_{Gon} = 9 \Omega$ $R_{Goff} = 1.6 \Omega$			0.56		mJ		
R _{Gint}	Internal gate resistance				1.9		Ω		
R _{thJC}	Junction-to-case thermal resistance					0.155	°C/W		

The following table shows the body diode ratings and characteristics per SiC MOSFET of the MSCSM70TAM05TPAG device.

Table 4 • Body Diode Ratings and Characteristics

Symbol	Characteristic	Test Conditions	Min	Тур	Max	Unit
V _{SD}	Diode forward voltage	V _{GS} = 0 V; I _{SD} = 120 A		3.4		V
		V _{GS} = -5V ; I _{SD} = 120 A		3.8		
t _{rr}	Reverse recovery time	I_{SD} = 120 A; V_{GS} = -5 V V_{R} = 400 V; d_{iF}/dt = 3000 A/ μ s		38		ns
Q _{rr}	Reverse recovery charge			954		nC
I _{rr}	Reverse recovery current			44		A



3.2 Thermal and Package Characteristics

The following table shows the package characteristics of the MSCSM70TAM05TPAG device.

Table 5 • Thermal and Package Characteristics

Symbol	Characteristic	Characteristic				Unit
V _{ISOL}	RMS isolation voltage, any terminal to case t = 1 min, 50 Hz/60 Hz			4000		V
T _J	Operating junction temperature range			-40	175	°C
T _{JOP}	Recommended junction temperature under switching conditions			-40	T _{Jmax} –25	
T _{STG}	Storage temperature range			-40	125	
T _C	Operating case temperature			-40	125	
Torque	Mounting torque	3	5	N.m		
Wt	Package weight	Package weight			250	g

The following table shows the temperature sensor NTC (see application note *APT0406* on www.microsemi.com) of the MSCSM70TAM05TPAG device.

Table 6 • Temperature Sensor NTC

Symbol	Characteristic I		Min	Тур	Max	Unit
R ₂₅	Resistance at 25 °C			50		kΩ
$\Delta R_{25}/R_{25}$				5		%
B _{25/85}	T ₂₅ = 298.15 K			3952		K
ΔΒ/Β		T _C = 100 °C		4		%

$$R_{T} = \frac{R_{25}}{\exp \left[B_{25/85} \left(\frac{1}{T_{25}} - \frac{1}{T} \right) \right]} \quad \text{T: Thermistor temperature } \\ R_{T}: \text{ Thermistor value at T}$$



3.3 Typical SiC MOSFET Performance Curves

This sections shows the typical SiC MOSFET performance curves of the MSCSM70TAM05TPAG device.

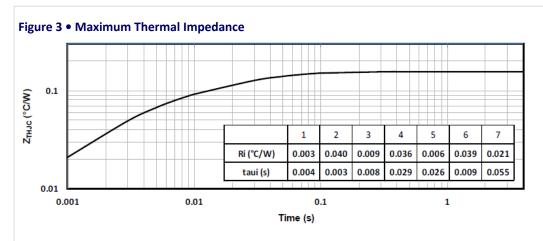


Figure 4 • Output Characteristics, T_J = 25 °C

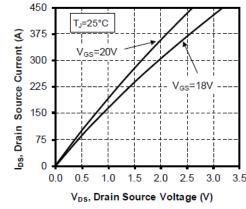


Figure 5 • Output Characteristics, T_J = 175 °C

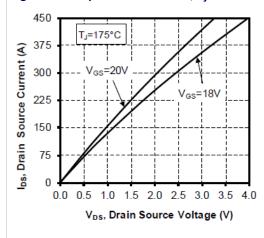


Figure 6 ● Normalized R_{DS(on)} vs. Temperature

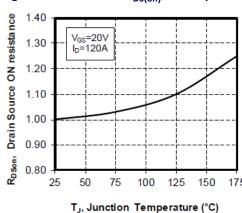


Figure 7 • Transfer Characteristics

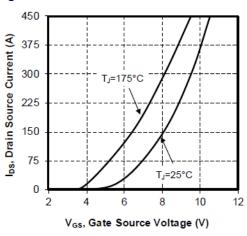




Figure 8 • Capacitance vs. Drain Source Voltage

100000
Ciss
1000
Coss
100
V_{DS}, Drain Source Voltage (V)

Figure 9 • Gate Charge vs. Gate Source Voltage Gate to Source Voltage (V) T_J = 25°C I_D = 120A 15 $V_{DS} = 470V$ 10 5 0 V_{GS}, -5 0 150 300 450 600 750 Gate Charge (nC)

Figure 10 • Body Diode Characteristics, T_J = 25 °C

-5.0 4.5 4.0 -3.5 -3.0 -2.5 -2.0 -1.5

(Y)

V_{GS}=-5V

-75

-150

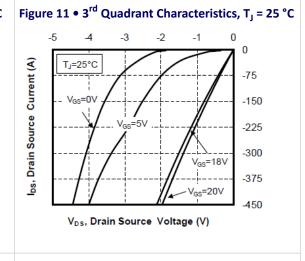
-225

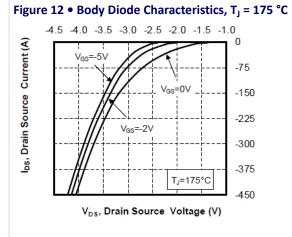
-300

-375

-450

V_{DS}, Drain Source Voltage (V)





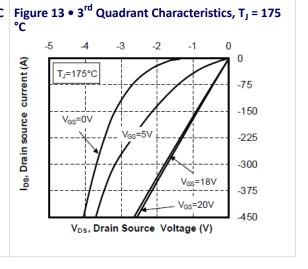




Figure 14 • Switching Energy vs. Current 2.5 /_{GS}=-5/20V Eon R_{GON}=9Ω 2.0 $R_{GOFF}=1.6\Omega$ V_{BUS}= 400V T_J = 150°C Losses (mJ) 1.5 1.0 Eoff 0.5 0.0 100 200

150

Current (A)

250

300

50

0

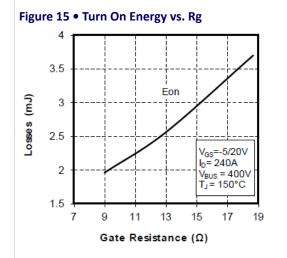
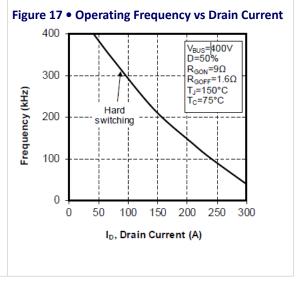


Figure 16 • Turn Off Energy vs. Rg 2.0 1.5 Eoff Losses (µJ) 1.0 V_{GS}=-5/20V 0.5 I_D= 240A V_{BUS} = 400V T_J = 150°C 0.0 2.0 2.5 1.5 3.0 3.5 Gate Resistance (Ω)





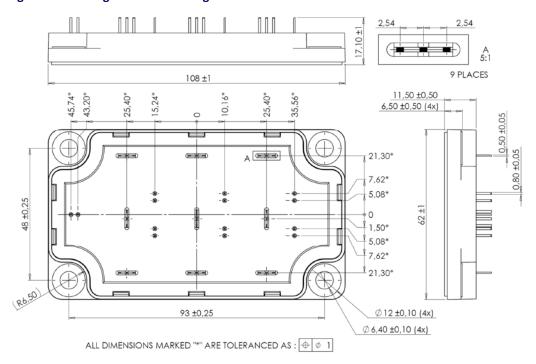
4 Package Specifications

This section shows the package specification of the MSCSM70TAM05TPAG device.

4.1 Package Outline Drawing

This section shows the package outline drawing of the MSCSM70TAM05TPAG device. The dimensions in the following figure are in millimeters.

Figure 18 • Package Outline Drawing



Note: See application note 1902 - Mounting Instructions for SP6-P (12 mm) Power Modules on www.microsemi.com.





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