HELLO FPGA REV 1

01 TITLE_BLOCK

02 DDR3 CONNECTION

03 SPI & MISC CONNECTION

04 PIC32MX775F256L CONNECTION

05 CLOCK & GND CONNECTION

06 POWER DECOUPLING CONNECTION

07 REGULATOR CONNECTION

08 USB TO UART CONNECTION

09 EXPANSION CONNECTOR

Drawn By: ProjectDrawnBy		MICROCHIP
Engineer: ProjectEngineer		
PartNumber: ProjectPartNumber	Project Title: *ProjectTitle*	
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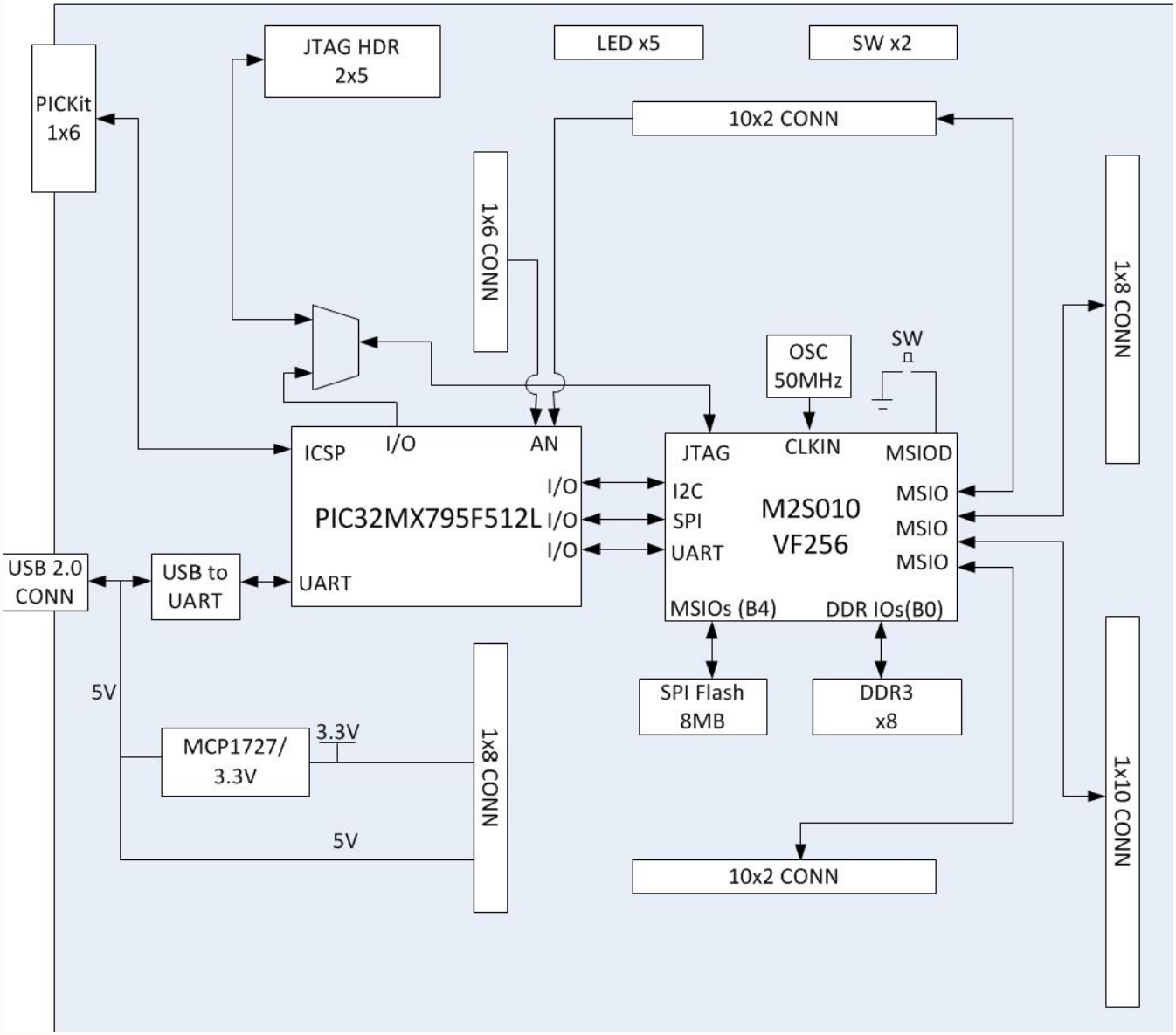
Size: A	Sch #:03-ProjectBoardNumber Revision:ProjectRevision$SH	Date:02-07-2019 15:19:36 Sheet 1 of 11
File: 01_TITLE_BLOCK_A.SchDoc		Altium.com

HELLO FPGA Rev 1



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Engineer: ProjectEngineer	
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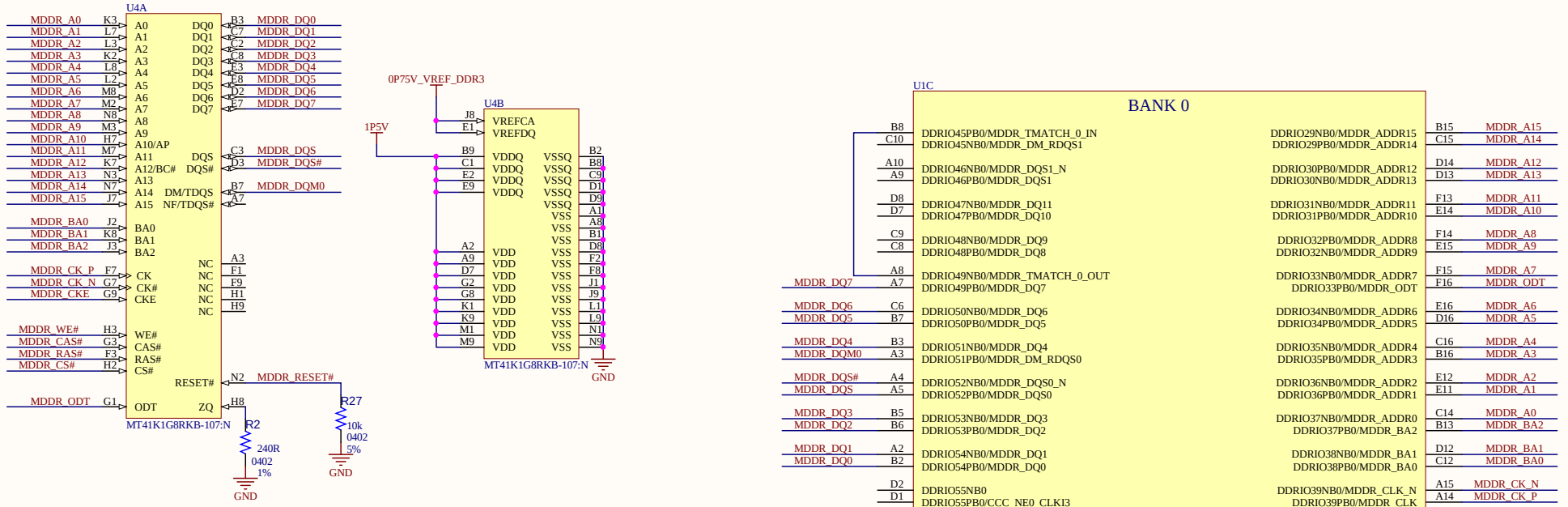
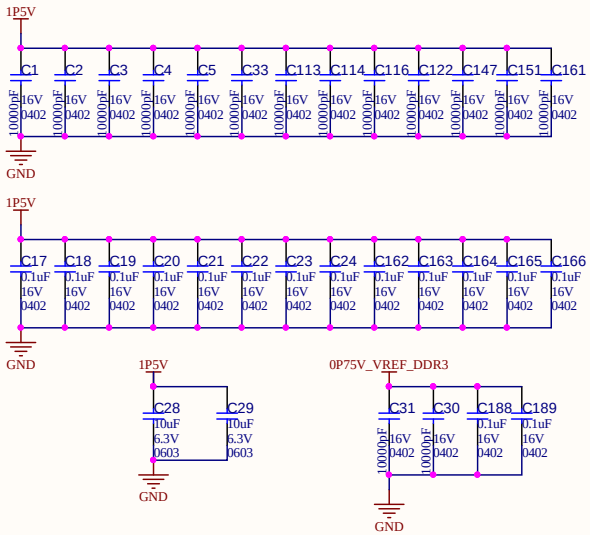
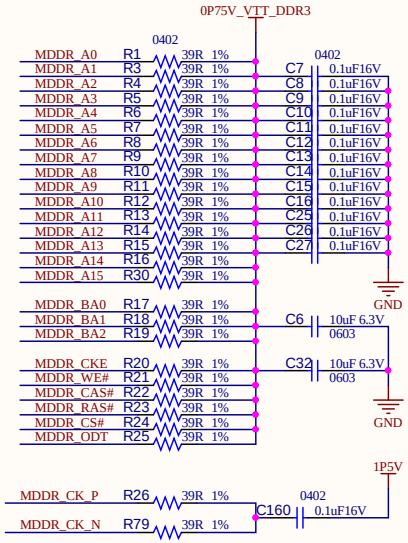
DDR3 CONNECTION







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Engineer: ProjectEngineer	
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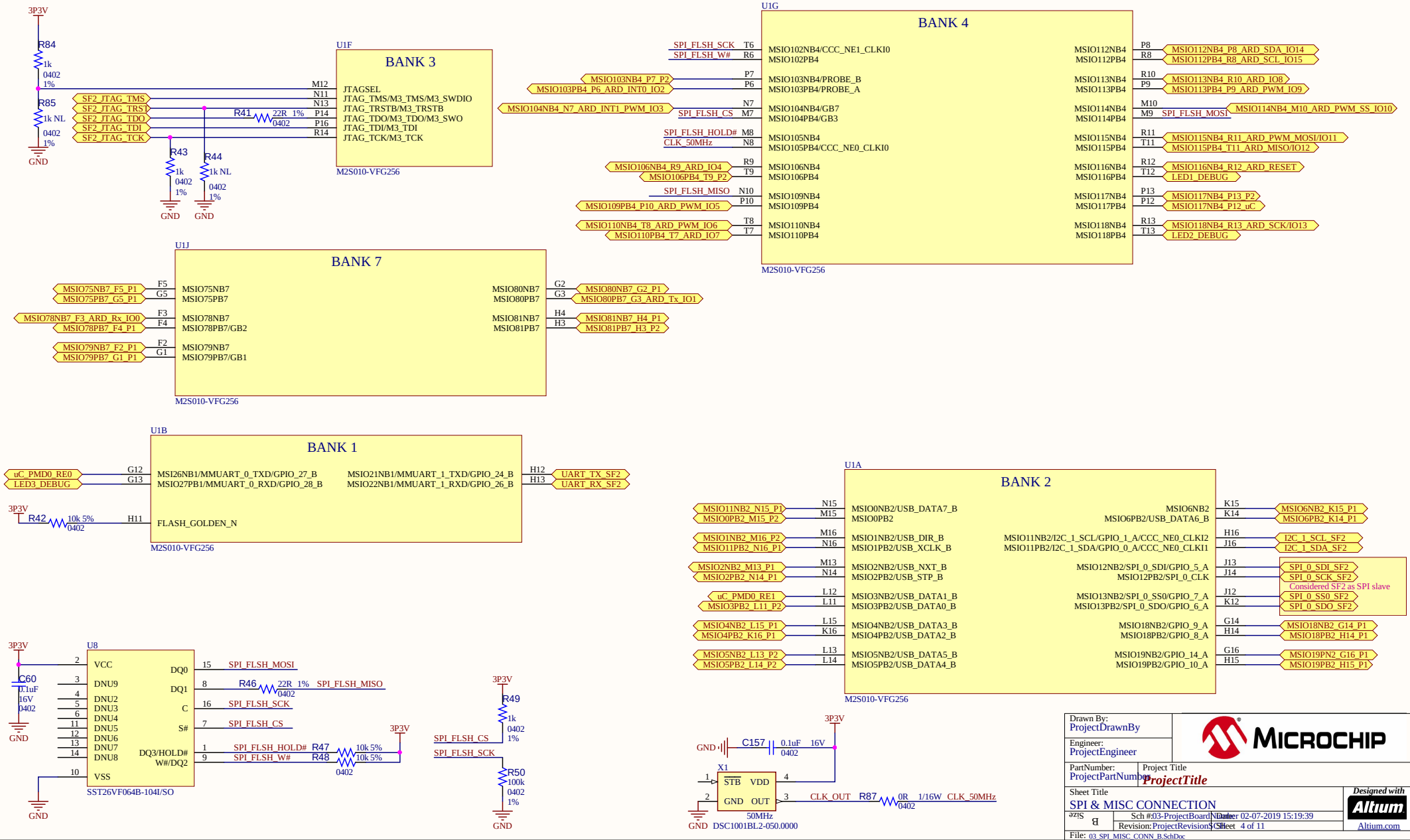
SPI & MISC CONNECTION



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Engineer: ProjectEngineer	
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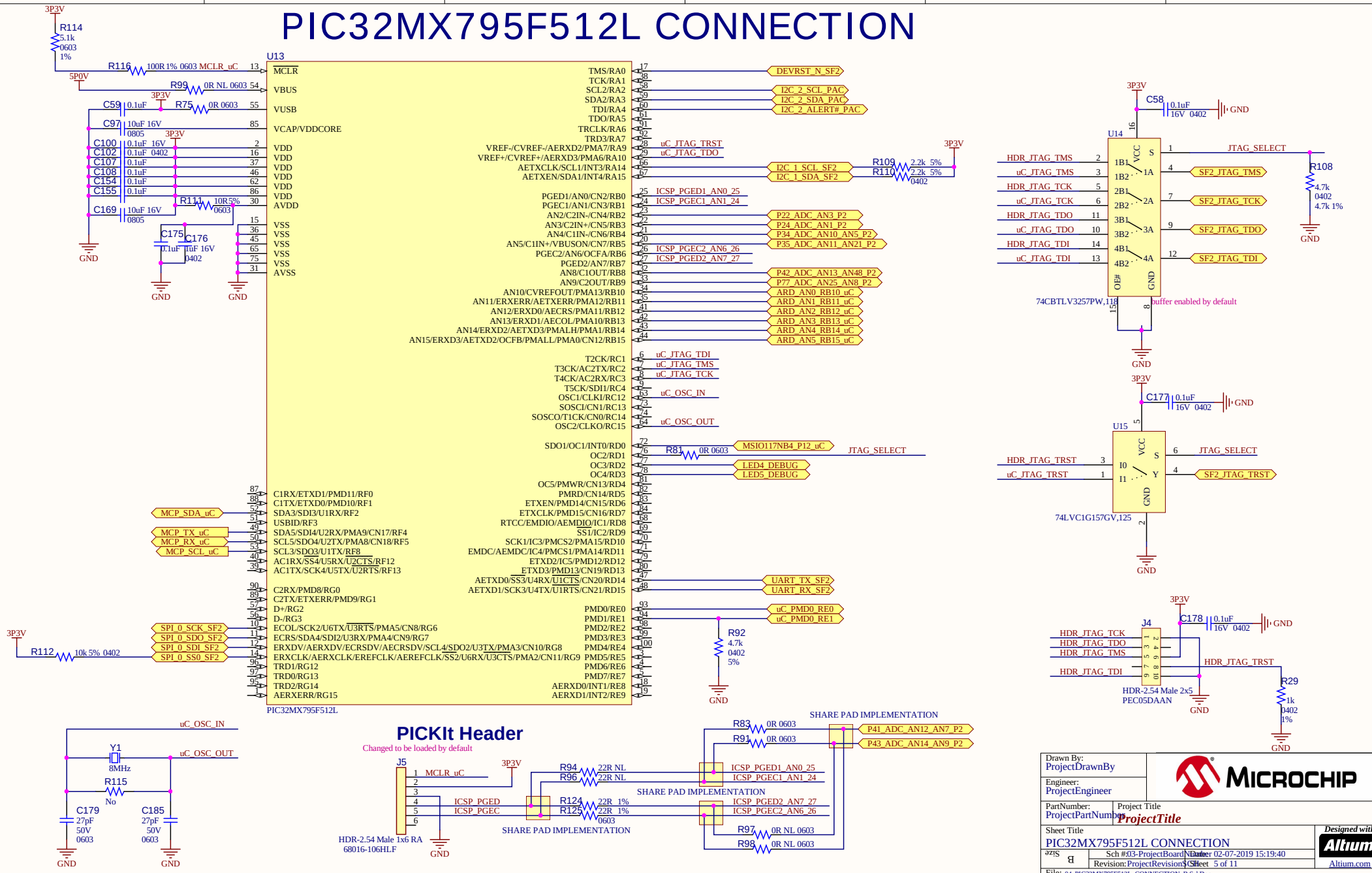
PIC32MX795F512L CONNECTION



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Engineer:	ProjectEngineer
PartNumber:	ProjectPartNumber
Project Title	*ProjectTitle*
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Sch #03-ProjectBoardNDate	02-07-2019 15:19:40
Revision: ProjectRevision	Sheet 5 of 11
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MICROCHIP

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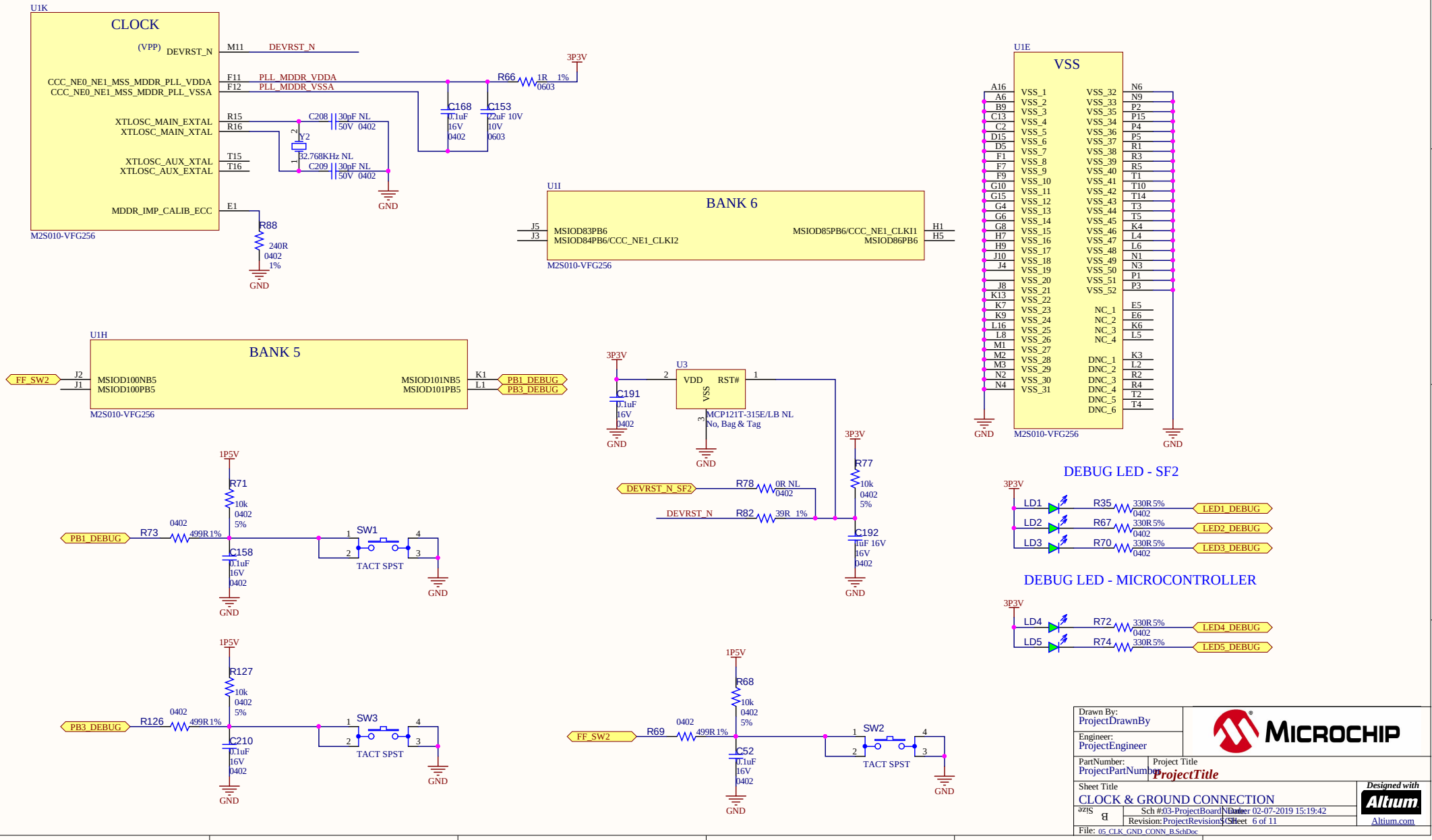
CLOCK & GROUND CONNECTION



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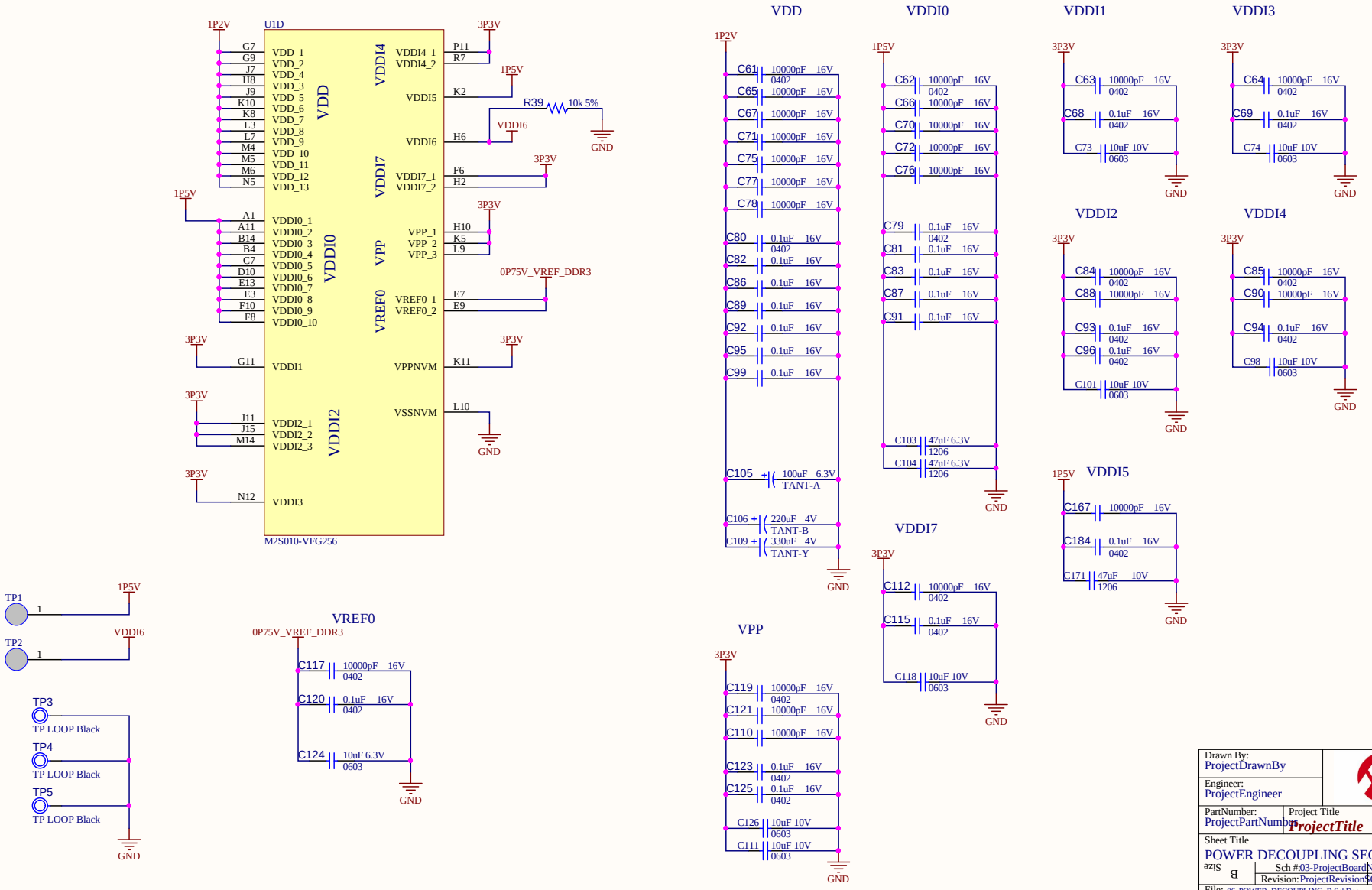
POWER DECOUPLING CONNECTION



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Engineer: ProjectEngineer	
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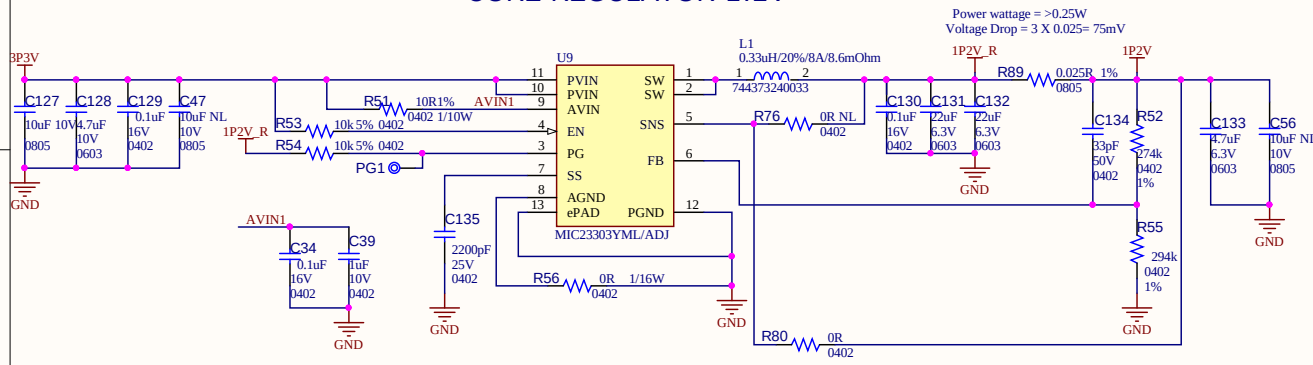
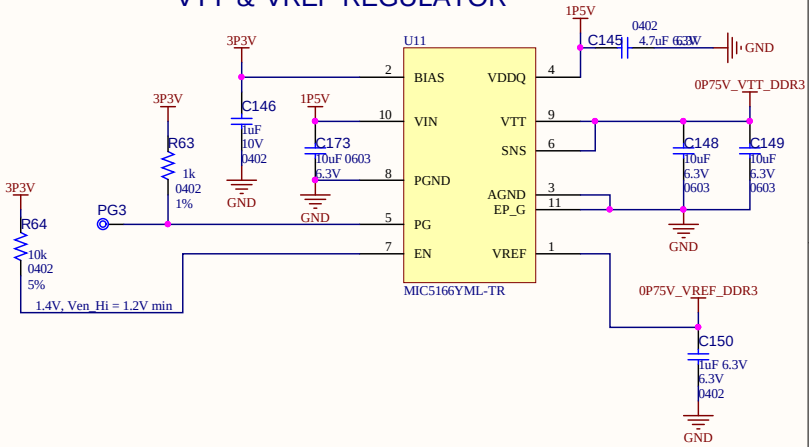
REGULATOR CONNECTION

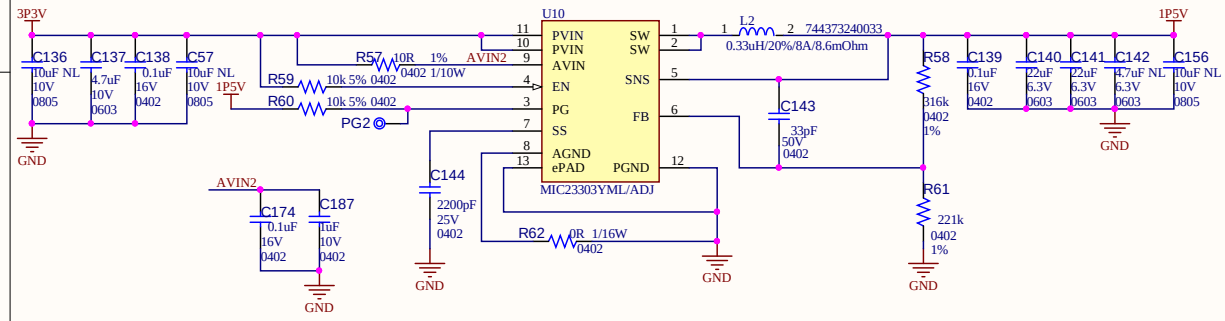
CORE REGULATOR 1.2V

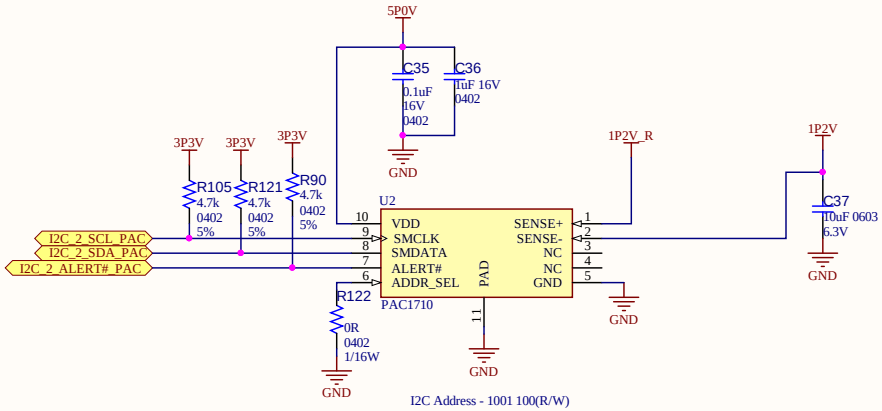


VTT & VREF REGULATOR

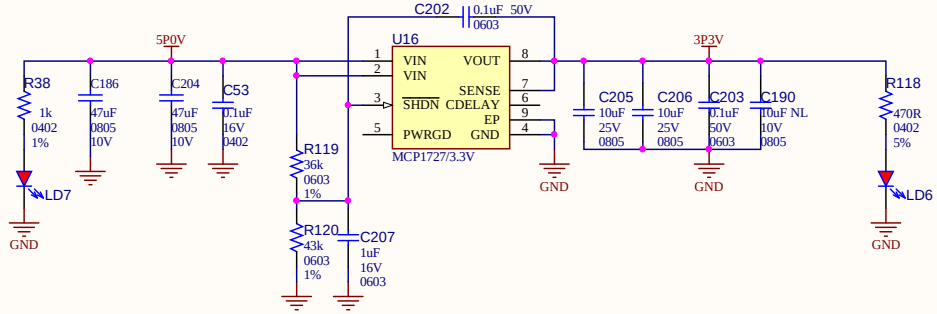


DDR3 SUPPLY REGULATOR - 1.5V





3.3V SUPPLY REGULATOR



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Engineer: ProjectEngineer	
PartNumber: ProjectPartNumber	Project Title: *ProjectTitle*
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Size: B	Sch #03-ProjectBoardNumber Date: 02-07-2019 15:19:44
	Revision: ProjectRevision Sheet 8 of 11
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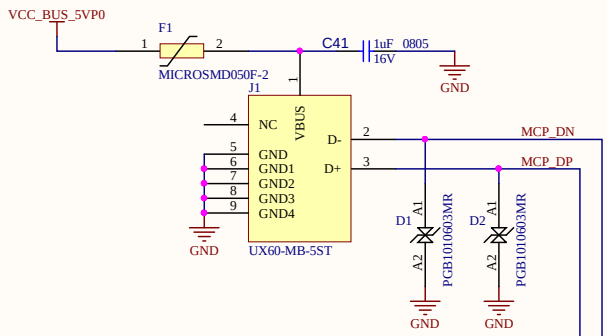
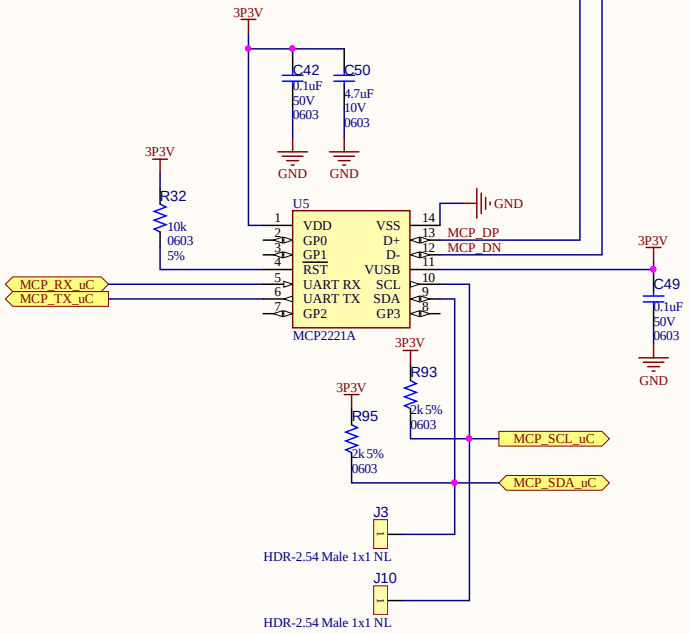
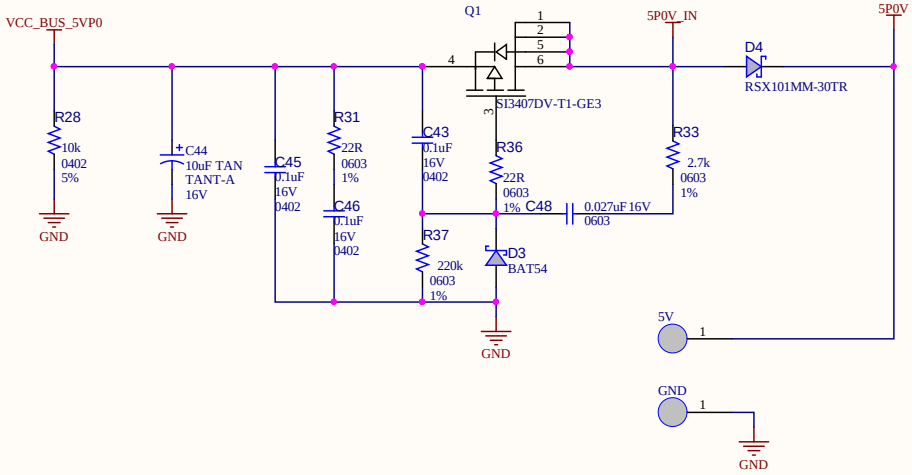
USB TO UART CONNECTION



USB to UART / I²C SERIAL (SF2)





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Engineer: ProjectEngineer	
PartNumber: ProjectPartNumber	Project Title: *ProjectTitle*
Sheet Title: USB TO UART CONNECTION	Designed with Altium
Size: B	Sch #03-ProjectBoardNumber Date: 02-07-2019 15:19:44
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EXPANSION CONNECTOR

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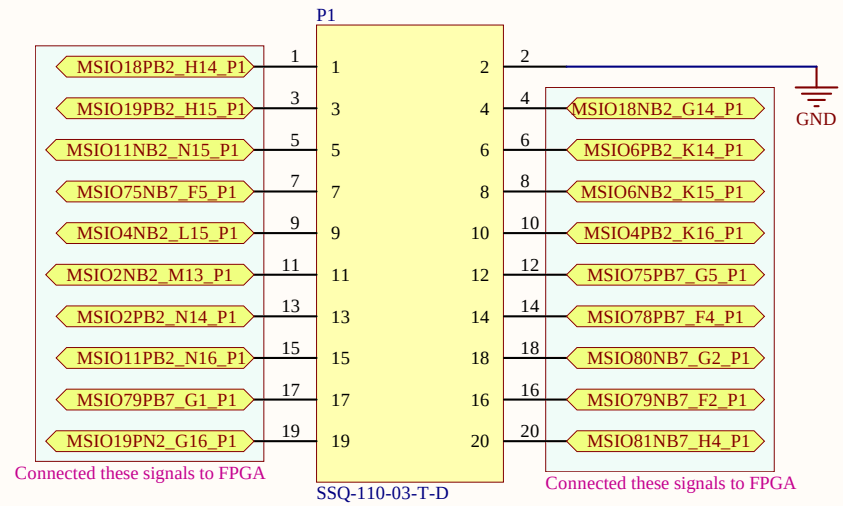

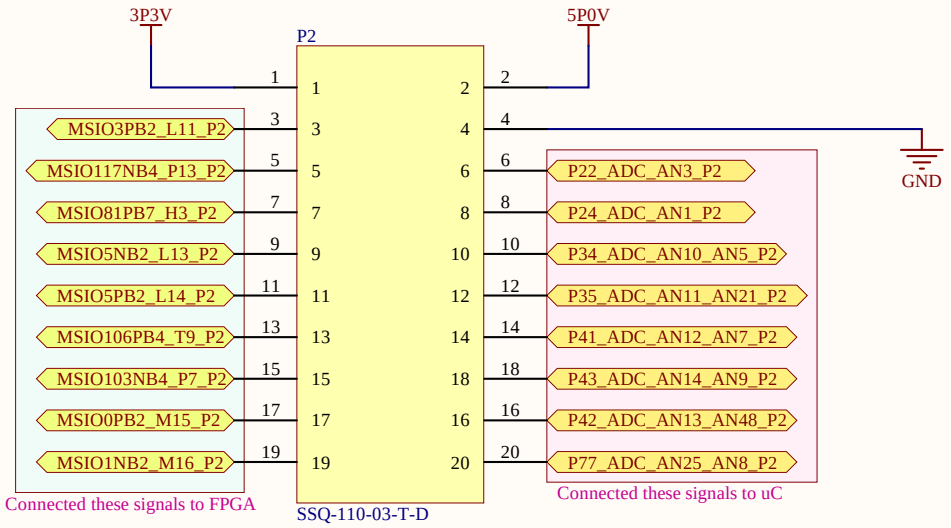

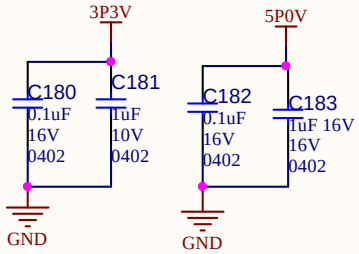

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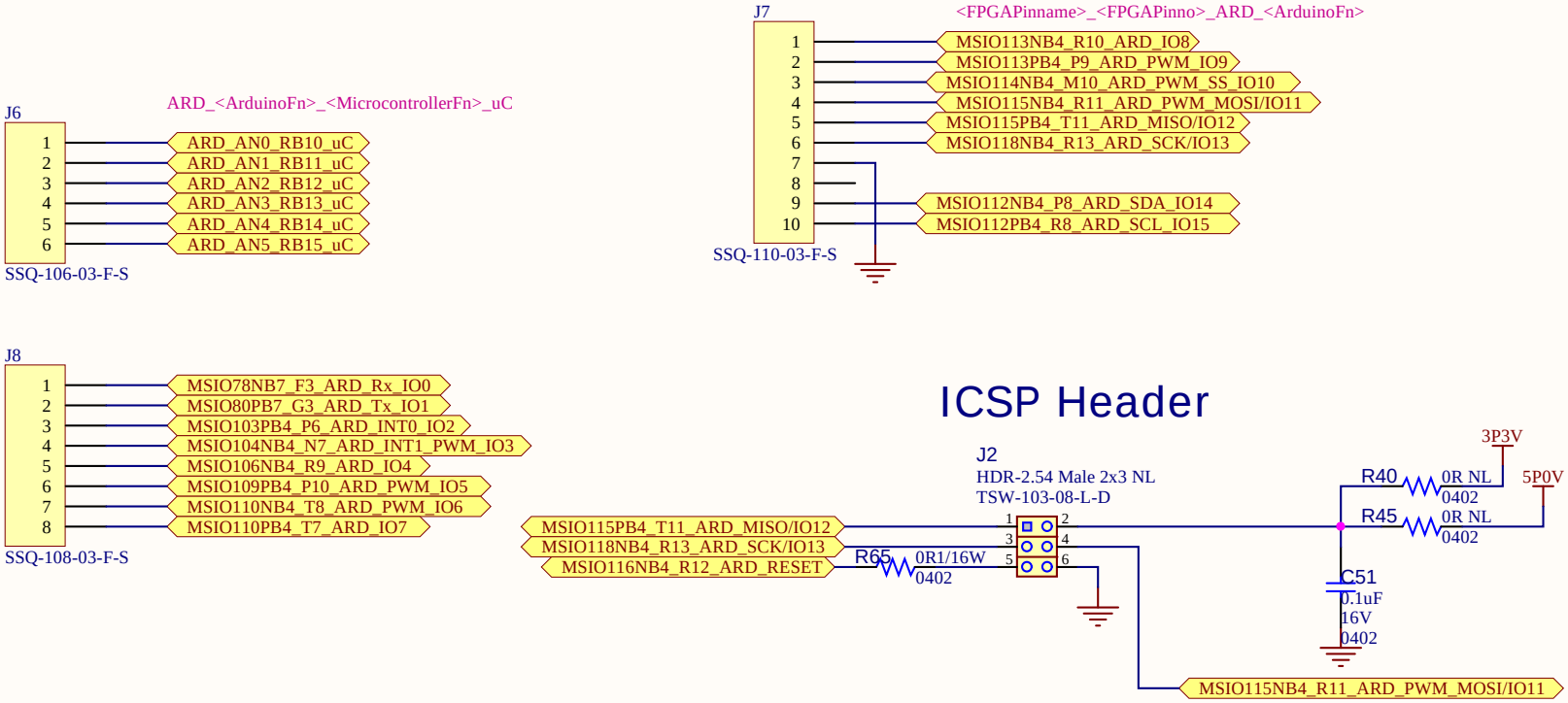
AURDINO INTERFACE CONNECTION



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Engineer: ProjectEngineer	
PartNumber: ProjectPartNumber	Project Title: *ProjectTitle*
Sheet Title: SheetTitle	DVP-100-000518-001
Size: A	Sch #:03-ProjectBoardNumber Date:02-07-2019 15:19:45
	Revision:ProjectRevision$SH Sheet 11 of 11
File: 11_AURDINO_INTERFACE.SchDoc	Designed with Altium — Altium.com