

# COMPUTER DIODE

General Purpose  
Switching

1N914; JAN, JANTX 1N914  
1N4148; JAN, JANTX, JANTXV 1N4148  
JAN, JANTX, JANTXV 1N4148-1  
1N4531; JAN, JANTX, JANTXV 1N4531

## FEATURES

- Metallurgical Bond
- Qualified to MIL-S-19500/116
- Planar Passivated Chip
- DO-34 or DO-35 Package
- Non-JAN Available

## DESCRIPTION

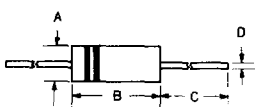
This series is very popular for general purpose switching applications in electronic equipment.

## ABSOLUTE MAXIMUM RATINGS, AT 25°C

Reverse Breakdown Voltage .....	100V
Peak Working Voltage .....	75V
Average Output Current, 1N914 .....	.75mAdc
1N4148 .....	200mAdc
1N4148-1 .....	200mAdc
1N4531 .....	125mAdc
Surge Current, 8.3ms .....	500mA
Operating Temperature Range .....	-65°C to +175°C
Storage Temperature Range .....	-65°C to +200°C

## MECHANICAL SPECIFICATIONS

J, JTX 1N914  
J, JTX, JTXV 1N4148  
J, JTX, JTXV 1N4148-1  
J, JTX, JTXV 1N4531




J, JTX & JTXV 1N4531		J, JTX 1N914	
INCHES	MILLIMETERS	INCHES	MILLIMETERS
A .050-.065	1.27-1.65	A .058-.107	1.42-2.72
B .080-.120	2.03-3.05	B .140-.300	3.56-7.62
C 1.0 MIN.-1.5 MAX.	25.4 MIN.-38.1 MAX.	C 1.0 MIN.-1.5 MAX.	25.4 MIN.-38.1 MAX.
D .018-.022	.46-.56	D .018-.022	.46-.56

J, JTX, JTXV 1N4148 and 1N4148-1	
INCHES	MILLIMETERS
A .056-.075	1.42-1.91
B .140-.180	3.56-4.57
C 1.0 MIN.-1.5 MAX.	25.4 MIN.-38.10 MAX.
D .018-.022	.46-.56

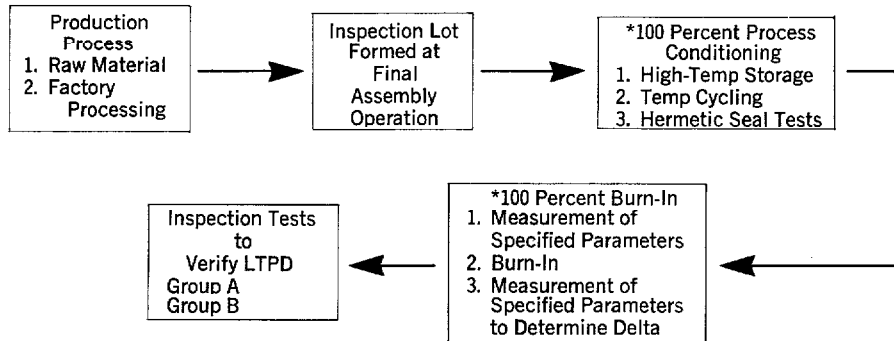
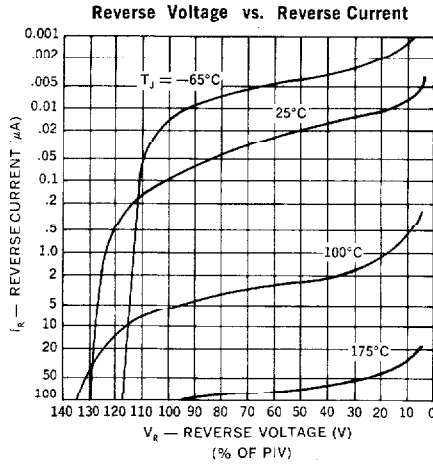
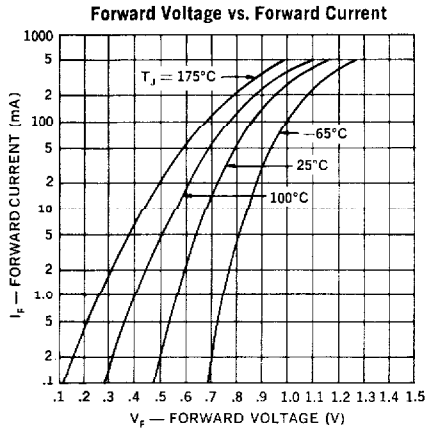
DO-34  
1N4531
DO-35  
1N914  
1N4148



**ELECTRICAL SPECIFICATIONS (at 25°C unless noted)**

Reverse Current @ 25°C 25nAdc @ $V_R = 20Vdc$	Reverse Current @ 25°C 0.5 $\mu$ Adc @ $V_R = 75Vdc$	Peak Reverse Current @ 25°C 100 $\mu$ A (pk) @ $V_R = 100V$ (pk)	Reverse Current @ 150°C 50 $\mu$ Adc @ $V_R = 20Vdc$	Reverse Current @ 150°C 100 $\mu$ Adc @ $V_R = 75Vdc$
Forward Voltage 1.0Vdc @ $I_F = 10mAdc$	Forward Recovery Voltage 5.0V (pk) @ $I_F = 50mAdc$	Forward Recovery Time 20ns @ $I_F = 50mAdc$	Reverse Recovery Time 5ns @ $I_F = I_R = 10mA$ $R_L = 100$ ohms	Capacitance 4.0 pF @ $V_R = 0V, f = 1$ MHz $V_{sig} = 50mV$ (pk-pk) 2.8 pF @ $V_R = 1.5V, f = 1$ MHz $V_{sig} = 50mV$ (pk-pk)

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\*Order of the tests in the blocks shall be performed as shown.  
 Order of procedure diagrams for TX types only.