



# Space Forum Agenda

## 20th June 2017 - Noordwijk

Start Time	Session Title	Presenter	Minutes
9:00	Introduction and Welcome Address	Ken O'Neill	15
9:15	RT FPGA Product Update	Minh Nguyen	45
10:00	High Precision Frequency and Timing Solutions for Space	Peter Cash	30
10:30	<b>Break / Demo Room</b>	Microsemi & Partners	30
11:00	Space Power and Point of Load Solutions Product Overview	Chris Hart	45
11:45	RTG4 Power Hardware Reference Design	Brian Wilkinson	30
12:15	<b>Lunch / Demo Room</b>	Microsemi & Partners	60
13:15	RT FPGA Radiation Characterization	J.J. Wang	30
13:45	Mixed Signal Solutions for Space	Dorian Johnson	45
14:30	Achieving Timing Closure for Maximum Performance in RTG4 FPGAs	Hichem Belhadj	30
15:00	<b>Break / Demo Room</b>	Microsemi & Partners	30
15:30	Using DirectC and RISC-V to Reprogram RTG4 FPGAs	Ken O'Neill	15
15:45	Next Generation Power Discretets	Chris Hart	30
16:15	Space Application Focus	Hichem Belhadj	30
16:45	Q&A session and Wrap up	All	15
17:00	End of Conference		