Microchip's motor control solution is designed specially to meet the challenging industrial requirements of performance, reliability and safety in an easy-to-use environment. We offer a modular Intellectual Property (IP) portfolio, tools, reference designs, kits and software to control motors such as Permanent-Magnet Synchronous Motor (PMSM)/Brushless DC (BLDC) and stepper motors.

**Summary**

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**Reference Design Features**

- Motor control algorithms implemented in FPGA fabric
- Scalability to a multi-axis motor drive design
- Design flexibility with modular IP suite
- Deterministic, high-precision, low-power, reliable and secure
- Robust sensorless solution—supports 100K RPM or more
- Integration of system functions to lower Total Cost of Ownership (TCO)
- Low latency of 1 μs for Field-Oriented Control (FOC) loop from ADC measurement to PWM generation

**IP Modules**

- Space Vector Modulation (SVM)
- Three-phase PWM
- FOC transformation IPs (Clarke, Park, Inverse Clarke and Inverse Park)
- PI controller
- Rate limiter
- Position and speed estimator
- Encoder, HALL and resolver interface

**Motors and Algorithms**

<table>
<thead>
<tr>
<th>Motor</th>
<th>Algorithm</th>
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<tbody>
<tr>
<td>PMSM/BLDC</td>
<td>FOC sensorless</td>
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<tr>
<td></td>
<td>FOC with HALL</td>
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<td>FOC with encoder</td>
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<td>FOC with resolver</td>
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<tr>
<td>Stepper</td>
<td>Microstepping</td>
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**Fully Modular IP Suite**

- Quick plug-and-play approach to implement algorithms
- Easy porting and customization through blockbased approach
- Precise algorithm for angle estimation in sensorless FOC
- PWM with dead time protection and delay time insertion
- IP blocks are coded for efficient use of FPGA resources
- IP blocks tested in simulations and on actual hardware
- IPs available as MATLAB models; VHDL Verilog code for FPGA

**Motor Control Block Diagram**

![Motor Control Block Diagram](image)

**Solutions for Motor Control Block Diagram**

- Automation Controller/Host CPU
- System Control
- Power Management
- Sensors: Speed, Torque, Position A/D Conversion
- Power Supply/Conversion
- Host Interface
- Arm® Cortex®-M3 Processor
- eNVM, eSRAM
- PID Control Loop
- PWM Timing
- Transforms
High-Performance, Reliable and Secured Motor Control Solution

SmartFusion®2 SoC-FPGA-based solution is an ideal reference platform for developing high-performance, reliable and secured dual-axis motor control applications. The solution includes algorithms such as sensorless FOC, FOC with HALL, FOC with encoder and FOC with resolver for PMSM/BLDC motors. The position control algorithm of the stepper motor supports up to 1024 micro-steps.

The SmartFusion2 Motor Control GUI allows for the dynamic tuning of parameters, such as reference speed, Kp/Ki gains of PI controllers, and viewing internal signals for debugging. The kit also supports various communication interfaces, including Ethernet, CAN, USB and others. SmartFusion2 SoC FPGAs feature stronger design security than other FPGAs and include Differential Power Analysis (DPA) resistant anti-tamper measures using technology licensed from Cryptography Research Incorporated (CRI). The security architecture was designed with a layered approach in mind, building on top of a foundation of secure hardware.

Microchip offers a one-stop shop with a portfolio of complementary products to meet your motor control needs. Microchip’s product portfolio includes FPGAs, SiC diodes/MOSFETs, power modules, timing, PoE/Midspans, sensors and more.

Software GUI for Motor Control

Dual-Axis Motor Control Board

Additional Reference Designs Available on Request

- Six axis motor control board with a https://emcraft.com/products/255 M2S010 Emcraft SOM

Ordering Information

For questions and feedback write to fpga.motorcontrol@microchip.com

<table>
<thead>
<tr>
<th>SmartFusion2 Dual-Axis Motor Control Starter Kit</th>
<th>SF2-MC-STARTER-KIT</th>
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</thead>
<tbody>
<tr>
<td>RTL source code for Motor Control IP cores</td>
<td>MCSOLCores-RM</td>
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</table>

For more information: www.microsemi.com/applications/motor-control