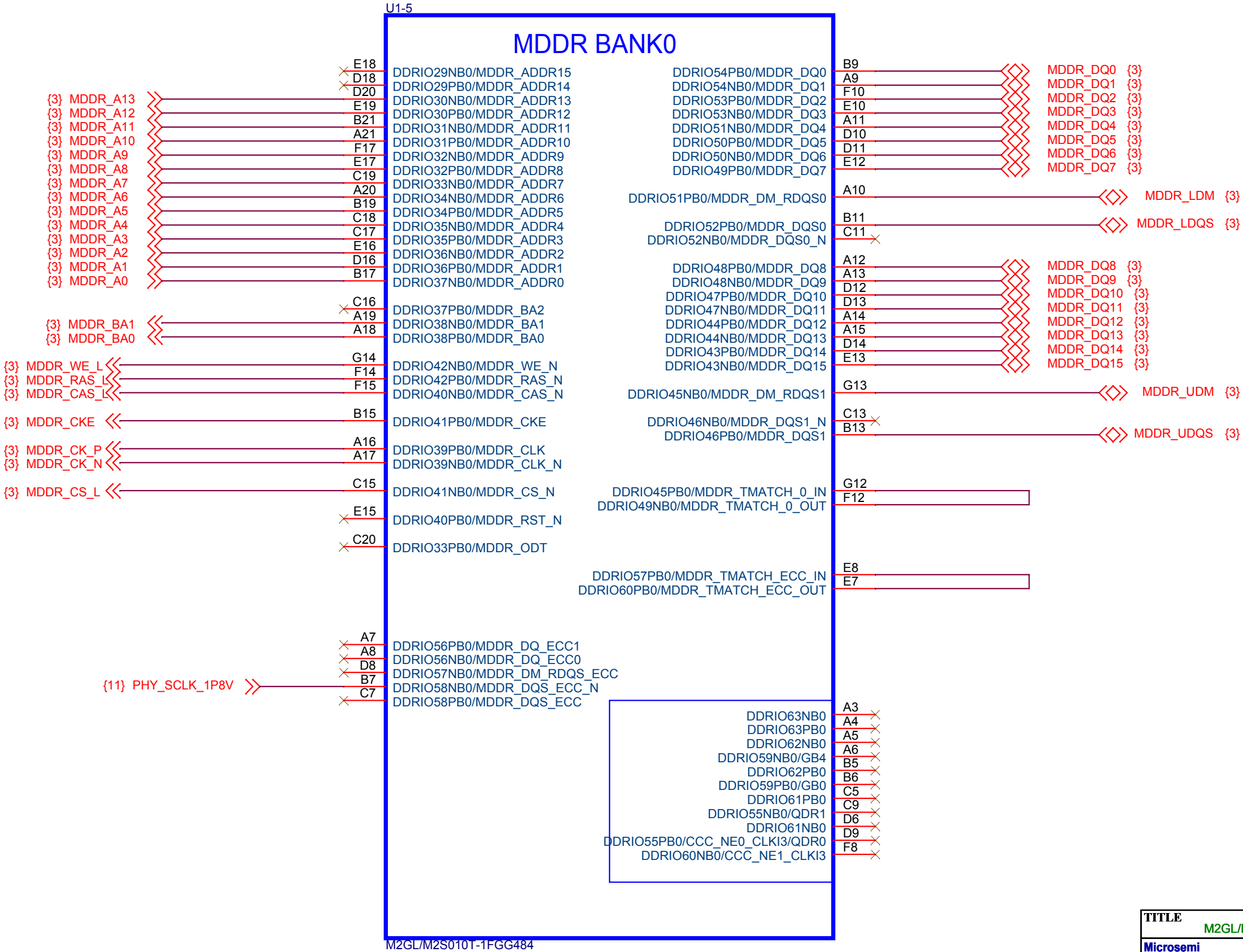


M2GL/M2S-EVAL-KIT

DVP-100-000402-001 RevE

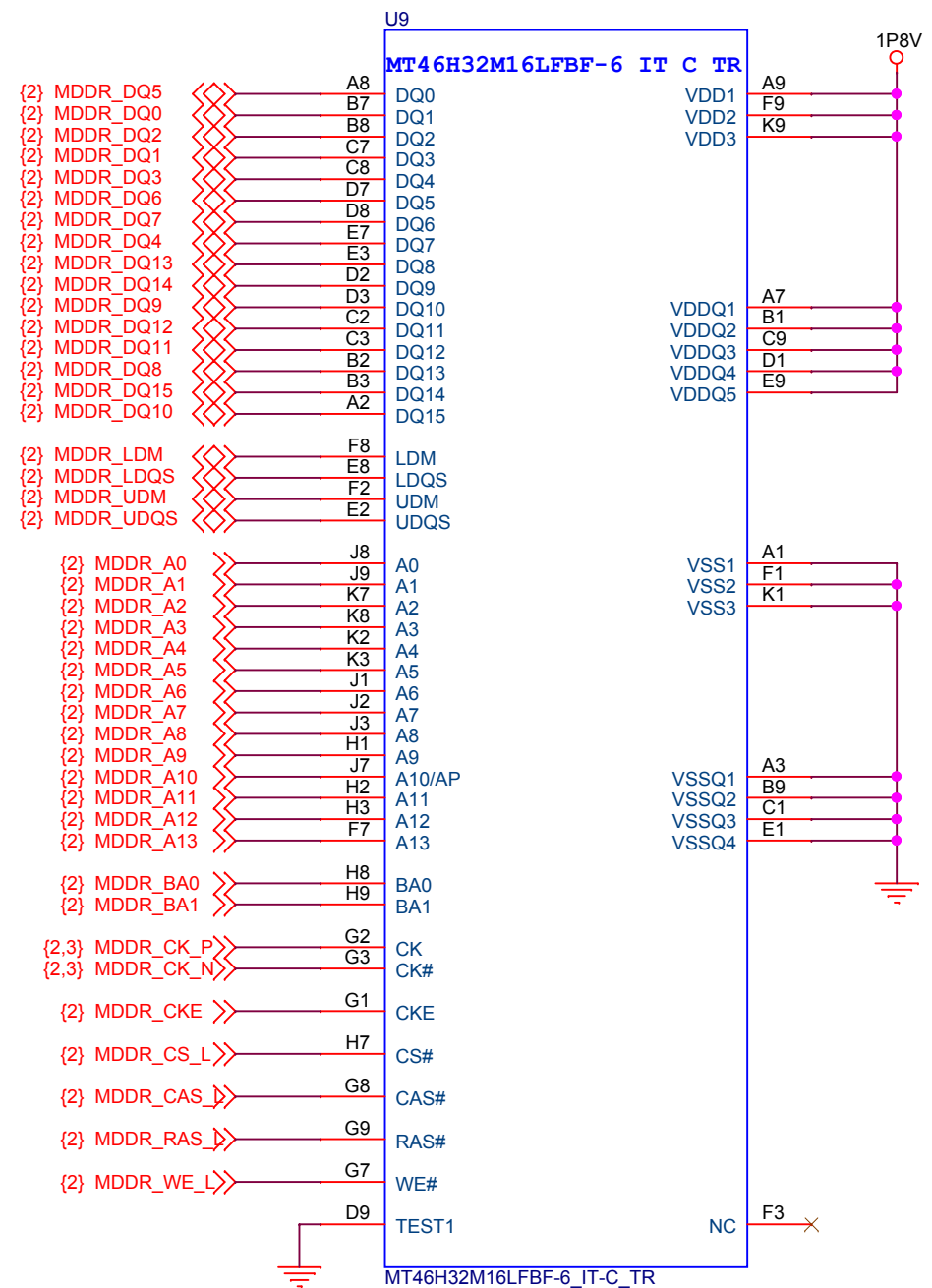
Microsemi Corporation	
Page	Schematic Title
01	TITLE AND INDEX
02	FG484 DDR INTERFACE
03	LPDDR MEMORY INTERFACE
04	SERDES 0 INTERFACE
05	BANK 1 & 2 CONNECTION
06	BANK 4,6 & 7 CONNECTION
07	GPIO HEADER
08	JTAG,I2C & SPI INTERFACE
09	SF2-POWER AND GROUND CONNECTION
10	USB INTERFACE
11	MARVELL PHY INTERFACE
12	MARVELL PHY INTERFACE
13	PROGRAMMING CIRCUITRY
14	FT4232H CONNECTION
15	DEBUGGING CIRCUITRY
16	POWER SUPPLIES 1
17	POWER SUPPLIES 2
18	POWER SUPPLIES 3
19	POWER SUPPLIES 4
20	POWER SUPPLIES 5
21	POWER SUPPLIES 6 & LED

MDDR - BANK0 INTERFACES



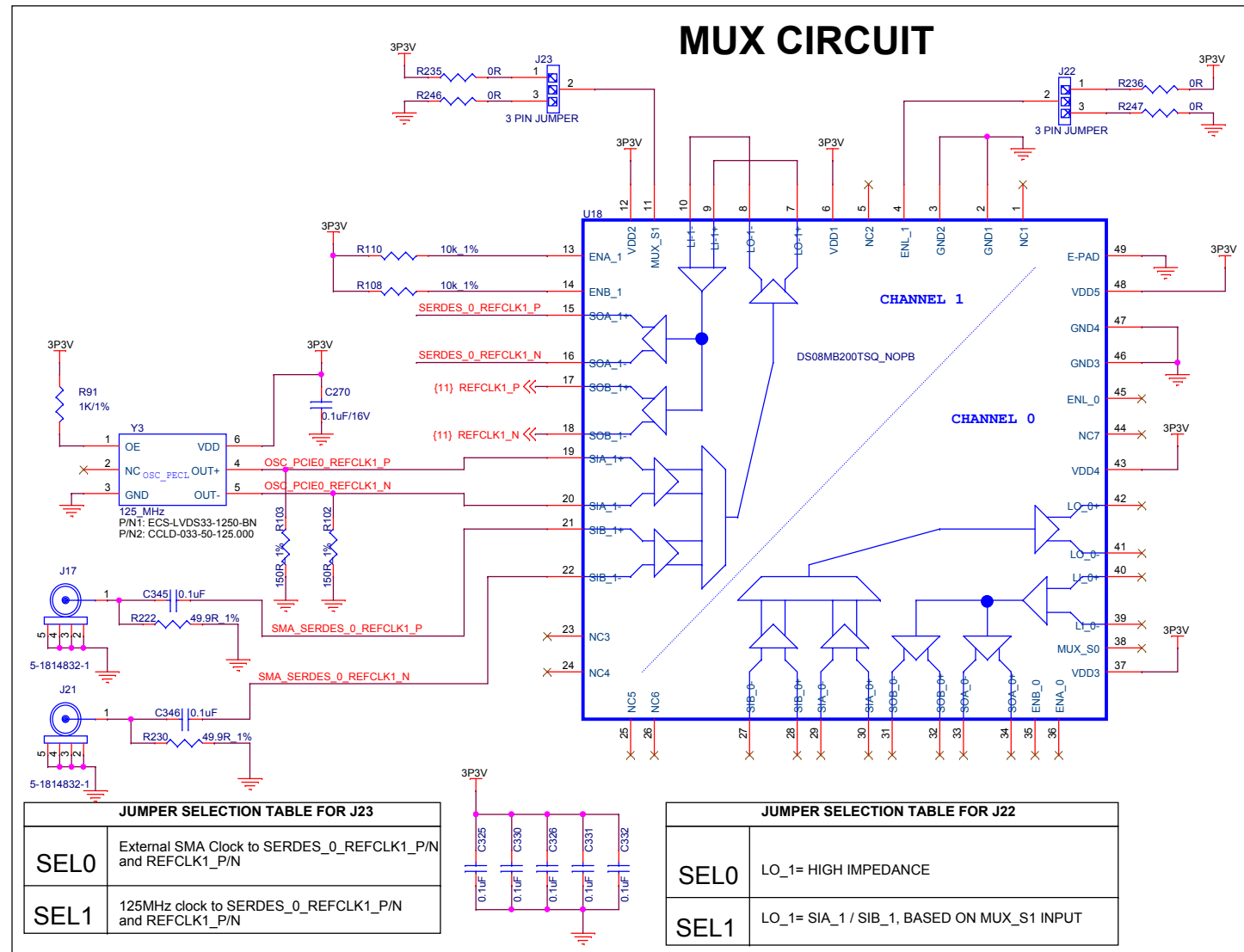
TITLE		
M2GL/M2S-EVAL-KIT		
Microsemi		
SIZE	DOCUMENT NO.	REV
Custom		E
DATE:	Monday, August 01, 2016	SH 2 OF 21

LPDDR MEMORY INTERFACE



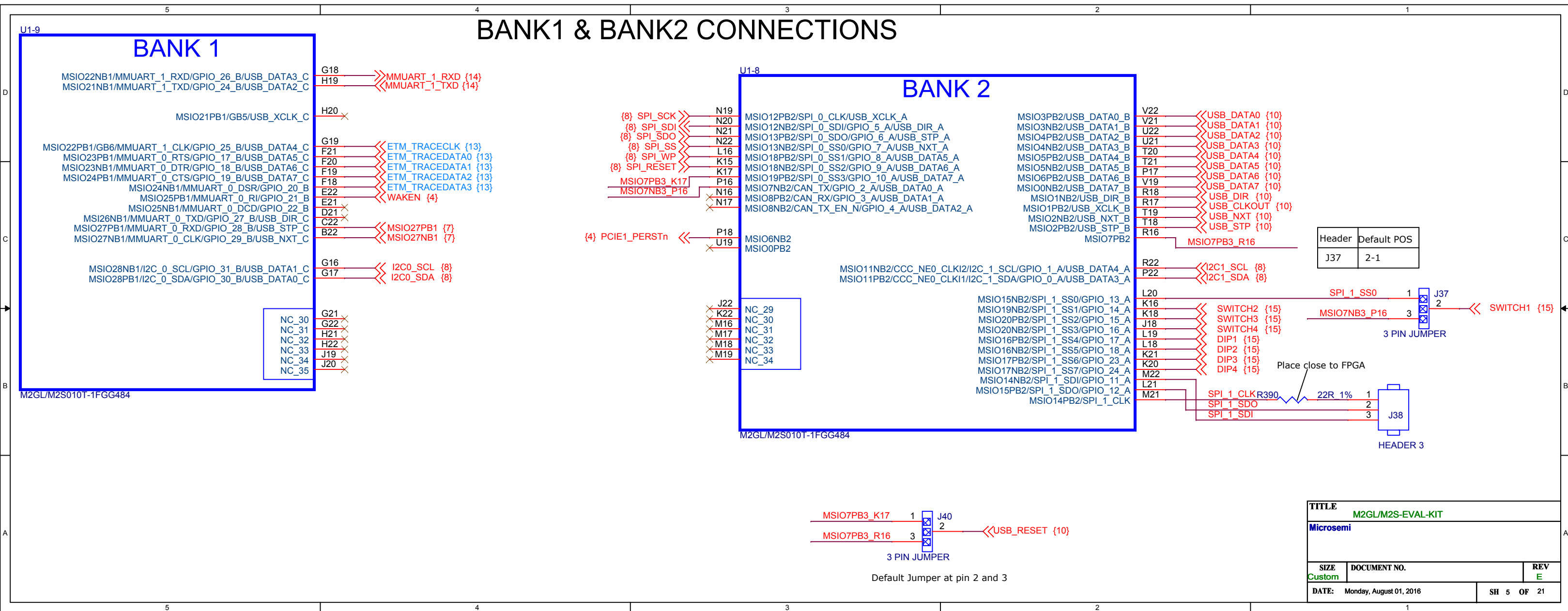
TITLE		
M2GL/M2S-EVAL-KIT		
Microsemi		
SIZE	DOCUMENT NO.	REV
Custom		E
DATE:	Monday, August 01, 2016	SH 3 OF 21

MUX CIRCUIT

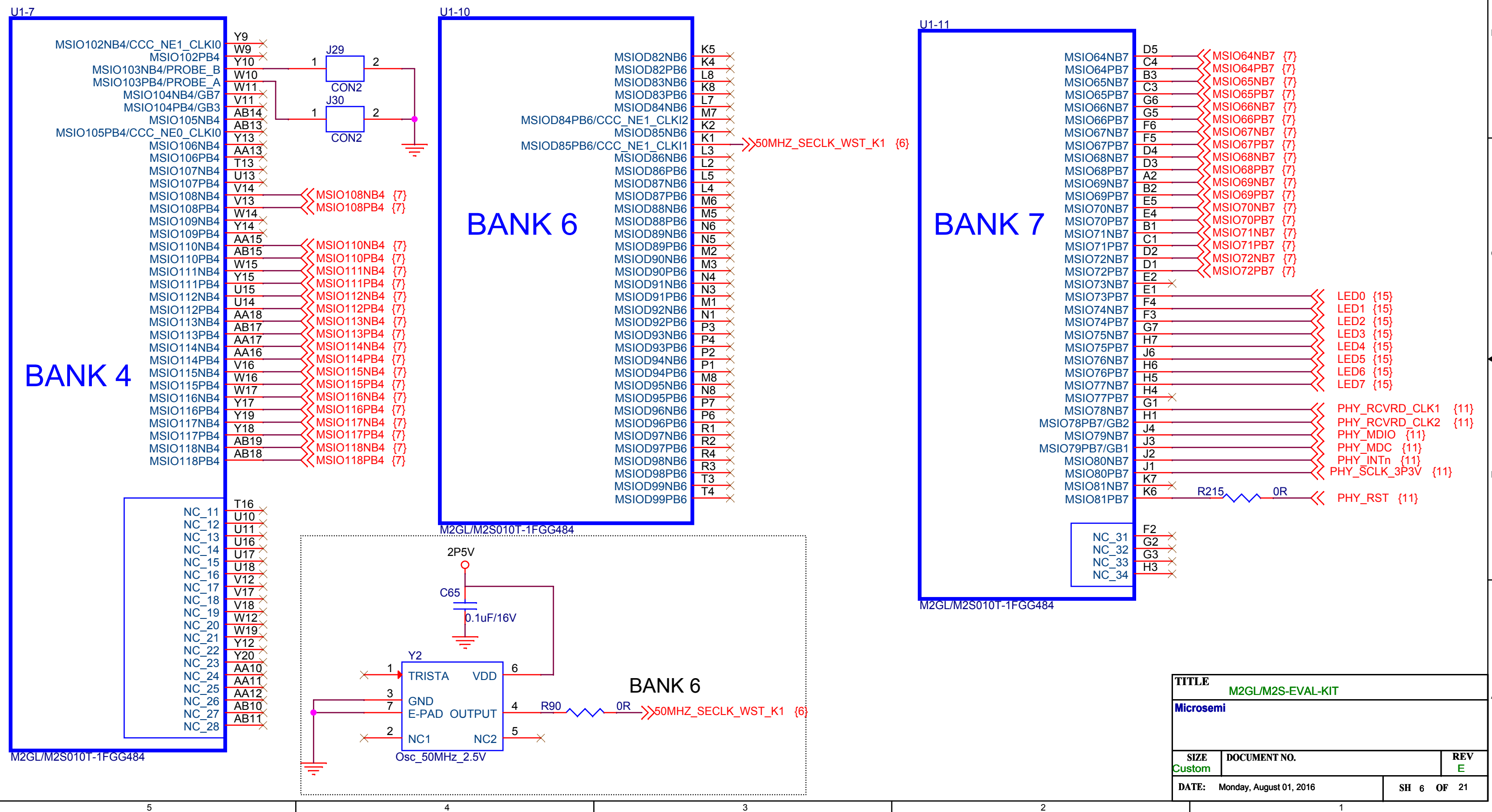


JUMPER SELECTION TABLE FOR J22	
SELO	LO_1= HIGH IMPEDANCE
SEL1	LO_1= SIA_1 / SIB_1, BASED ON MUX_S1 INPUT

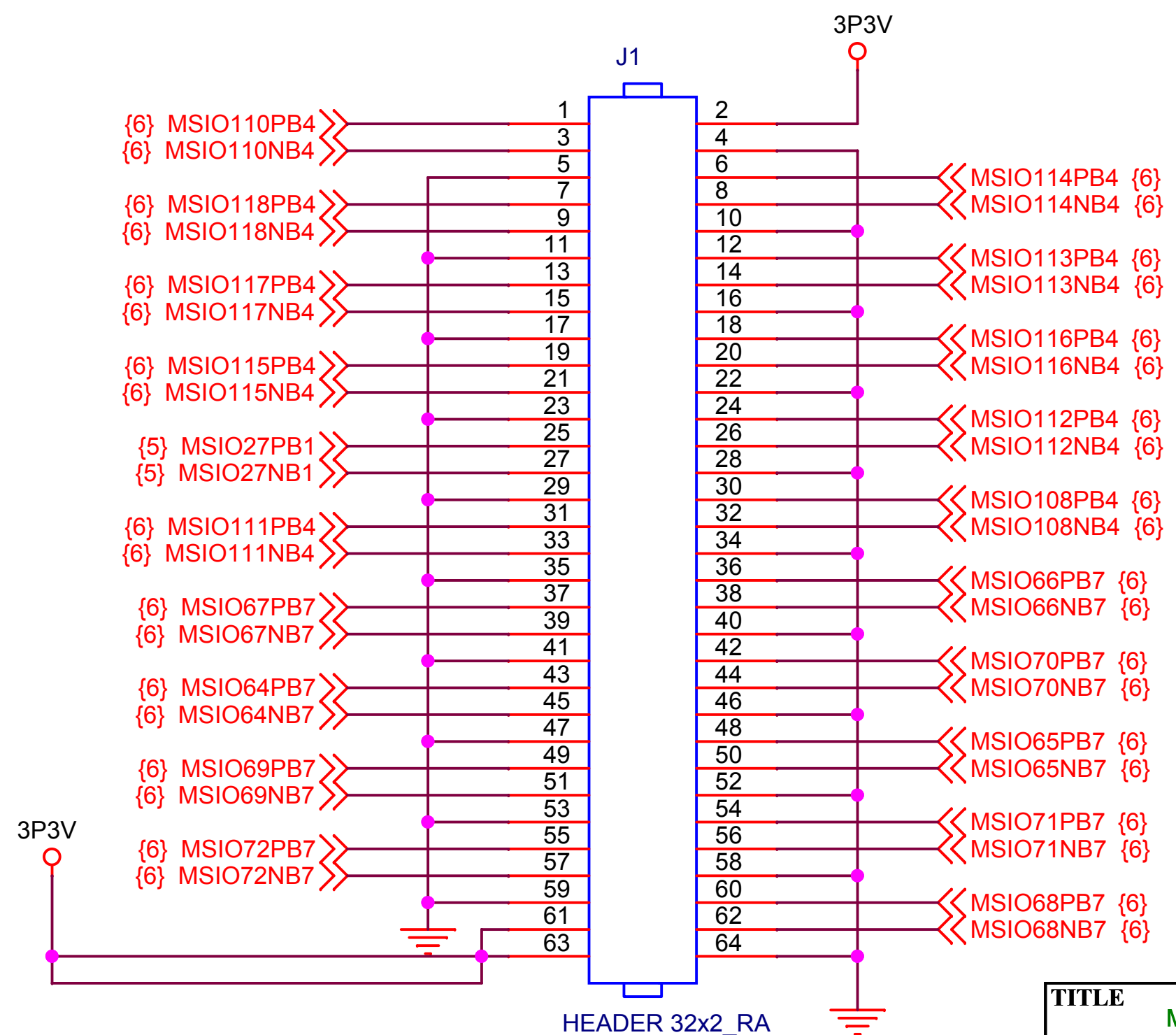
TITLE M2GL/M2S-EVAL-KIT			
Microsemi			
SIZE C	DOCUMENT NO.		REV E
DATE: Monday, August 01, 2016		SH	4 OF 21



BANK 4, 6 and 7 CONNECTIONS



GPIO HEADER

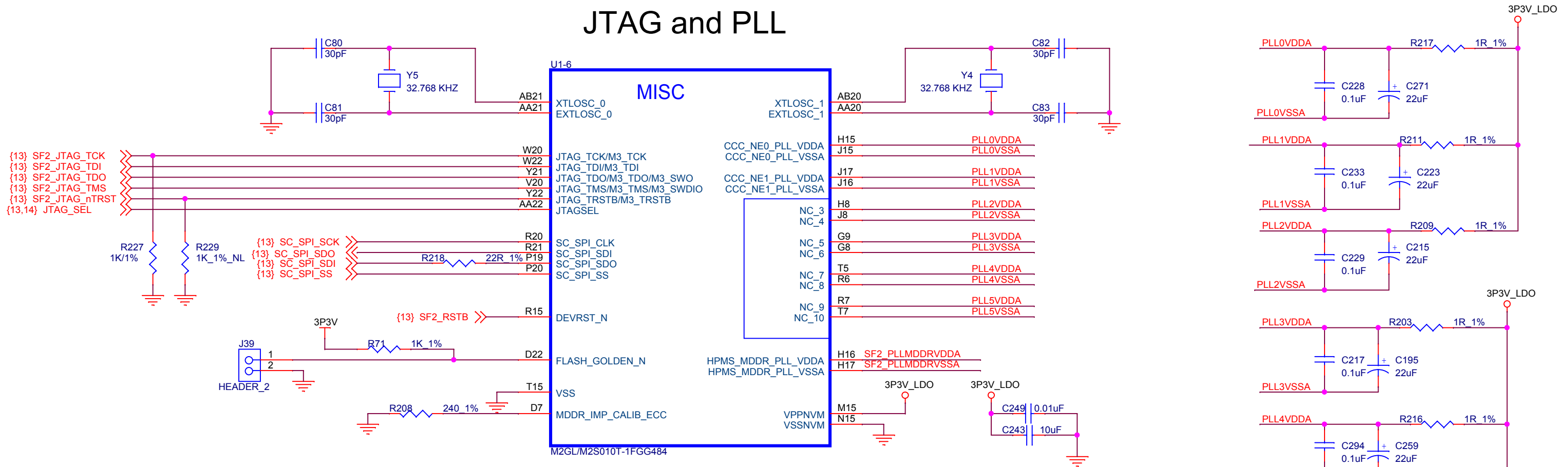


MANUFACTURER P/N = TSW-132-08-T-D-RA
MANUFACTURER = Samtec

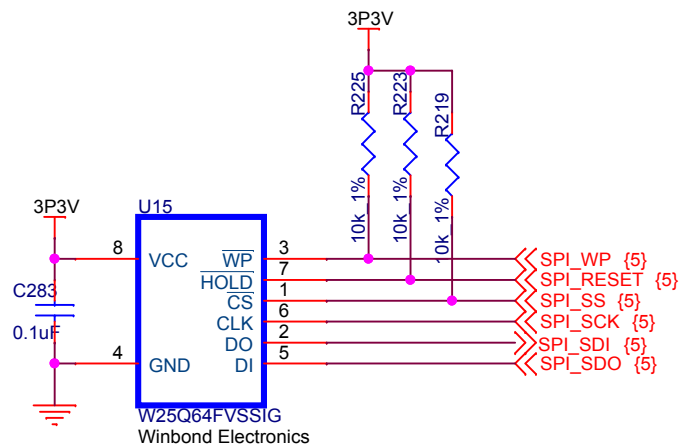
MATING RECEPTACLE P/N: SSW-132-02-S-D-RA
MANUFACTURER: Samtec

TITLE		
M2GL/M2S-EVAL-KIT		
Microsemi		
SIZE	DOCUMENT NO.	REV
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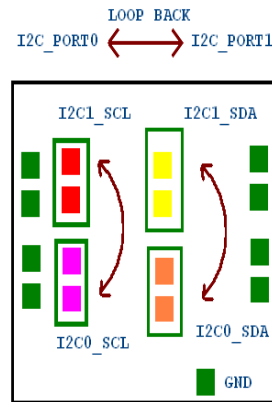
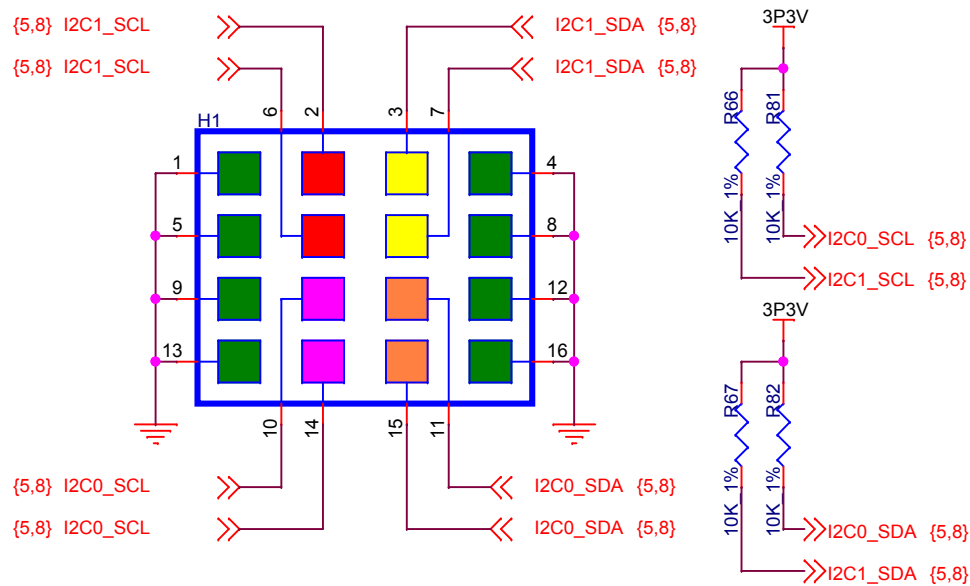
JTAG and PLL



SPI FLASH INTERFACE

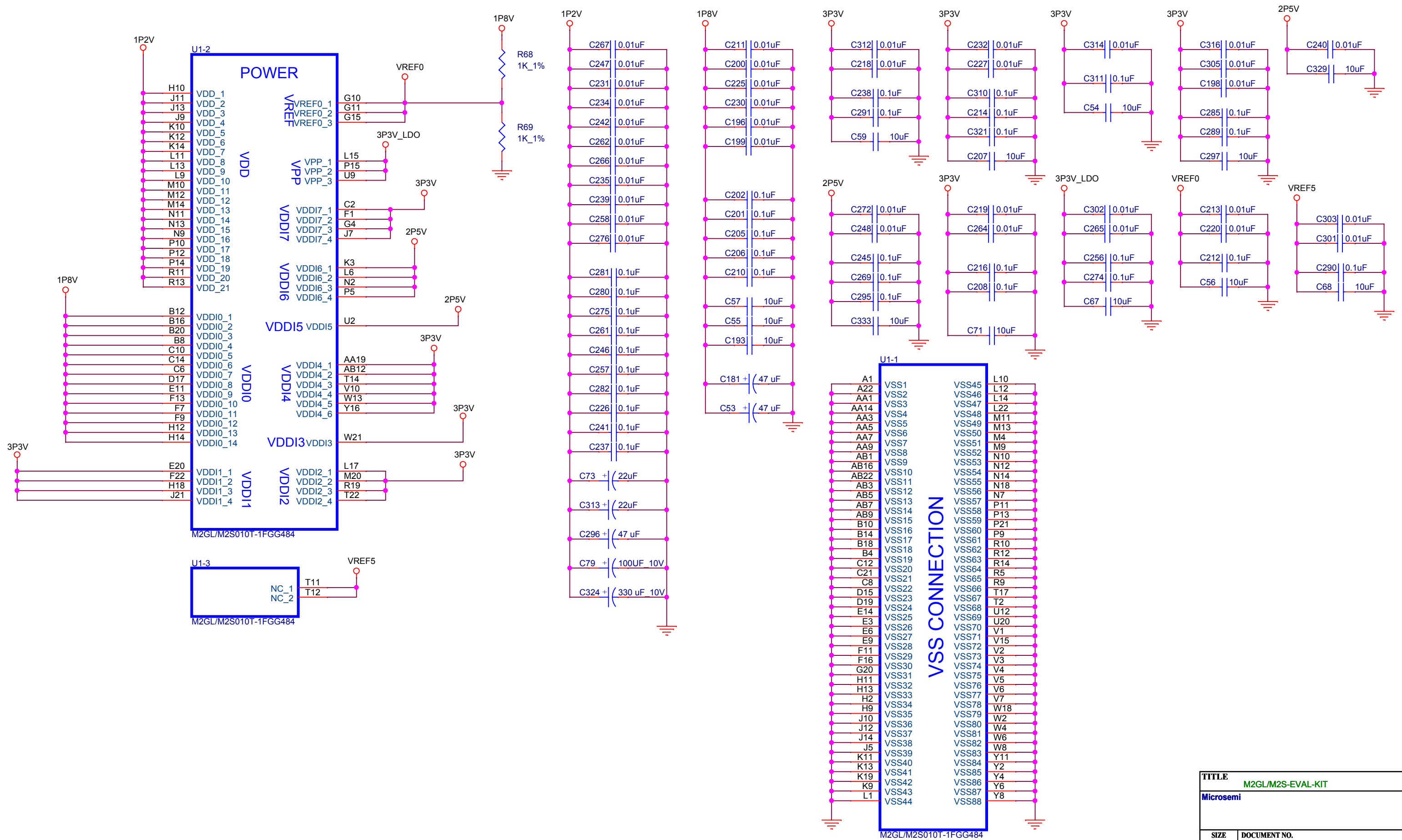


I2C PORT HEADER



TITLE		
M2GL/M2S-EVAL-KIT		
Microsemi		
SIZE	DOCUMENT NO.	REV
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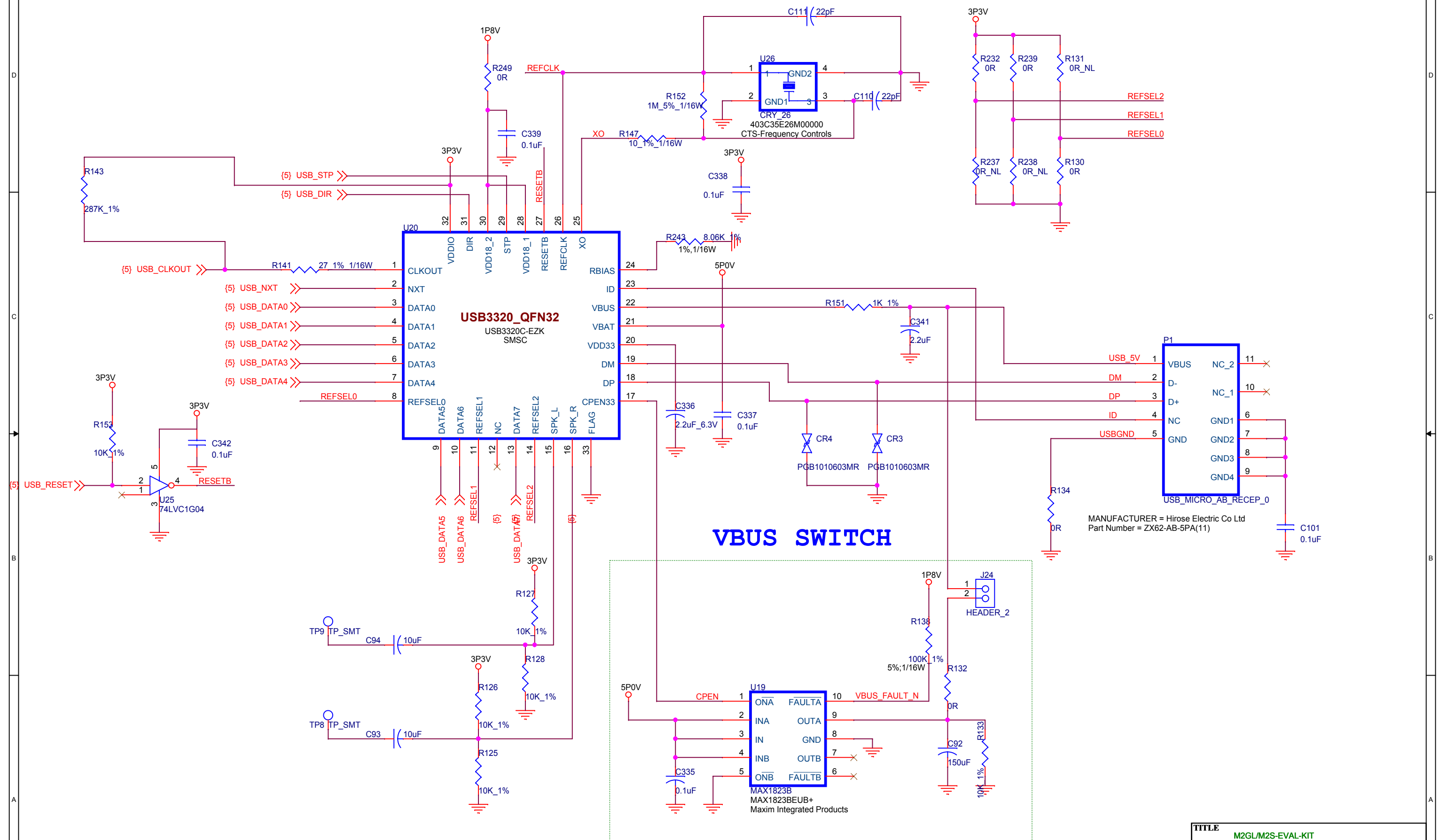
SF2-POWER, GND AND DECOUPLING CAPACITORS



TITLE M2GL/M2S-EVAL-KIT			
Microsemi			
SIZE B	DOCUMENT NO.		REV E
DATE: Monday, August 01, 2016		SH 9 OF 21	

USB Interface

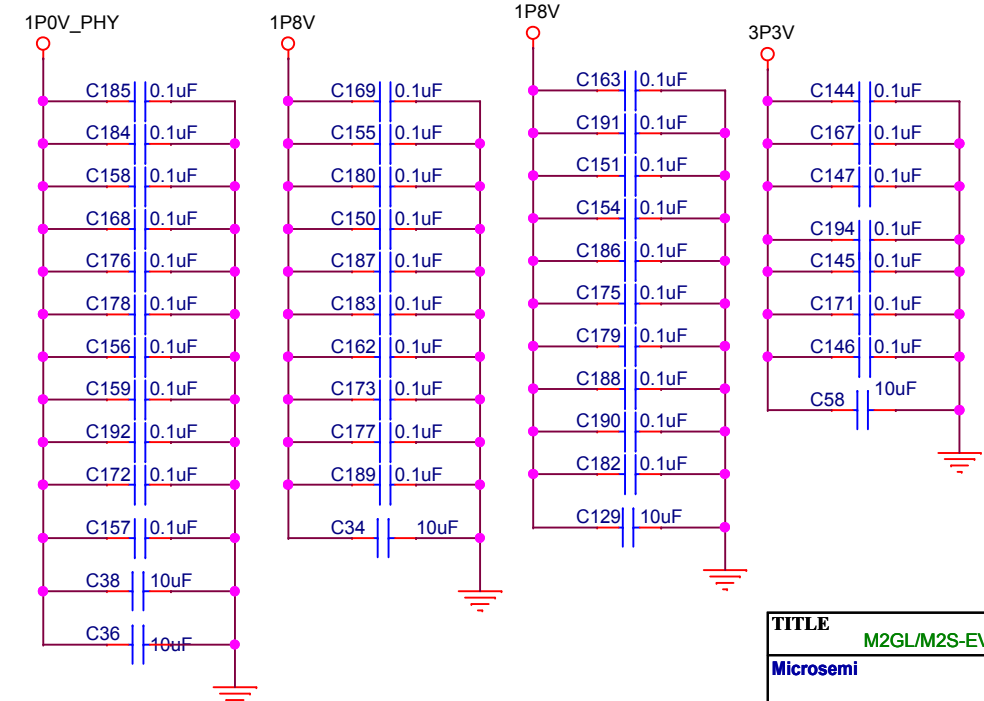
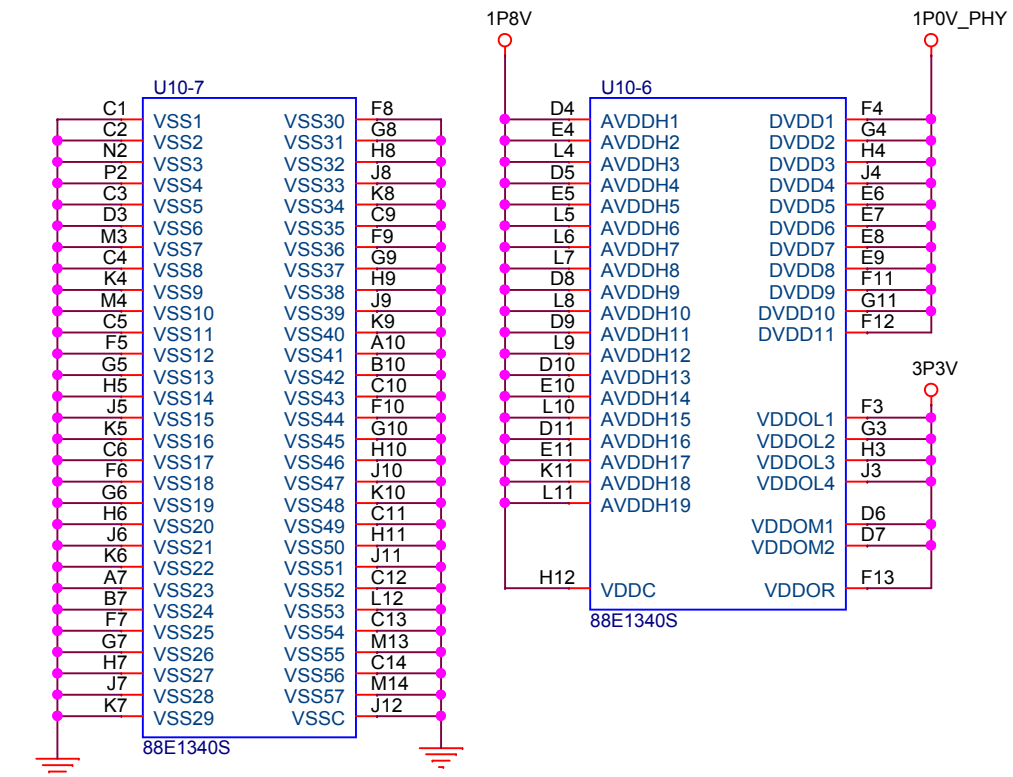
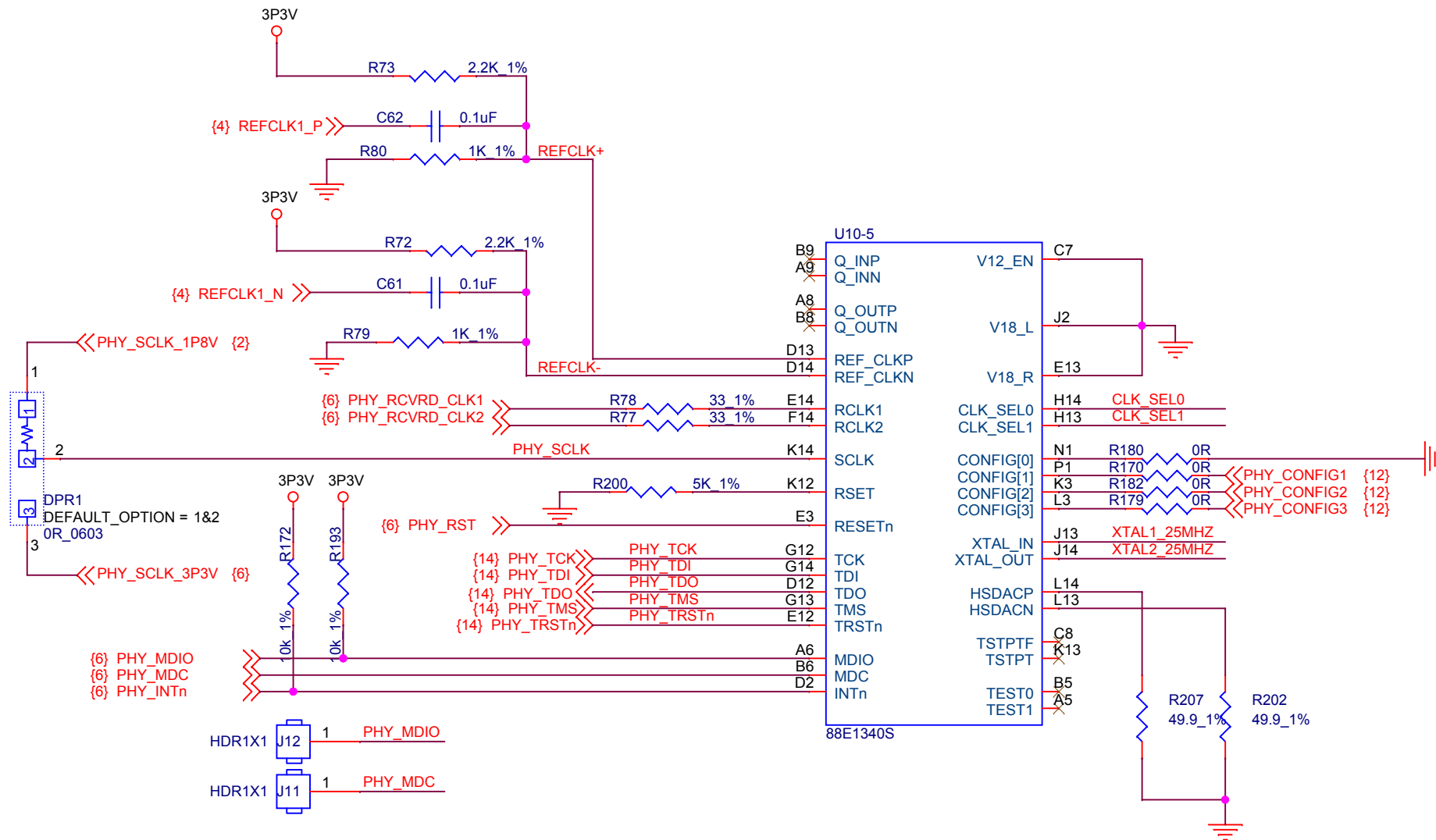
Schematic shows 26.0 MHz Configuration.
REFCLK[2:0] = 110



TITLE			
M2GL/M2S-EVAL-KIT			
Microsemi			
SIZE Custom	DOCUMENT NO.		REV E
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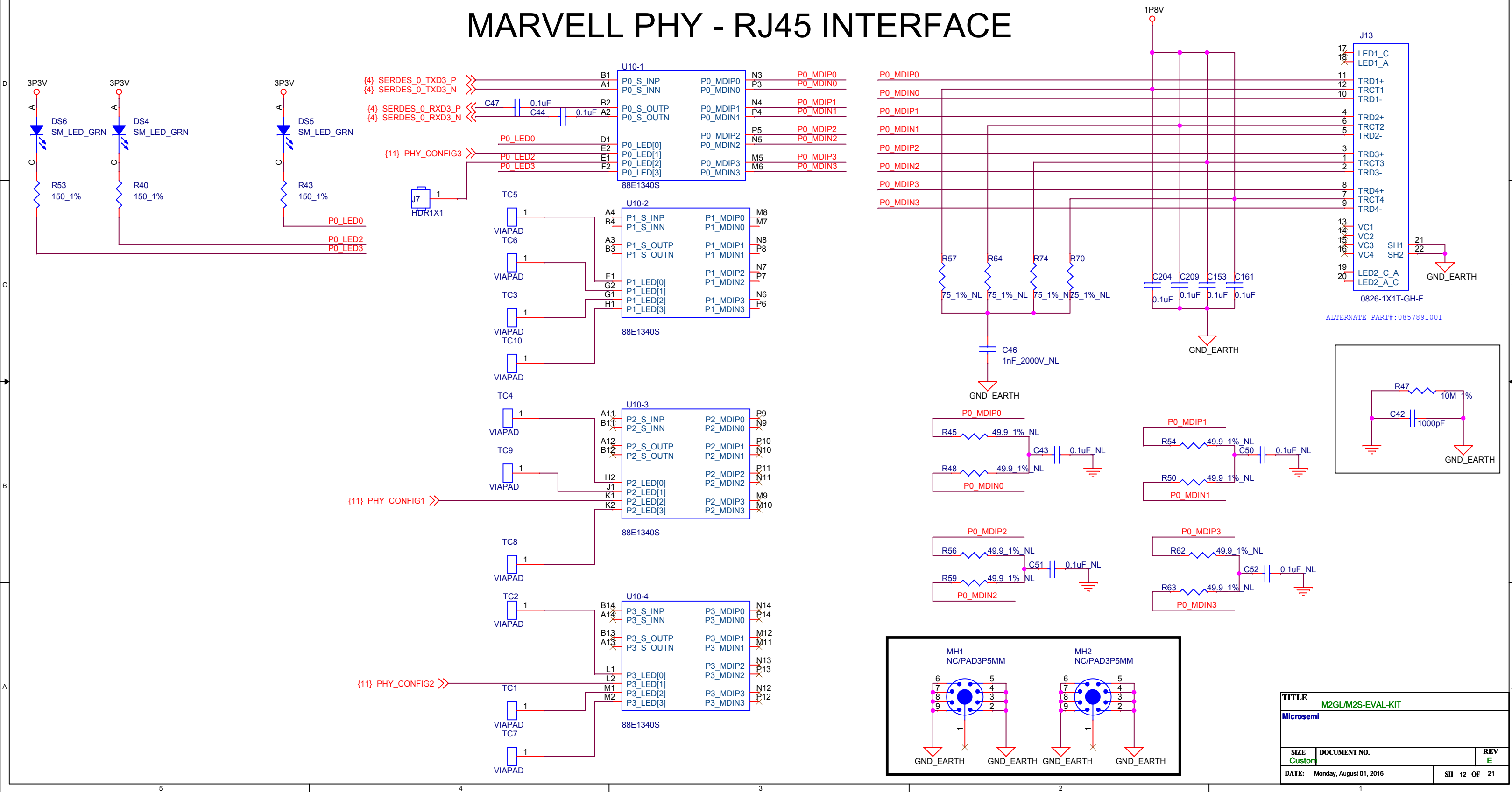
MARVELL PHY INTERFACE

need to check voltage swing level



TITLE M2GL/M2S-EVAL-KIT		
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MARVELL PHY - RJ45 INTERFACE



PROGRAMMING CIRCUITRY

SC SPI Header

TO FPGA SC_SPI

TRACE ETM HEADER

RVI HEADER

MUX D

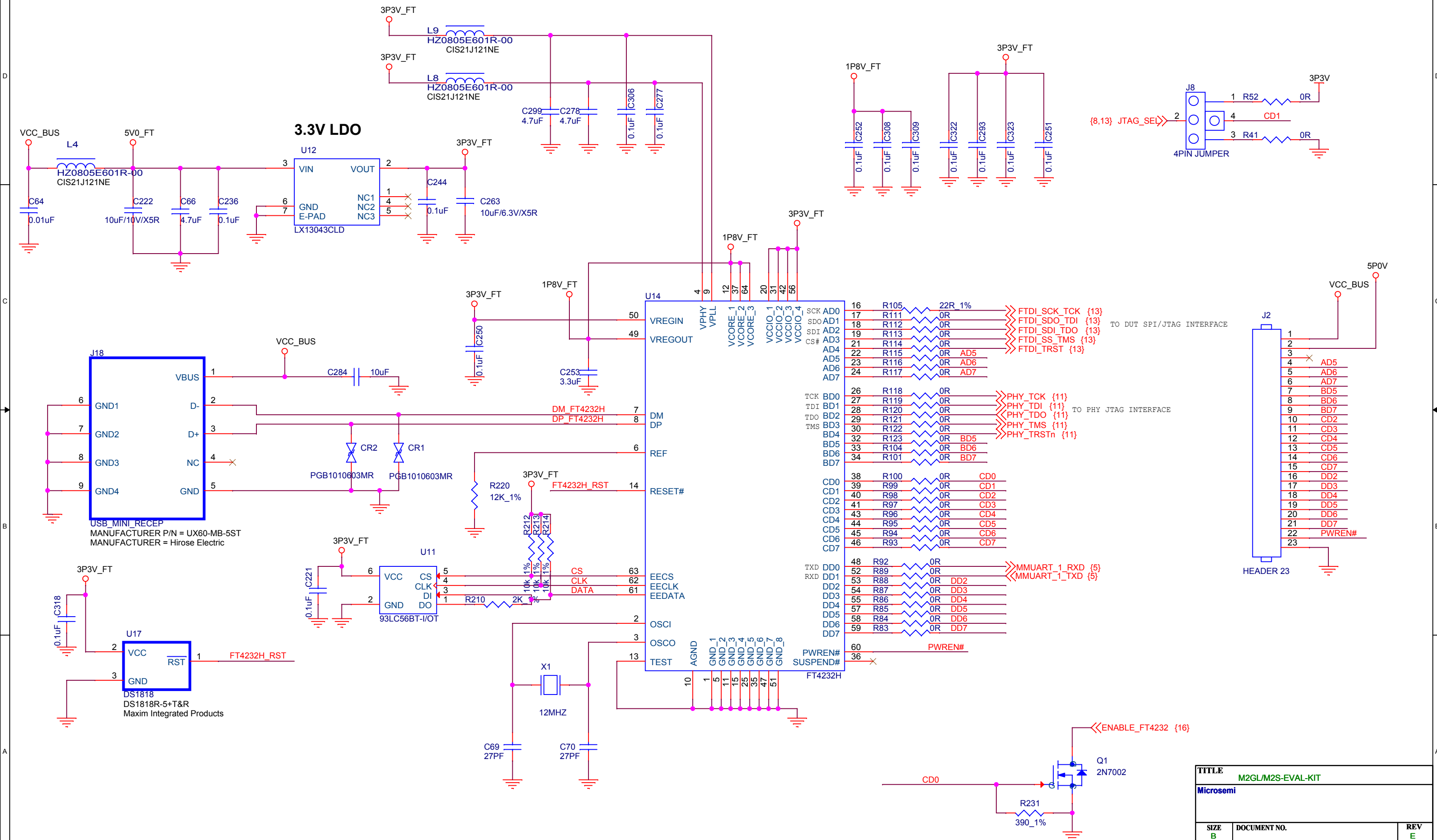
TO FPGA JTAG

RESET

FP4 HEADER

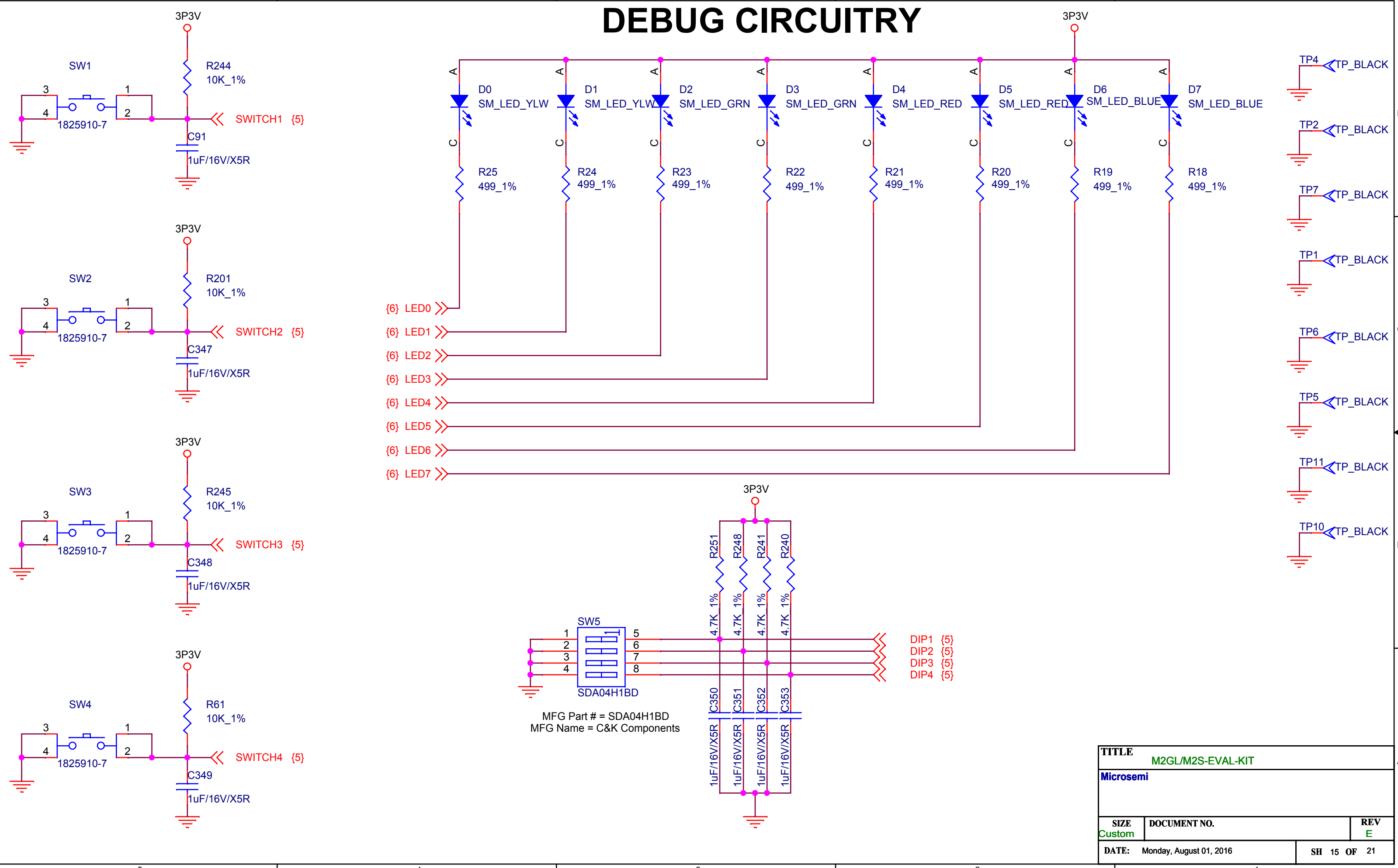
H PATH 1 - Programming Through FP4 HEADER
L- PATH 0 - Programming Through FTDI JTAG

FT4232H CIRCUITRY



TITLE M2GL/M2S-EVAL-KIT			
Microsemi			
SIZE B	DOCUMENT NO.		REV E
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DEBUG CIRCUITRY

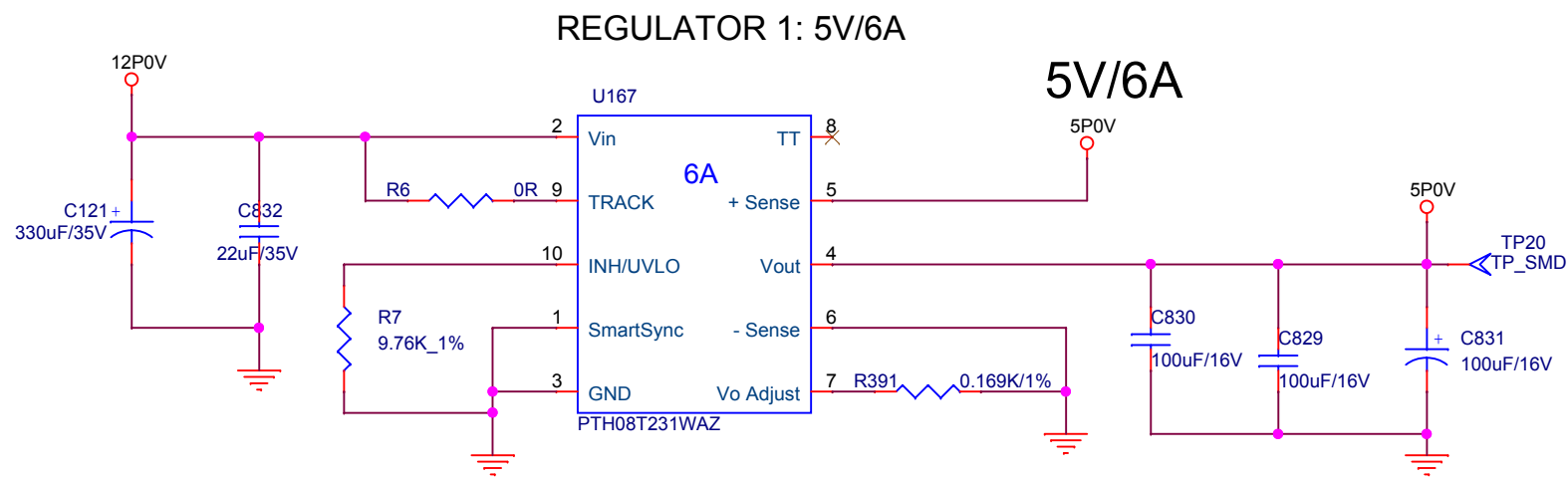


TITLE		
M2GL/M2S-EVAL-KIT		
Microsemi		
SIZE	DOCUMENT NO.	REV
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D

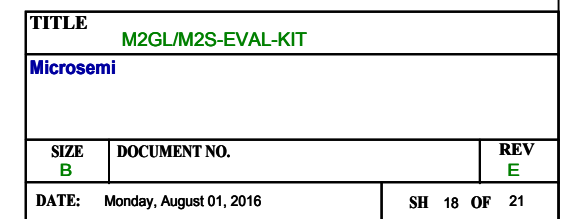
B

POWER SUPPLY 2



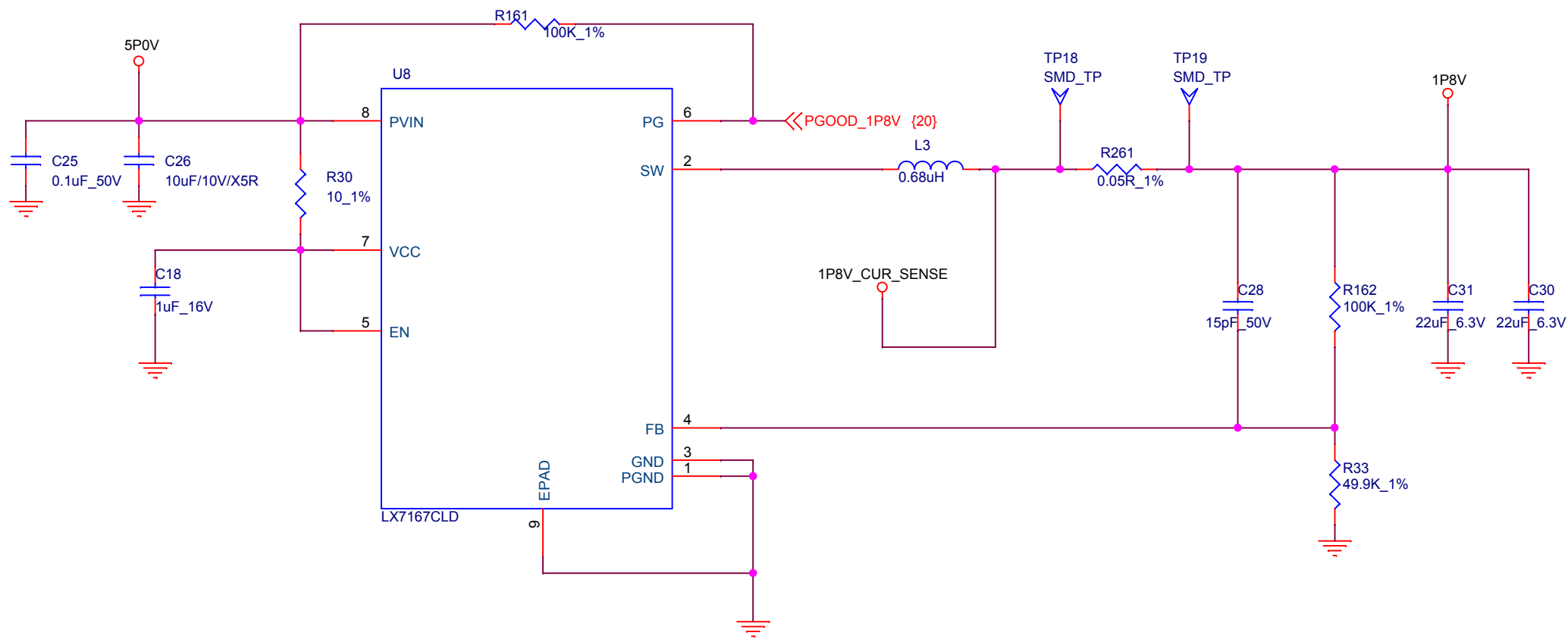
TITLE		
M2GL/M2S-EVAL-KIT		
Microsemi		
SIZE	DOCUMENT NO.	REV
B		E
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1.2V REGULATOR

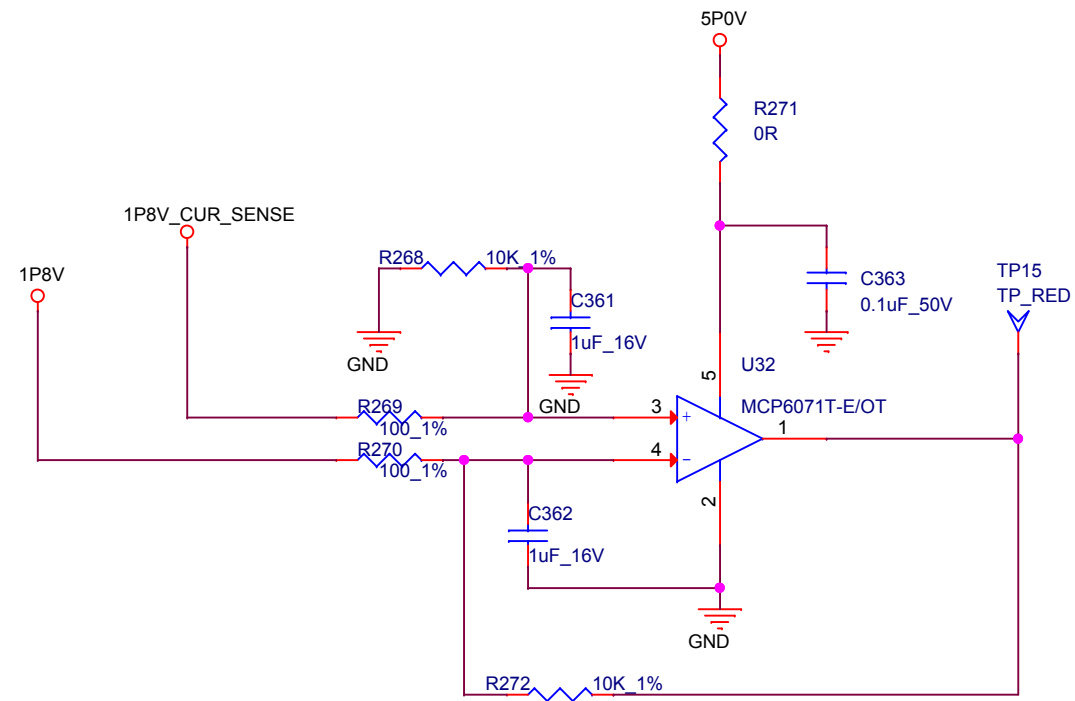


POWER SUPPLY 4

1.8V REGULATOR



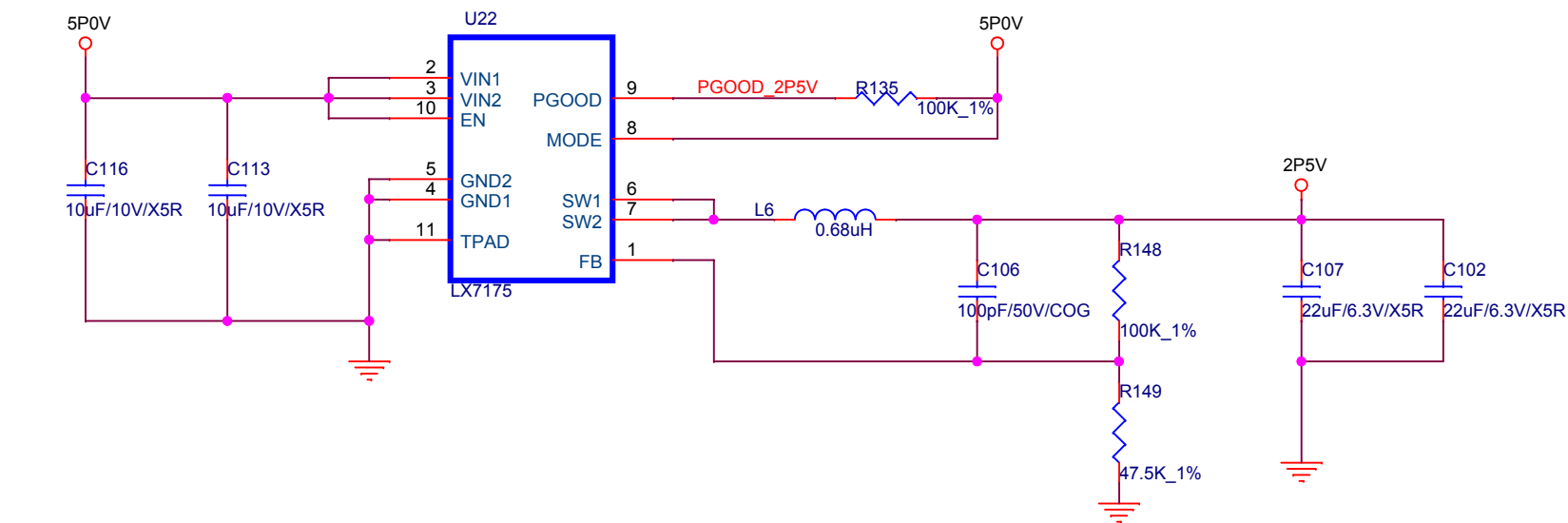
1.8V CURRENT SENSING CIRCUIT



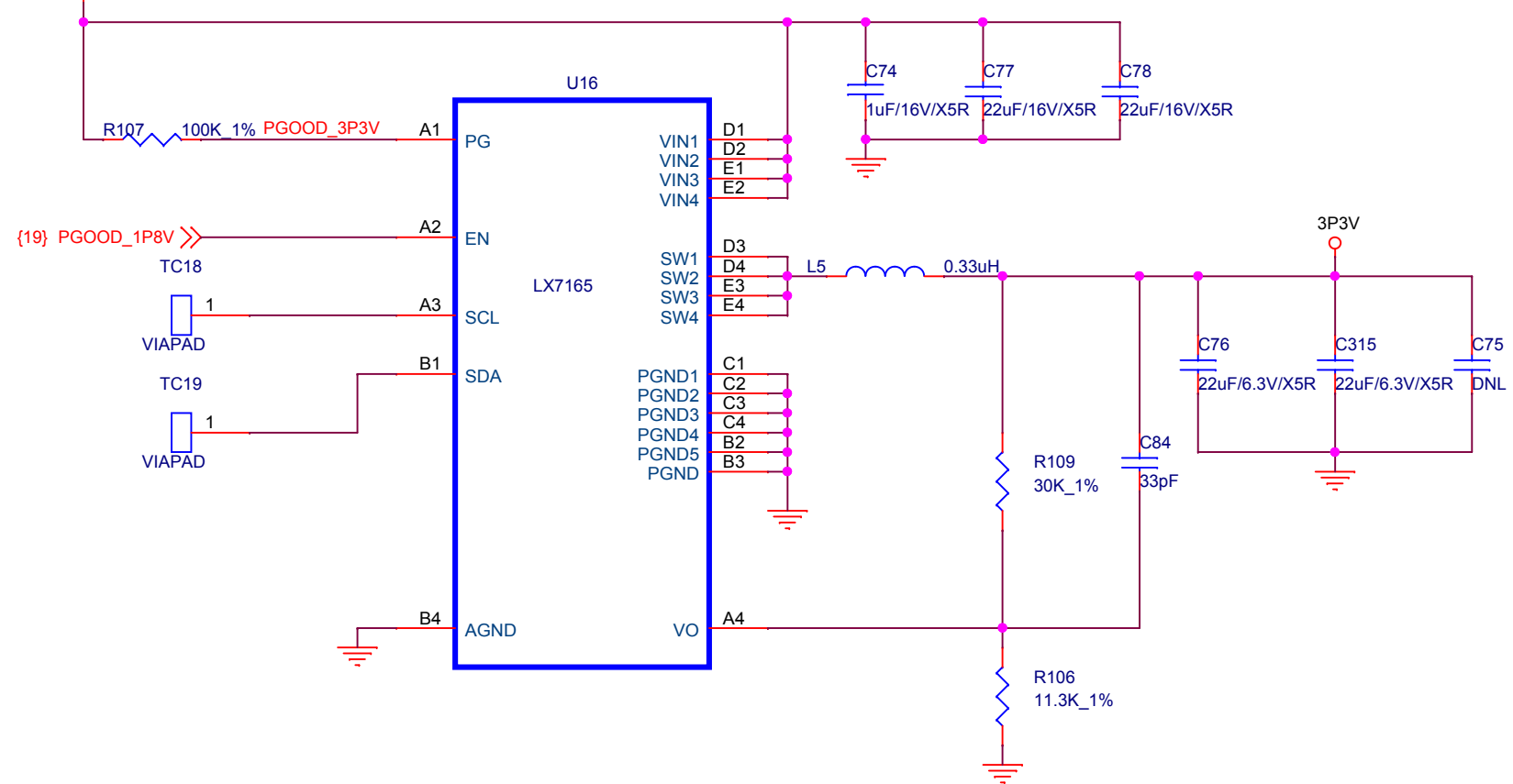
TITLE		
M2GL/M2S-EVAL-KIT		
Microsemi		
SIZE	DOCUMENT NO.	REV
B		E
DATE:	Monday, August 01, 2016	SH 19 OF 21

POWER SUPPLY 5

2.5V REGULATOR



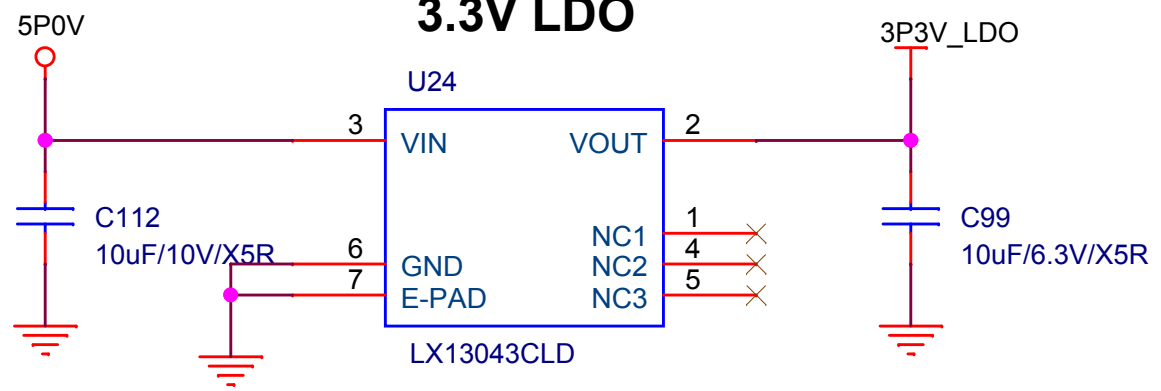
3.3V REGULATOR



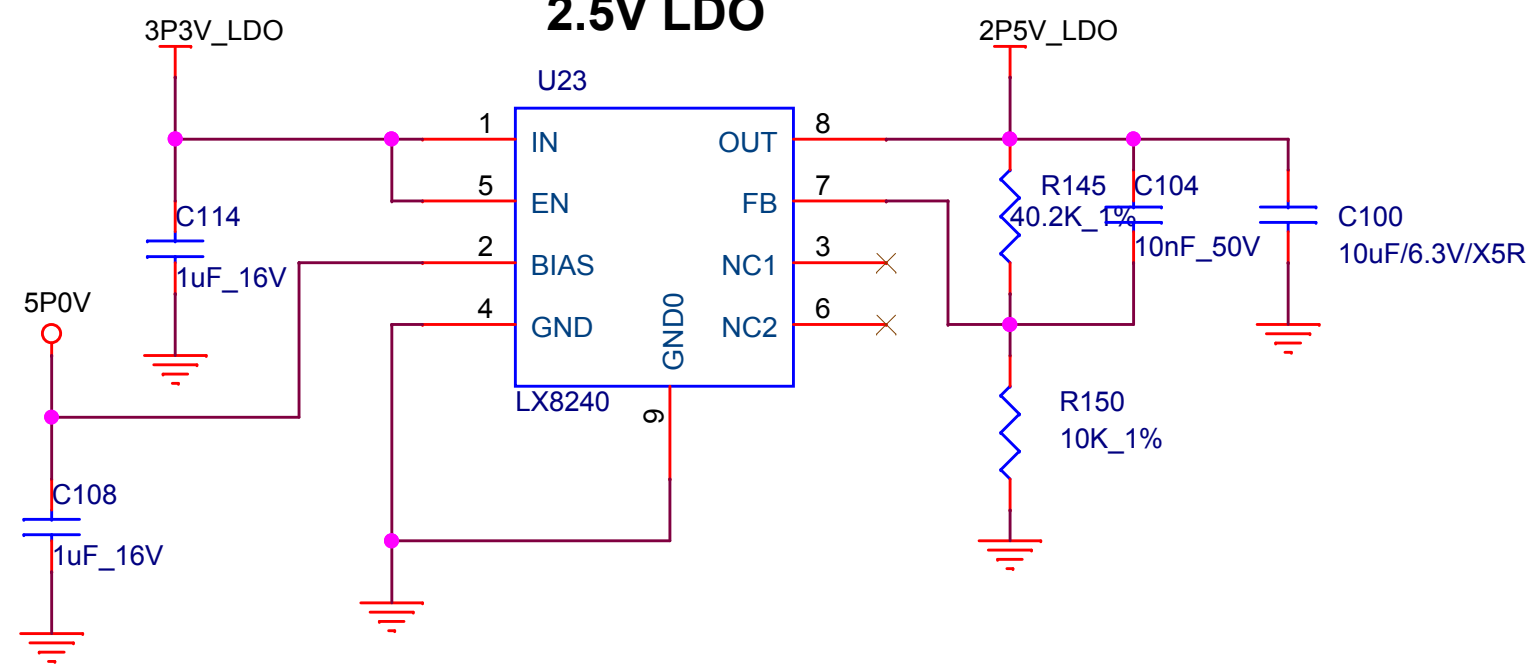
TITLE		
M2GL/M2S-EVAL-KIT		
Microsemi		
SIZE	DOCUMENT NO.	REV
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POWER SUPPLY 6 & LED

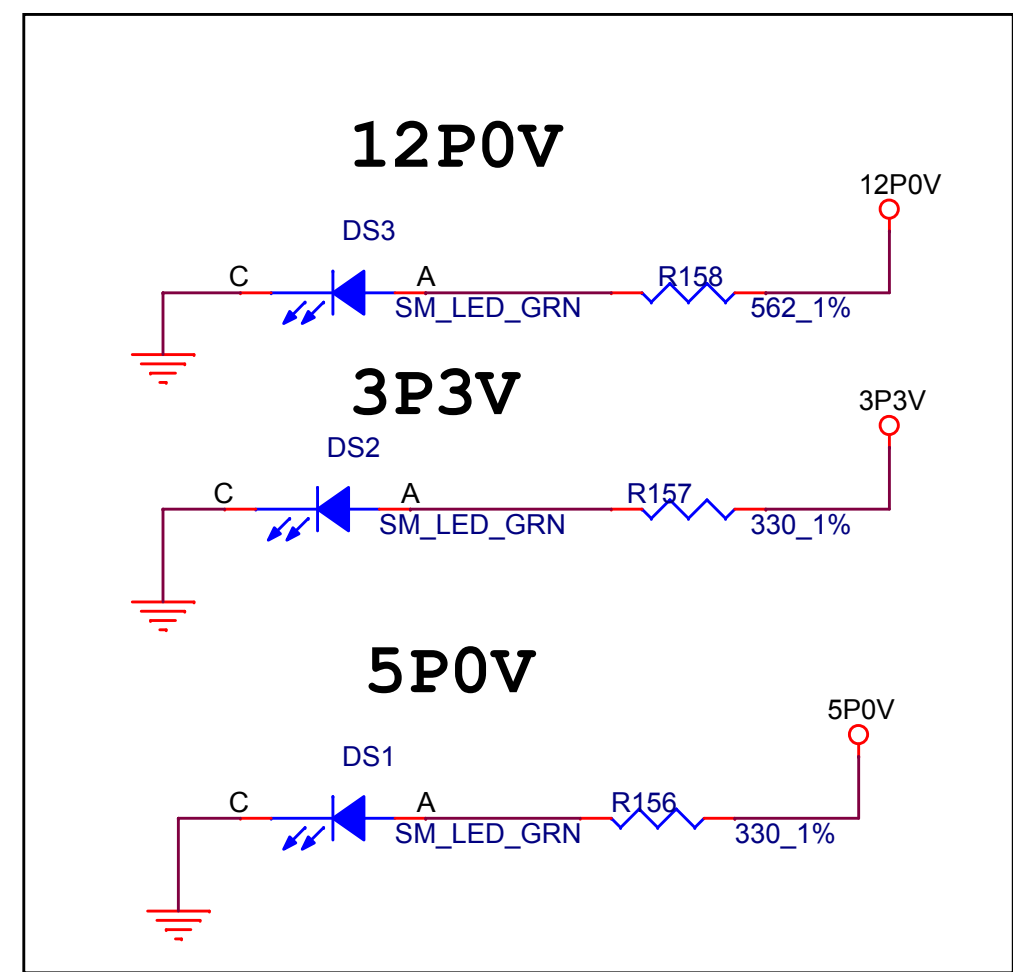
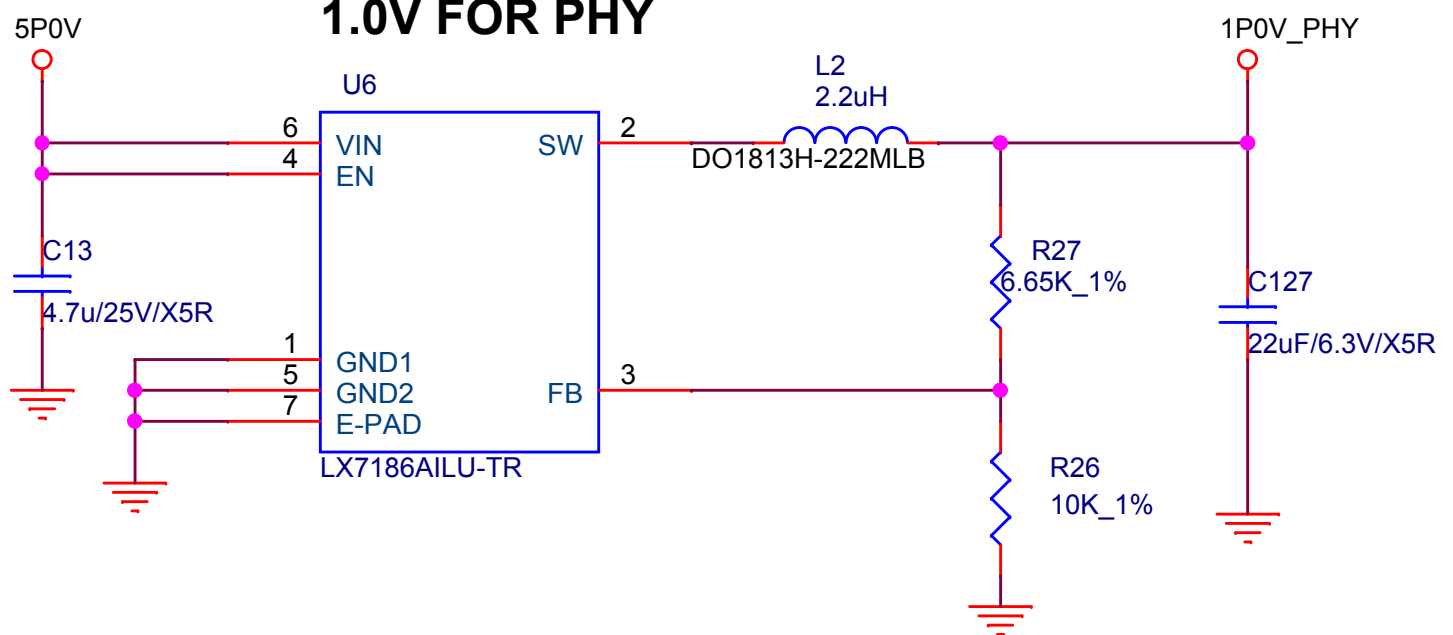
3.3V LDO



2.5V LDO



1.0V FOR PHY



TITLE		
M2GL/M2S-EVAL-KIT		
Microsemi		
SIZE	DOCUMENT NO.	REV
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