Libero SoC v11.5 SP2B Release Notes

Libero® System-on-Chip (SoC) is a comprehensive and powerful field programmable gate array (FPGA) design and development software offered by Microsemi®. The Libero SoC software provides start-to-finish design flow guidance and support for novice and experienced users alike. Libero SoC combines Microsemi SoC Products Group tools with such electronic design automation (EDA) powerhouses as Synplify Pro® and ModelSim®.

Libero SoC v11.5 SP2B delivers updates to timing data for the M2S090T device. Please note that this release only supports the following design parameters

- **Device**: M2S090T-FCS325
- **Operating System**: Linux

Contact your FAE to obtain more details on running the M2S090T device with the above parameters.

To access Datasheets and Silicon User Guides, visit www.microsemi.com. Any product can be selected and clicked to go to the **Documents** tab of that particular product. Tutorials, Application Notes, Development Kits and Starter Kits are listed in the **Design Resources** tab of each product.

For more information on new software features and enhancements, refer to the *Libero SoC Online Help*.

Contents

What's New in Libero SoC v11.5 SP2B

SmartFusion2 and IGLOO2 Timing Updates

Libero SoC v11.5 SP2B contains critical updates to timing models and timing data for the SmartFusion2 M2S090 device. Microsemi recommends that your existing SmartFusion2 designs are verified for timing using this release. Verifying timing can easily be done without re-running the entire project by simply importing your old Libero SoC project and running "Verify Timing". In most cases the project will keep the post Layout status.

Updating Your Design to Libero SoC v11.5 SP2B

As mentioned above, release 11.5 SP2B comes with updated timing models and data. The new models may trigger the appearance of new timing violations in your design. Some of these violations are false violations due to built-in retiming techniques. A tcl script : *coreconfigp_coreresetp_scenarios.tcl*
is provided with this release to address the false violations due to the new timing models by generating and adding the appropriate timing constraints to the existing design constraint database. The migration requires going through the following steps:

1. Install 11.5 SP2B
2. Open the project, make sure that it is in post-Layout state
3. Execute the tcl script (click Project -> Execute Script)
4. Run Verify Timing
5. Check the report for any remaining new violations

System Requirements

Refer to *System Requirements* on the web for more information regarding operating systems support and minimum system requirements. 64-bit OS is required for designing SmartFusion2 and IGLOO2 devices.

Setup Instructions for Linux OS can be found on the *Libero SoC Documents* webpage.

Supported Operating Systems

Windows 7, Windows 8.1

RHEL 5* and RHEL 6, CentOS 5* and CentOS 6

** RHEL 5 and CentOS 5 do not support programming using FlashPro5*

Discontinued

32-bit operating systems are no longer supported.

Windows XP is no longer supported.

Synopsys and Mentor Graphics Tools

These tools are included with the Libero SoC v11.5 installation.

Synplify Pro ME 2014.03M SP1 Release Notes

ModelSim ME 10.3c Release Notes

Identify ME 2014.09M-1 Release Notes

Synphony Model Compiler 2014.09M Release Notes

Prerequisite Software: In order to run Synphony Model Compiler ME, you must have *MATLAB/Simulink* by MathWorks installed with a current license. You cannot run Synphony Model Compiler ME without MATLAB/Simulink.

Download Libero SoC v11.5 SP2B

Installation requires Admin privileges.

- Linux

Libero SoC v11.5 SP2B is an incremental service pack and must be installed over Libero SoC v11.5.

Download Libero SoC v11.5

Installation requires Admin privileges.

- Linux

SoftConsole v3.4 SP1 should be installed over SoftConsole v3.4 for use with Libero SoC v11.5 & v11.5 SP2B.

SoftConsole 3.4 SP1

SoftConsole v3.4 requires a service pack to be compatible with Libero SoC v11.5.

Download *SoftConsole 3.4 SP1*

Product Support

Microsemi SoC Products Group backs its products with various support services, including Customer Service, Customer Technical Support Center, a website, electronic mail, and worldwide sales offices. This appendix contains information about contacting Microsemi SoC Products Group and using these support services.

Customer Service

Contact Customer Service for non-technical product support, such as product pricing, product upgrades, update information, order status, and authorization.

From North America, call **800.262.1060**
From the rest of the world, call **650.318.4460**
Fax, from anywhere in the world **650. 318.8044**

Customer Technical Support Center

Microsemi SoC Products Group staffs its Customer Technical Support Center with highly skilled engineers who can help answer your hardware, software, and design questions about Microsemi SoC Products. The Customer Technical Support Center spends a great deal of time creating application notes, answers to common design cycle questions, documentation of known issues and various FAQs. So, before you contact us, please visit our online resources. It is very likely we have already answered your questions.

Technical Support

For Microsemi SoC Products Support, visit http://www.microsemi.com/products/fpga-soc/design-support/fpga-soc-support.

Website

You can browse a variety of technical and non-technical information on the Microsemi SoC Products Group home page, at http://www.microsemi.com/soc/.

Contacting the Customer Technical Support Center

Highly skilled engineers staff the Technical Support Center. The Technical Support Center can be contacted by email or through the Microsemi SoC Products Group website.

Email

You can communicate your technical questions to our email address and receive answers back by email, fax, or phone. Also, if you have design problems, you can email your design files to receive assistance. We constantly monitor the email account throughout the day. When sending your request to us, please be sure to include your full name, company name, and your contact information for efficient processing of your request.

The technical support email address is soc_tech@microsemi.com.

My Cases

Microsemi SoC Products Group customers may submit and track technical cases online by going to My Cases.

Outside the U.S.

Customers needing assistance outside the US time zones can either contact technical support via email (soc_tech@microsemi.com) or contact a local sales office. Sales office listings can be found at www.microsemi.com/soc/company/contact/default.aspx.

ITAR Technical Support

For technical support on RH and RT FPGAs that are regulated by International Traffic in Arms Regulations (ITAR), contact us via soc_tech_itar@microsemi.com. Alternatively, within My Cases, select **Yes** in the ITAR drop-down list. For a complete list of ITAR-regulated Microsemi FPGAs, visit the ITAR web page.

Microsemi Corporate Headquarters
One Enterprise, Aliso Viejo,
CA 92656 USA

Within the USA: +1 (800) 713-4113
Outside the USA: +1 (949) 380-6100
Sales: +1 (949) 380-6136
Fax: +1 (949) 215-4996

E-mail: sales.support@microsemi.com

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