



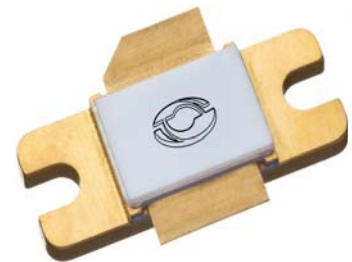
1214GN-400LV

400 Watts - 50 Volts, 4.5ms, 30%
Broad Band 1200 - 1400 MHz

GENERAL DESCRIPTION

The 1214GN-400LV is an internally matched, COMMON SOURCE, class AB GaN on SiC HEMT transistor capable of providing over 16dB gain, 400 Watts of pulsed RF output power at 4.5ms pulse width, 30% duty factor across the 1200 to 1400 MHz band. The transistor has internal pre-match for optimal performance. This hermetically sealed transistor is designed for L-Band Radar applications. It utilizes gold metallization and eutectic attach to provide highest reliability and superior ruggedness.

CASE OUTLINE 55-KR Common Source



ABSOLUTE MAXIMUM RATINGS

Maximum Power Dissipation

Device Dissipation @ 25°C 800 W

Maximum Voltage and Current

Drain-Source Voltage (V_{DSS}) 150 V

Gate-Source Voltage (V_{GS}) -8 to +0 V

Maximum Temperatures

Storage Temperature (T_{STG}) -55 to +125 °C

Operating Junction Temperature +250 °C

ELECTRICAL CHARACTERISTICS @ 25°C

Symbol	Characteristics	Test Conditions	Min	Typ	Max	Units
Pout	Output Power	Pout=400W, Freq=1200, 1300, 1400 MHz	400			W
Gp	Power Gain	Pout=400W, Freq=1200, 1300, 1400 MHz	16	16.8		dB
η_d	Drain Efficiency	Pout=400W, Freq=1200, 1300, 1400 MHz	60	68		%
Dr	Droop	Pout=400W, Freq=1200, 1300, 1400 MHz			0.6	dB
VSWR-T	Load Mismatch Tolerance	Pout=400W, Freq=1200 MHz			3:1	
Θ_{jc}	Thermal Resistance	Pulse Width=4.5mS, Duty=30%			0.3	°C/W

- Bias Condition: Vdd=+50V, Idq=200mA average current (Vgs= -2.0 ~ -4.5V) with constant gate Bias

FUNCTIONAL CHARACTERISTICS @ 25°C

$I_{D(Off)}$	Drain leakage current	$V_{gs} = -8V, V_D = 50V$			16.8	mA
$I_{G(Off)}$	Gate leakage current	$V_{gs} = -8V, V_D = 0V$			2	mA
BV_{DSS}	Drain-source breakdown voltage	$V_{gs} = -8V, I_D = 28mA$	150			V

Export Classification: EAR -99

For the most current data, consult MICROSEMI's website: www.MICROSEMI.com
Specifications are subject to change, consult the RFIS factory at (408) 986-8031 for the latest information

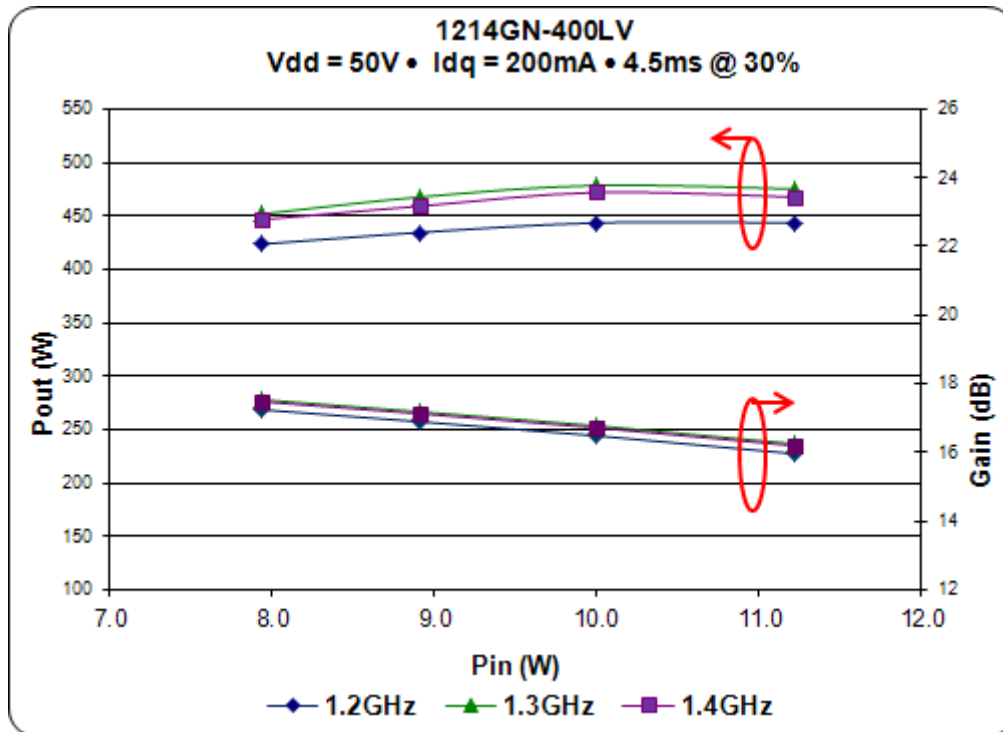


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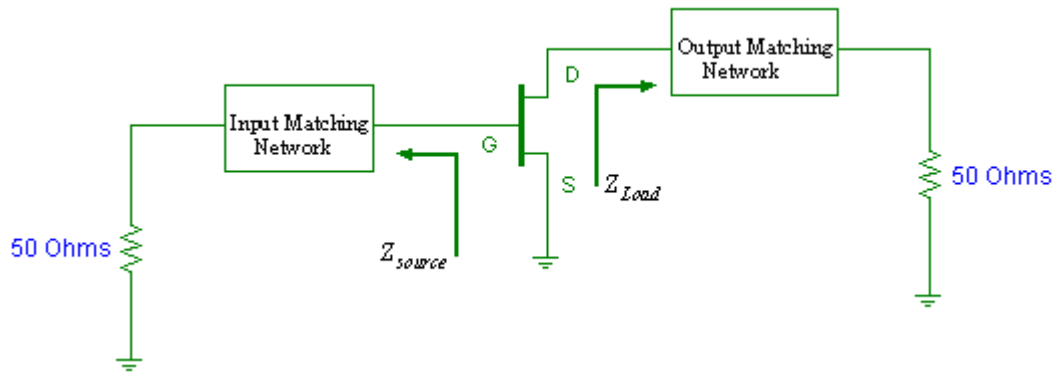
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Typical Performance Data

Freq(GH)	Pin (W)	Pout (W)	Id (A)	RL (dB)	Eff (%)	G (dB)	Droop (dB)
1.2	8.9	443	3.67	-8.1	72%	16.97	0.11
1.3	8.9	461	4.05	-19	68%	17.12	0.24
1.4	8.9	444	3.82	-7.8	68%	16.98	0.21



Transistor Impedance Information

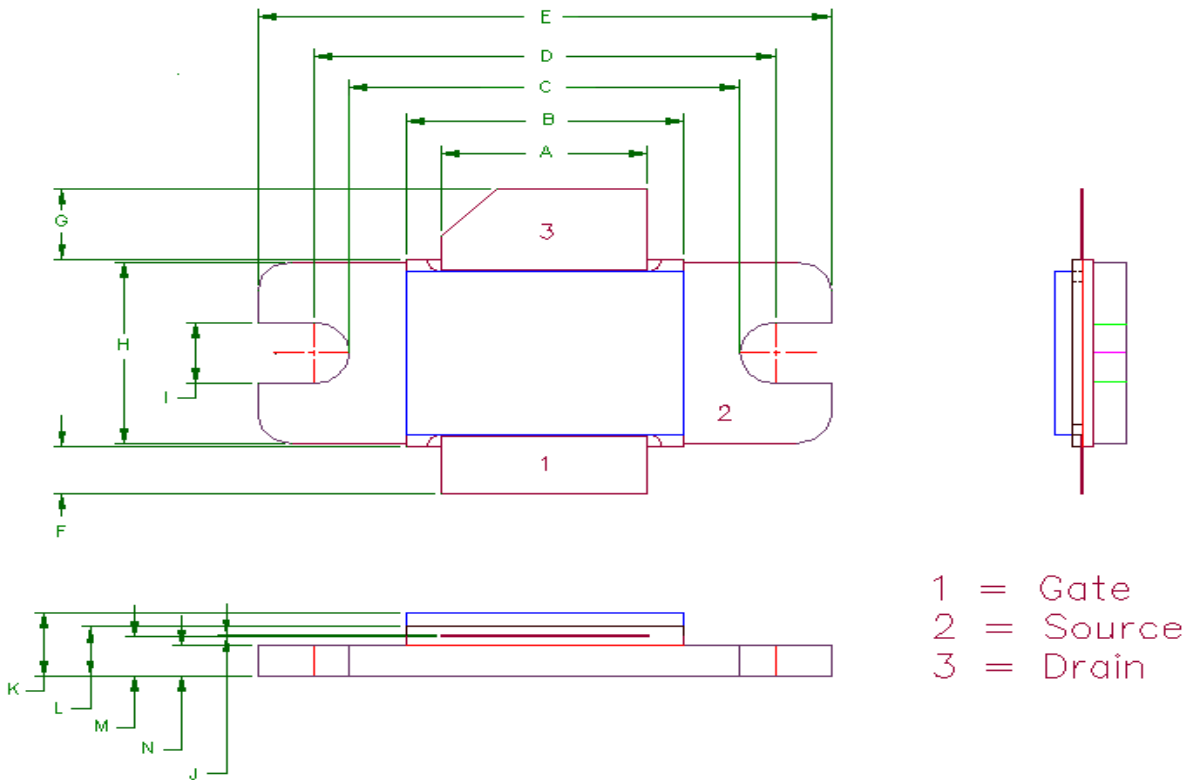


Note: Z_{in} is looking into the input circuit;
 Z_{Load} is looking into the output circuit.

Impedance Data		
Freq (GHz)	Zs	Zl
1.2	2.25-j1.89	0.36 - j0.19
1.3	2.15-j1.25	0.24 + j0.36
1.4	2.04-j0.61	0.18 + j0.9

Please contact us for the test circuit

55-KR PACKAGE DIMENSION



Dimension	Min (mil)	Min (mm)	Max (mil)	Max (mm)
A	370	9.40	372	9.44
B	498	12.65	500	12.7
C	700	17.78	702	17.83
D	830	21.08	832	21.13
E	1030	26.16	1032	26.21
F	86	2.18	116	2.946
G	136	3.45	166	4.22
H	385	9.78	387	9.83
I	130	3.30	132	3.35
J	003	.076	004	0.10
K	120	3.04	144	3.66
L	100	2.54	114	2.90
M	080	2.03	90	2.29
N	065	1.65	66	1.68



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Revision History

Revision Level / Date	Para. Affected	Description
0.2/ April 9, 2014	-	Initial Preliminary Release

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