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***Running the Secure Webserver on  
SmartFusion2 Devices Using PolarSSL,  
lwIP and FreeRTOS - Libero SoC v11.4***

***Demo Guide***

Superseded

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September 2014

## Revision History

<b>Date</b>	<b>Revision</b>	<b>Change</b>
16 September 2014	Revision 2	Second release
7 April 2014	Revision 1	First release

### Confidentiality Status

This is a non-confidential document.

Superseded

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## Preface

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### About this document

This demo is for SmartFusion<sup>®</sup>2 system-on-chip (SoC) field programmable gate array (FPGA) devices. It provides instructions on how to use the corresponding reference design.

### Intended Audience

The following designers using the SmartFusion2 devices:

- FPGA designers
- Embedded designers
- System-level designers

### References

The following references are used in this document:

- PolarSSL TLS/SSL protocol: <https://polarssl.org/>
- lwIP TCP/IP stack:
  - [www.sics.se/~adam/lwip/](http://www.sics.se/~adam/lwip/)
  - <http://download.savannah.gnu.org/releases/lwip/>
- FreeRTOS stack: [www.freertos.org](http://www.freertos.org)

### Microsemi Publications

- SmartFusion2 Microcontroller Subsystem User Guide
- SmartFusion2 SoC FPGA High Speed Serial Interfaces User Guide
- Libero SoC User Guide
- SmartFusion2 Advanced Development Kit User Guide

Refer to the following web page for a complete and up-to-date listing of SmartFusion2 device documentation: <http://www.microsemi.com/products/fpga-soc/soc-fpga/sf2docs>

# Running the Secure Webserver Demo Design on the SmartFusion2 Devices Using PolarSSL, lwIP and FreeRTOS

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## Introduction

This demo explains the Secure Webserver capabilities using transport layer security (TLS) and secure sockets layer (SSL) protocol and TSEMAC of the SmartFusion2 devices. This demo describes:

- Use of SmartFusion2 Ethernet MAC connected to a serial gigabit media independent interface (SGMII) PHY.
- Integration of SmartFusion2 MAC driver with the PolarSSL library (free TLS/SSL protocol library), lwIP TCP/IP stack and the FreeRTOS operating system.
- Use of Microsemi<sup>®</sup> cryptographic system services in the implementation of TLS/SSL protocol.
- Implementation of the Secure Webserver application on the SmartFusion2 Advanced Development Kit board.
- Procedure to run the demo.

The microcontroller subsystem (MSS) of the SmartFusion2 device has an instance of the TSEMAC peripheral. The TSEMAC can be configured between the host PC and the Ethernet network at the following data transfer rates (line speeds):

- 10 Mbps
- 100 Mbps
- 1000 Mbps

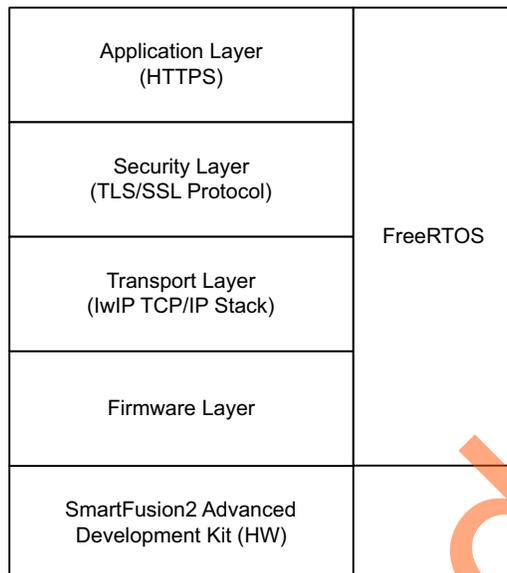
Refer to the [SmartFusion2 Microcontroller Subsystem User Guide](#) for more information on the TSEMAC interface for SmartFusion2 devices.

## Secure Webserver Demo Design Overview

The Secure Webserver application supports TLS/SSL security protocol that encrypts and decrypts the messages to secure the communication against message tampering. Communication from the Secure Webserver ensures that the sensitive data can be translated into a secret code that is difficult to tamper the data. The Secure Webserver demo design consists of the following layers:

- Application Layer
- Security Layer
- Transport Layer
- Firmware Layer

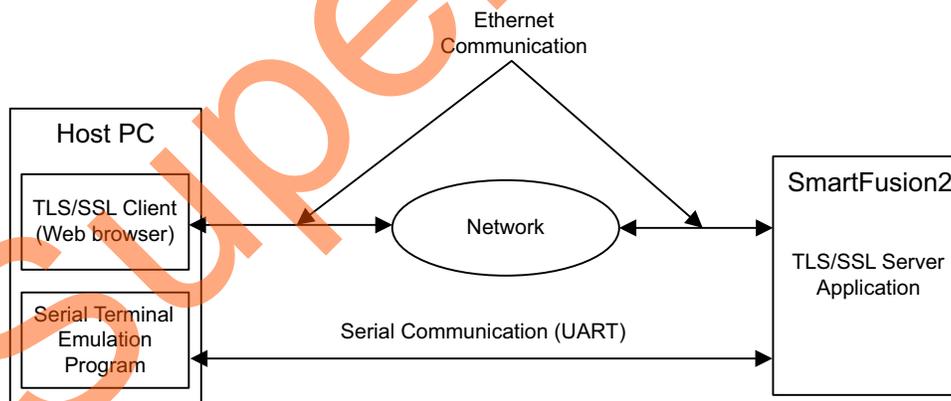
Figure 1 shows the block diagram of the Secure Webserver demo design.



**Figure 1 • Block Diagram of Secure Webserver Demo Design on SmartFusion2 Device**

### Application Layer

The Secure Webserver application is implemented on the SmartFusion2 Advanced Development Kit board. The application handles the HTTPS request from the client browser and transfers the static pages to the client in response to their requests. These pages run on the client (Host PC) browser. Figure 2 shows the block diagram of the connecting server (Secure Webserver application running on SmartFusion2 device) and client (web browser running on Host PC).



**Figure 2 • Client Server Communication Block Diagram**

When the URL with IP address (for example, `https://10.60.3.120`) is typed in the browser, the HTTPS request is sent to the port on the Secure Webserver. The Secure Webserver then interprets the request and responds to the client with the requested page or resource.

### **Security Layer (TLS/SSL Protocol)**

Internet browsers and Webservers use TLS/SSL protocol to transmit information securely. TLS/SSL protocol is used to authenticate the server and client to establish the secure communication between authenticated parties using encrypted messages. This protocol is layered above the transport protocol, TCP/IP as shown in [Figure 1 on page 6](#). This protocol provides privacy and reliability in data transfers between the client (internet browser) and the Webserver. An Open Source PolarSSL library is used to implement the TLS/SSL protocol for the Secure Webserver application in this demo.

Refer to the following URLs for complete TLS/SSL protocol implementation details:

Transport Layer Security protocol Version 1.2: <http://tools.ietf.org/html/rfc5246>

Transport Layer Security protocol Version 1.1: <http://tools.ietf.org/html/rfc4346>

Transport Layer Security protocol Version 1.0: <http://tools.ietf.org/html/rfc2246>

Secure Sockets Layer protocol Version 3.0: <http://tools.ietf.org/html/rfc6101>

The PolarSSL library includes the cryptographic and TLS/SSL protocol implementations. This library provides the application programming interface functions to implement Secure Webserver application using the TLS/SSL protocol and the software cryptographic algorithms.

Refer to <https://polarssl.org/> for TLS/SSL protocol library source code written in C and licensing information.

### **Transport Layer (lwIP TCP/IP Stack)**

The lwIP stack is suitable for the embedded systems because of less resource usage. It can be used with or without the operating system. The lwIP consists of the actual implementations of the IP, ICMP, UDP, and TCP protocols, as well as the support functions such as buffer and memory management.

For more information on the design and implementation, refer to [www.sics.se/~adam/lwIP/doc/lwIP.pdf](http://www.sics.se/~adam/lwIP/doc/lwIP.pdf).

The lwIP is available (under a BSD license) in C source-code format for download from the following address: <http://download.savannah.gnu.org/releases/lwIP/>

### **RTOS and Firmware Layer**

FreeRTOS™ is an open source real time operating system kernel. FreeRTOS is used in this demo to prioritize and schedule the tasks. Refer to <http://www.freertos.org> for more information and the latest source code.

The firmware provides the software driver implementation to configure and control the following MSS components:

- Ethernet MAC
- System controller services
- MMUART
- GPIO
- SPI

## Design Requirements

**Table 1 • Design Requirements**

Design Requirements	Description
<b>Hardware Requirements</b>	
SmartFusion2 Advanced Development Kit <ul style="list-style-type: none"><li>• 12 V adapter</li><li>• FlashPro5</li><li>• USB A to Mini-B cable</li></ul>	Rev B or later
RJ45 cable	–
Host PC or Laptop	Windows 64-bit Operating System
<b>Software Requirements</b>	
Liberio <sup>®</sup> System-on-Chip (SoC) for viewing the design files	v11.4
FlashPro Programming Software	v11.4
SoftConsole	v3.4 SP1
Host PC Drivers	<a href="#">USB to UART drivers</a>
One of the following serial terminal emulation programs: <ul style="list-style-type: none"><li>• HyperTerminal</li><li>• TeraTerm</li><li>• PuTTY</li></ul>	–
Browser	Mozilla Firefox version 24 or later Internet Explorer version 8 or later

## Demo Design

### Introduction

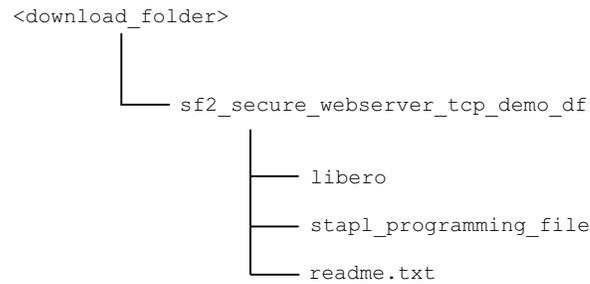
The demo design files are available for download from the Microsemi<sup>®</sup> website:

[http://soc.microsemi.com/download/rsc/?f=sf2\\_running\\_secure\\_webserver\\_using\\_polarssl\\_lwip\\_and\\_freertos\\_liberov11p4\\_dg\\_df](http://soc.microsemi.com/download/rsc/?f=sf2_running_secure_webserver_using_polarssl_lwip_and_freertos_liberov11p4_dg_df)

The demo design files include:

- The Liberio SoC hardware project with SoftConsole firmware project
- STAPL programming file
- readme.txt file

Figure 3 shows the top-level structure of the design files. For further details, refer to the readme.txt file.



**Figure 3 • Demo Design Files Top-Level Structure**

## Demo Design Features

The demo has the following options:

- Blinking LEDs
- HyperTerminal Display
- SmartFusion2 Google Search

## Demo Design Description

The demo design is implemented using an SGMII PHY interface by configuring the TSEMAC for the ten-bit interface (TBI) operation. For more information on the TSEMAC TBI interface, refer to the [SmartFusion2 Microcontroller Subsystem User Guide](#).

The demo design comprises:

- Libero SoC Hardware Project
- SoftConsole Firmware Project

### Libero SoC Hardware Project

Figure 4 shows the Libero SoC hardware design implementation for this demo design.

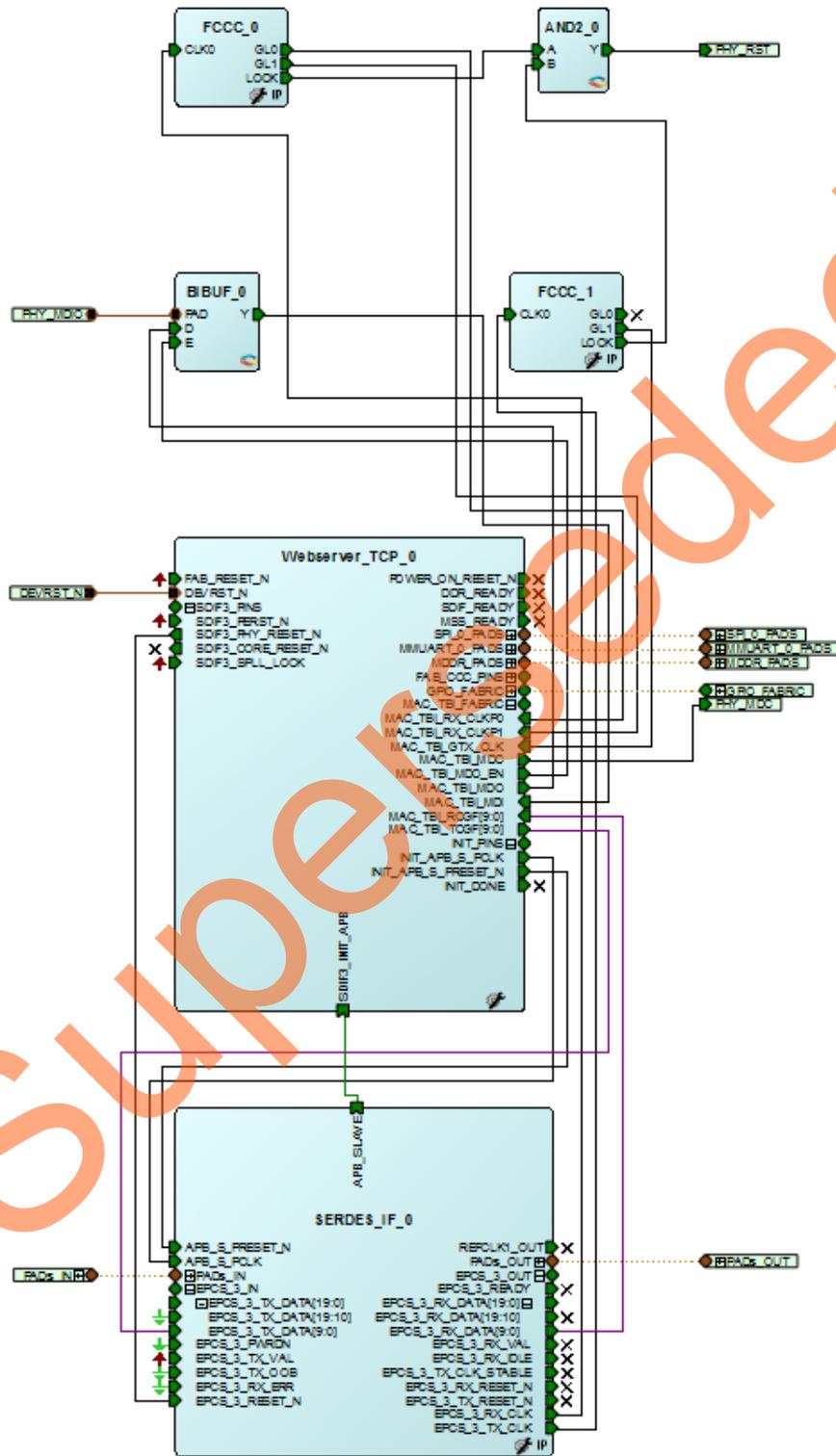


Figure 4 • Libero Top-Level Design

The Libero Hardware project uses the following SmartFusion2 MSS resources and IPs:

1. **TSEMAC TBI** interface.
2. **MMUART\_0** for RS-232 communications on the Advanced Development Kit.
3. General purpose input and output (GPIO): Interfaces with the light emitting diodes (LEDs).
4. High speed serial interface (SERDESIF) **SERDES\_IF IP**, configured for **SERDESIF\_3 EPCS lane3** as shown in [Figure 5](#).

For more information on high speed serial interfaces, refer to the [SmartFusion2 SoC FPGA High Speed Serial Interfaces User Guide](#).

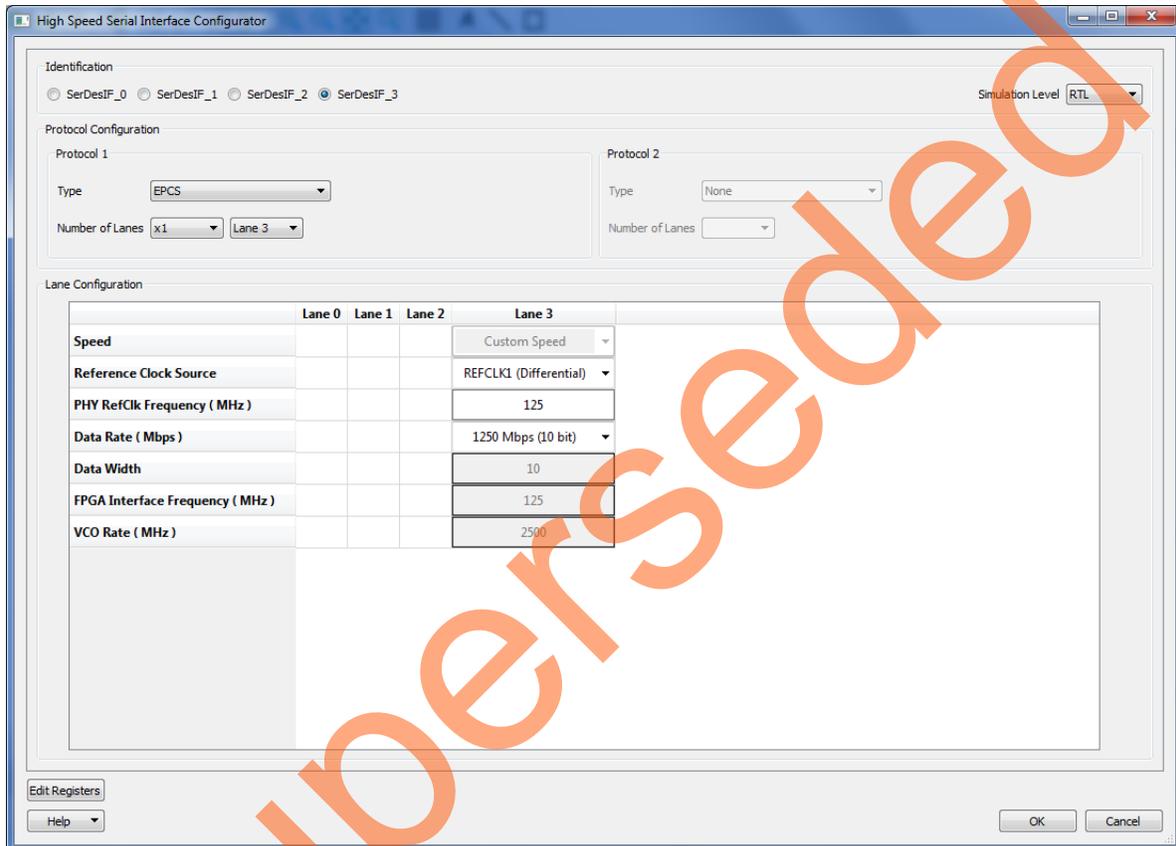


Figure 5 • High Speed Serial Interface Configurator Window

5. Cryptographic system controller services: To implement TLS/SSL protocol.

### Package Pin Assignments

Package pin assignments for LEDs and PHY interface signals are shown in [Table 2](#) and [Table 3](#).

[Table 2](#) shows the port names for the package pins.

**Table 2 • LED to Package Pins Assignments**

Port Name	Package Pin
LED_1	D26
LED_2	F26
LED_3	F27
LED_4	C26
LED_5	C28
LED_6	B27
LED_7	C27
LED_8	E26

[Table 3](#) shows the port names and directions for the package pins.

**Table 3 • PHY Interface Signals to Package Pins Assignments**

Port Name	Direction	Package Pin
PHY_MDC	Output	F3
PHY_MDIO	Input	K7
PHY_RST	Output	F2

### SoftConsole Firmware Project

Invoke the SoftConsole project using the **Write Application Code** option available under **Develop Firmware** in the Libero SoC **Design Flow** window. Refer to the [Libero SoC User Guide](#) for more information.

The following stacks are used for this demo design:

- **PolarSSL** library version 1.2.8 (<https://polarssl.org/>)
- **lwIP TCP/IP** stack version 1.4.1 ([www.sics.se/~adam/lwIP/](http://www.sics.se/~adam/lwIP/))
- **FreeRTOS** ([www.freertos.org](http://www.freertos.org))

[Figure 1](#) on [page 6](#) shows the block diagram of the Secure Webserver application on the SmartFusion2 devices used in this demo design.

Figure 6 shows an example SoftConsole software directory structure of the demo design.

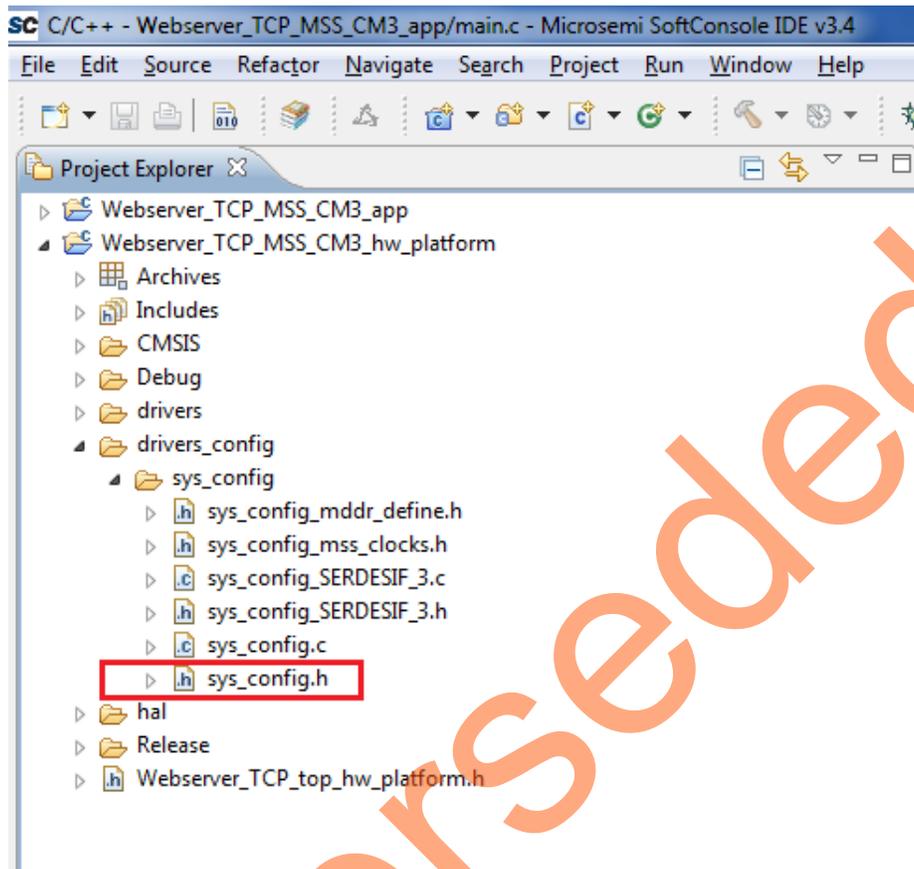


Figure 6 • Example SoftConsole Project Explorer Window

The SoftConsole workspace consists of two projects.

1. Webserver\_TCP\_MSS\_CM3\_app

This project contains the Secure Webserver application implementation using PolarSSL, LWIP, and FreeRTOS.

The advanced encryption standard (AES) and non-deterministic random bit generator (NRBG) system services are used to implement the Secure Webserver application. The following macros need to be commented for using the PolarSSL AES and software NRBG algorithms.

**Table 4 • Macros to Enable or Disable System Controller Services**

System Service	Macro	Macro Location
AES	#define HW_AES 1	<download_folder>\sf2_secure_webserver_tcp_demo_df\libero\SoftConsole\Webserver_TCP_MSS_CM3\Webserver_TCP_MSS_CM3_app\polarssl-1.2.8\include\polarssl\aes.h
NRBG	#define HW_NRBG 1	<download_folder>\sf2_secure_webserver_tcp_demo_df\libero\SoftConsole\Webserver_TCP_MSS_CM3\Webserver_TCP_MSS_CM3_app\polarssl-1.2.8\include\polarssl\ssl.h

**Note:** The system services AES and NRBG are supported for data security enabled SmartFusion2 device like M2S0150TS. If the SmartFusion2 device is not data security enabled, disable the macros mentioned in Table 4.

2. Webserver\_TCP\_MSS\_CM3\_hw\_platform

This project contains all the firmware and hardware abstraction layers that correspond to the hardware design. This project is configured as a library and is referenced by the Webserver\_TCP\_MSS\_CM3\_app application project. The contents of this folder get over-written by regenerating the root design every time and exporting the SoftConsole firmware project in the Libero SoC software.

### TLS/SSL Protocol Implementation using PolarSSL Library

The TLS/SSL protocol is divided into the following two protocol layers:

- Handshake protocol layer
- Record protocol layer

#### Handshake Protocol Layer

This layer consists of the following sub protocols:

- **Handshake:** Used to negotiate session information between the server and the client. The session information includes session ID, peer certificates, the cipher spec, the compression algorithm, and a shared secret code that is used to generate required keys.
- **Change Cipher spec:** Used to change the key used for encryption between the client and the server. The key is computed from the information exchanged during the client-server handshake.
- **Alert:** Alert messages are generated during the client-server handshake to report an error or a change in status to the peer.

Figure 7 shows the overview of the TLS/SSL handshake procedure. Refer to <http://tools.ietf.org/html/rfc5246> for detailed information on handshake protocol, record protocol and cryptographic algorithms.

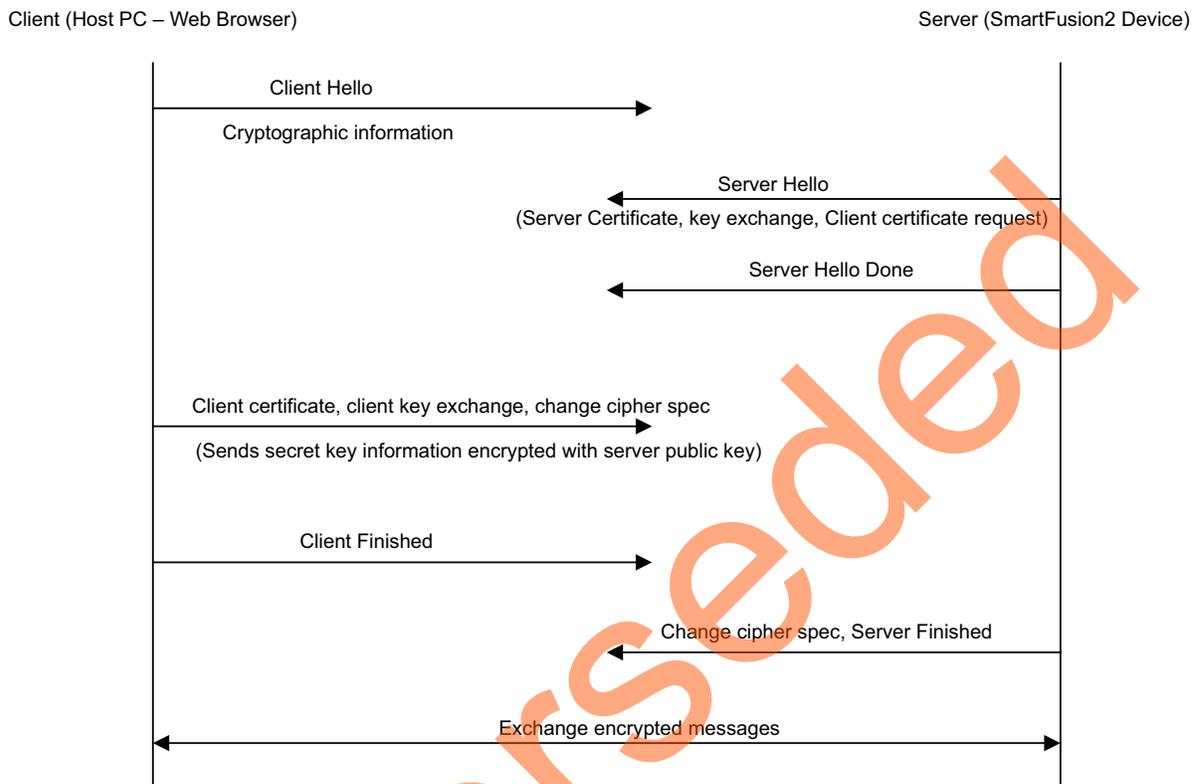


Figure 7 • TLS/SSL Handshake Procedure

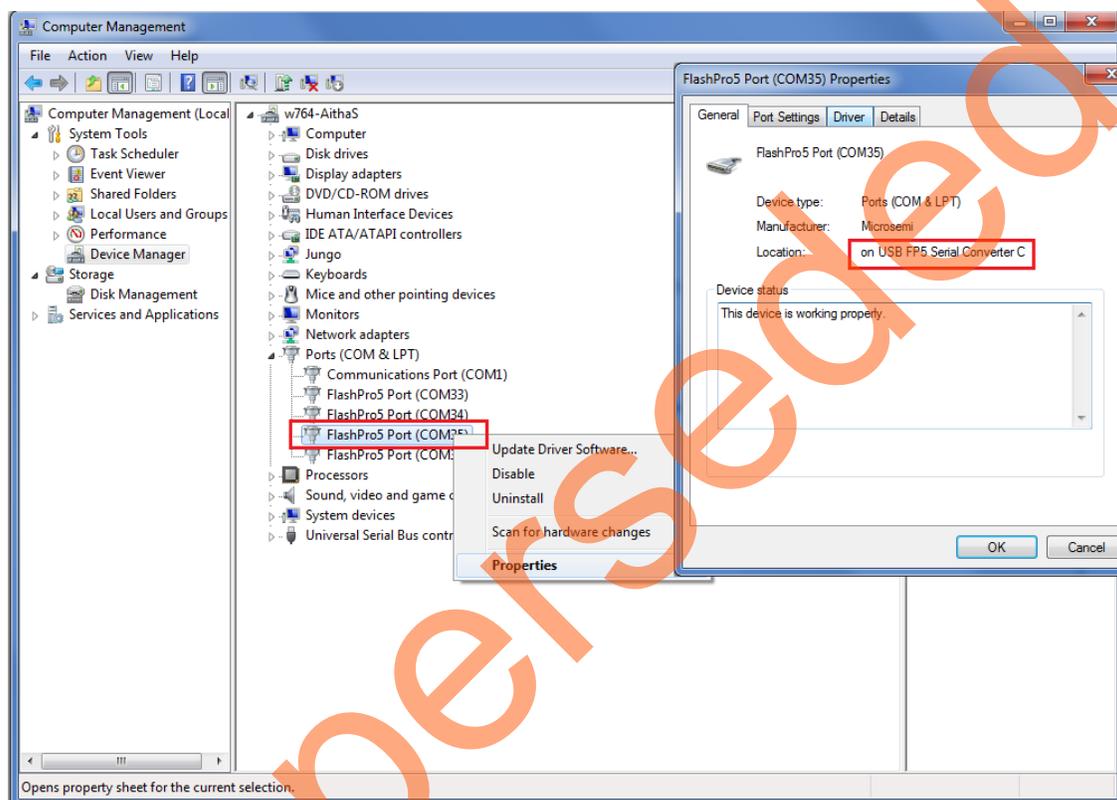
**Record Protocol Layer**

The record protocol receives and encrypts data from the application and transfers to the transport layer. The record protocol fragments the received data to a size appropriate to the cryptographic algorithm and optionally compresses the data. The protocol applies a MAC or HMAC and encrypts or decrypts the data using the information negotiated during the handshake protocol.

## Setting Up the Demo Design

The following steps describe how to setup the demo for SmartFusion2 Advanced Development Kit board:

1. Connect the Host PC to the J33 Connector using the USB A to mini-B cable. The USB to UART bridge drivers are automatically detected.
2. From the detected four COM ports, select the one which location on its Properties window must be as **on USB FP5 Serial Converter C**. Make a note of the COM port number for serial port configuration and ensure that the COM port Location is specified as **on USB FP5 Serial Converter C**, as shown in [Figure 8](#).



**Figure 8 • Device Manager Window**

3. If USB drivers are not detected automatically, install the USB driver.
4. For serial terminal communication through the FTDI mini USB cable, install the FTDI D2XX driver. Download the drivers and installation guide from:  
[www.microsemi.com/soc/documents/CDM\\_2.08.24\\_WHQL\\_Certified.zip](http://www.microsemi.com/soc/documents/CDM_2.08.24_WHQL_Certified.zip)

- Connect the jumpers on the SmartFusion2 Advanced Development Kit board as shown in [Table 5](#). For information on jumper locations, refer to "[Appendix 2: Jumper Locations](#)" on page 29.  
**Caution:** Before making the jumper connections, switch off the power supply switch, SW7.

**Table 5 • SmartFusion2 FPGA Advanced Kit Jumper Settings**

Jumper	Pin (from)	Pin (to)	Comments
J116, J353, J354, J54	1	2	These are the default jumper settings of the Advanced Dev Kit Board. Make sure these jumpers are set accordingly.
J123	2	3	
J124, J121, J32	1	2	JTAG programming via FTDI
J118, J119	1	2	Programming SPI Flash

- In the SmartFusion2 Advanced Development Kit, connect the power supply to the J42 connector.
- This design example can run in both Static IP and Dynamic IP modes. By default, programming files are provided for dynamic IP mode.
  - For static IP, connect the host PC to the J21 connector of the SmartFusion2 Advanced Development Kit Board using an RJ45 cable.
  - For dynamic IP, connect any one of the open network ports to the J21 connector of the SmartFusion2 Advanced Development Kit Board using an RJ45 cable.

## Board Setup Snapshot

Snapshots of the SmartFusion2 Advanced Development Kit board with all the setup made is given in "[Appendix 1: Board Setup for Running the Secure Webserver](#)" on page 28.

## Running the Demo Design

- Download the demo design from:  
[http://soc.microsemi.com/download/rsc/?f=sf2\\_running\\_secure\\_webserver\\_using\\_polarssl\\_lwip\\_and\\_freertos\\_liberov11p4\\_dg\\_df](http://soc.microsemi.com/download/rsc/?f=sf2_running_secure_webserver_using_polarssl_lwip_and_freertos_liberov11p4_dg_df)
- Switch ON the SW7 power supply switch.
- Start any serial terminal emulation program such as:
  - HyperTerminal
  - PuTTY
  - Tera Term

Note: In this demo PuTTY is used.

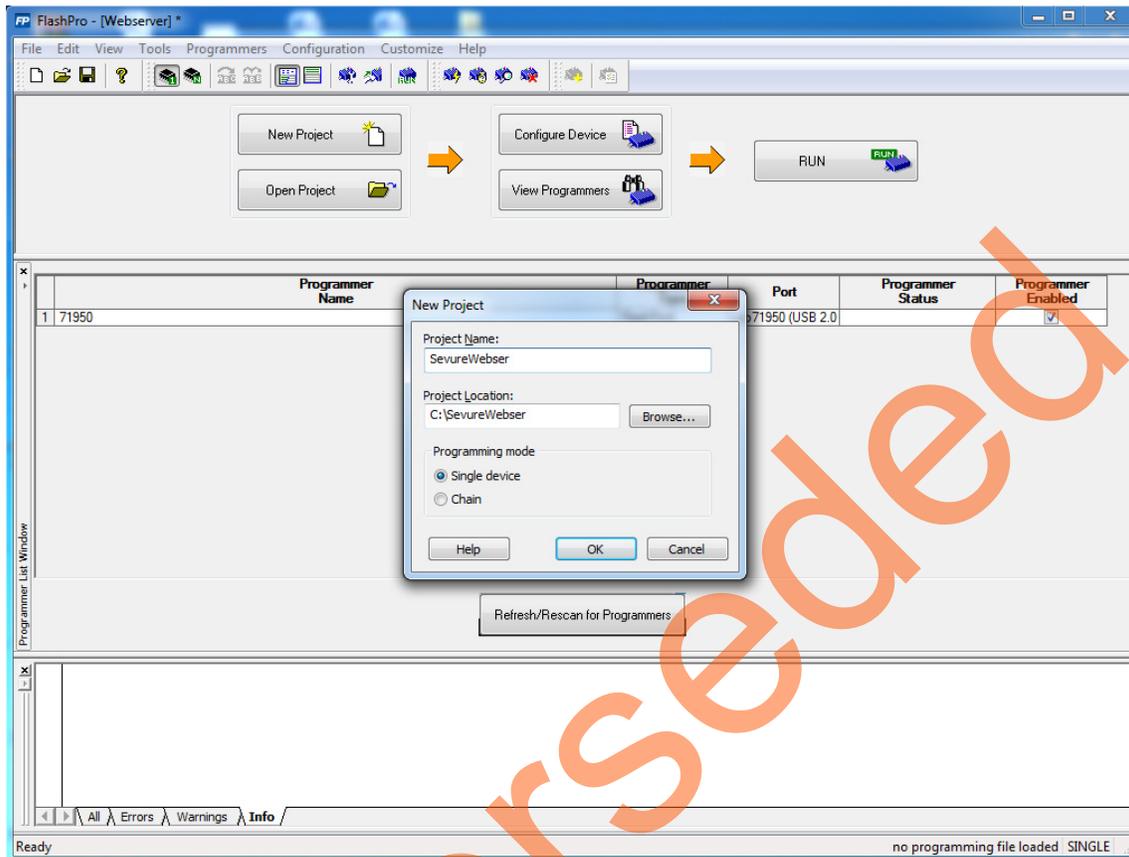
The configuration for the program is:

- Baud Rate: 115200
- Eight data bits
- One stop bit
- No Parity
- No flow control

For information on configuring the serial terminal emulation programs, refer to the [Configuring Serial Terminal Emulation Programs Tutorial](#).

- Launch the **FlashPro** software.
- Click **New Project**.

6. In the **New Project** window, type the project name as shown in Figure 9.



**Figure 9 • FlashPro New Project**

7. Click **Browse** and navigate to the location where the project is required to be saved.
8. Select **Single device** as the **Programming mode**.
9. Click **OK** to save the project.
10. Click **Configure Device**.

- Click **Browse** and navigate to the location where the `secure_Webserver_tcp_top.stp` file is located and select the file. The default location is: `<download_folder>\sf2_secure_Webserver_tcp_demo_df\stapl_programming_file\Webserver_tcp_top_Secure_Demo.stp`

The required programming file is selected and is ready to be programmed in the device as shown in Figure 10.

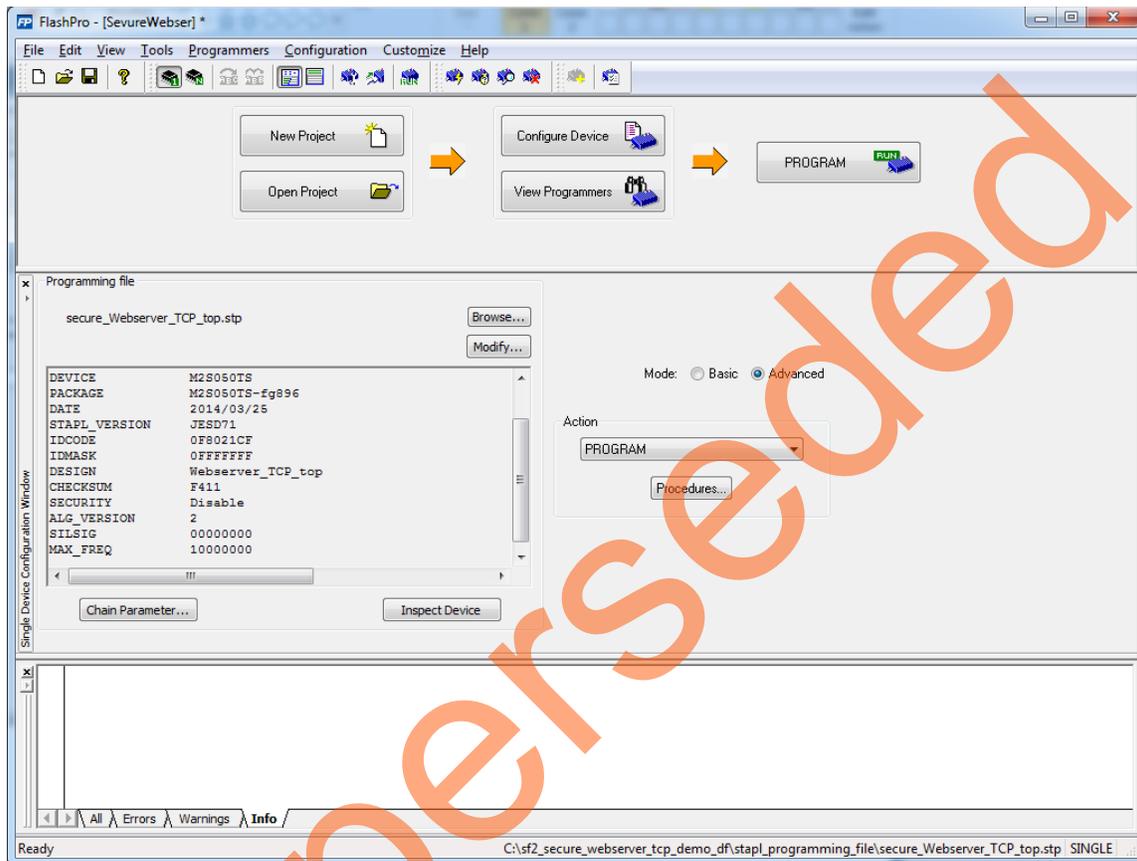
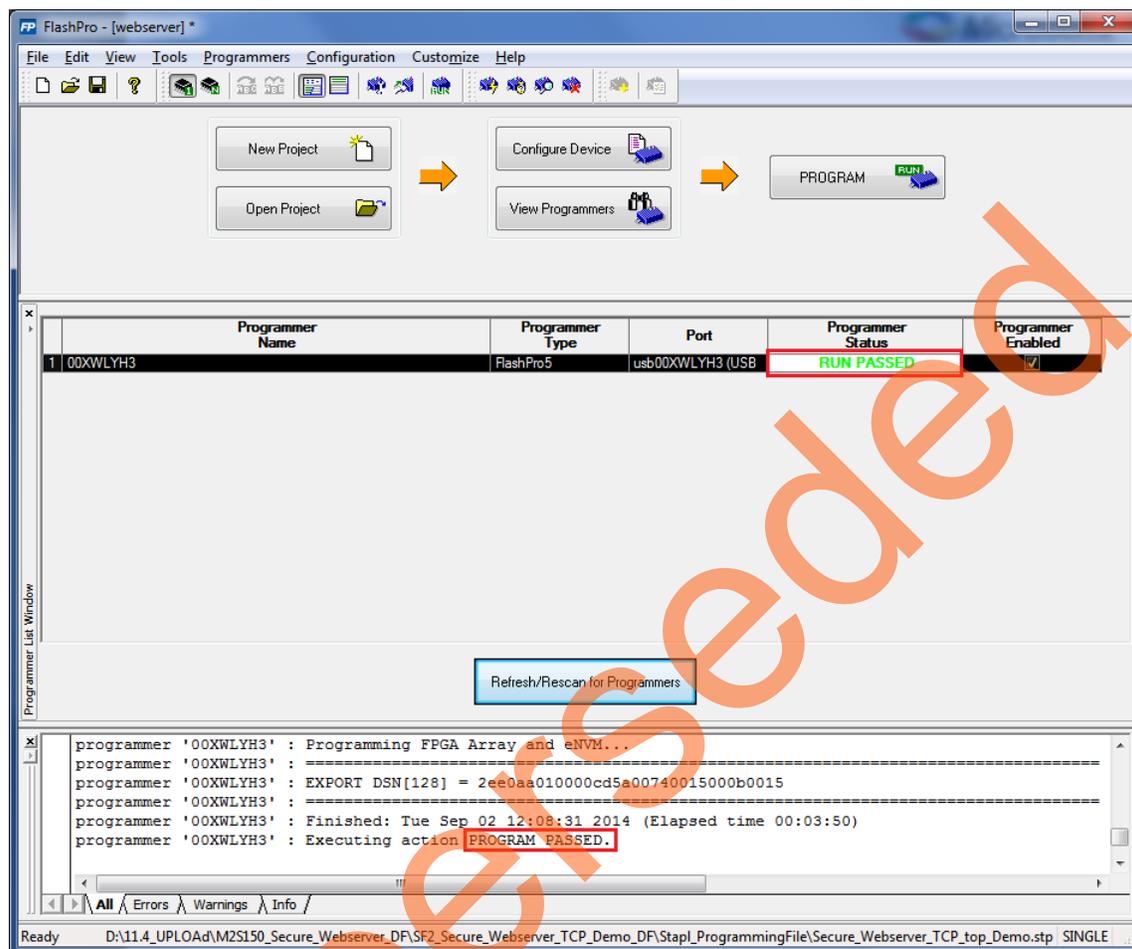


Figure 10 • FlashPro Project Configured

- Click **PROGRAM** to start programming the device. Wait until a message is displayed, indicating that the program has passed.



**Figure 11 • FlashPro Program Passed**

**Note:** The demo can be run in static and dynamic modes. To run the design in Static IP mode, follow the steps mentioned in the "Appendix 3: Running the Design in Static IP Mode" on page 30.

13. Power cycle the SmartFusion2 Advanced Development Kit board.

A welcome message with the dynamic IP address is displayed in the serial terminal emulation program as shown in Figure 12.

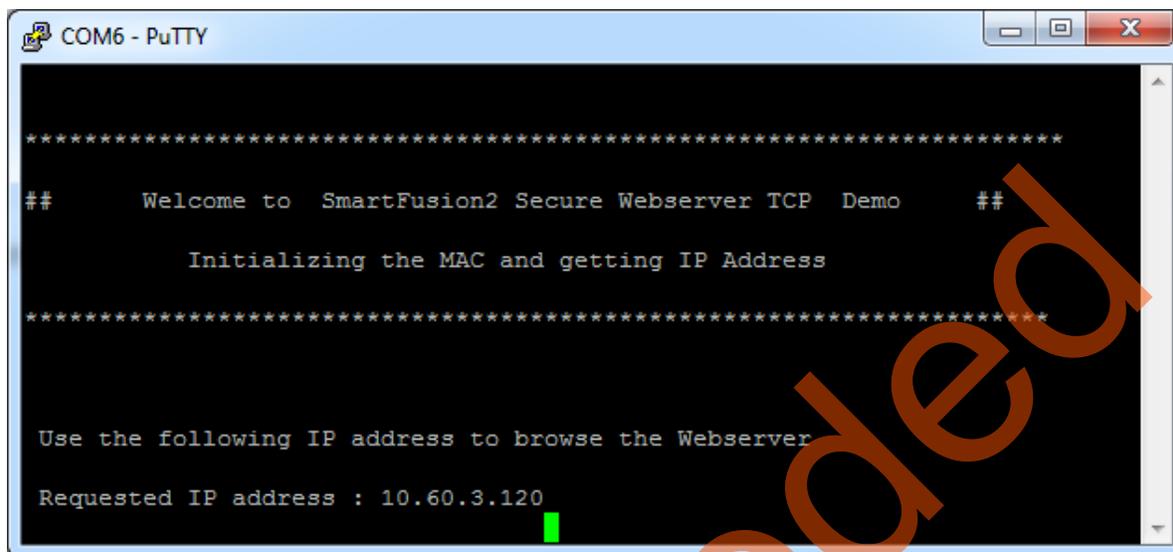


Figure 12 • User Options

14. The IP address displayed on PuTTY should be entered in the address bar of the browser to run the Secure Webserver. If the IP address is 10.60.3.120, type https://10.60.3.120 in the address bar of the browser. This demo supports both Microsoft Internet Explorer and Mozilla Firefox browsers.

## Running the Secure Webserver Demo with Microsoft Internet Explorer

1. Open the Microsoft Internet Explorer and type the URL (for example, <https://10.60.3.120>) in the address bar. The browser shows a warning message as shown in Figure 13.

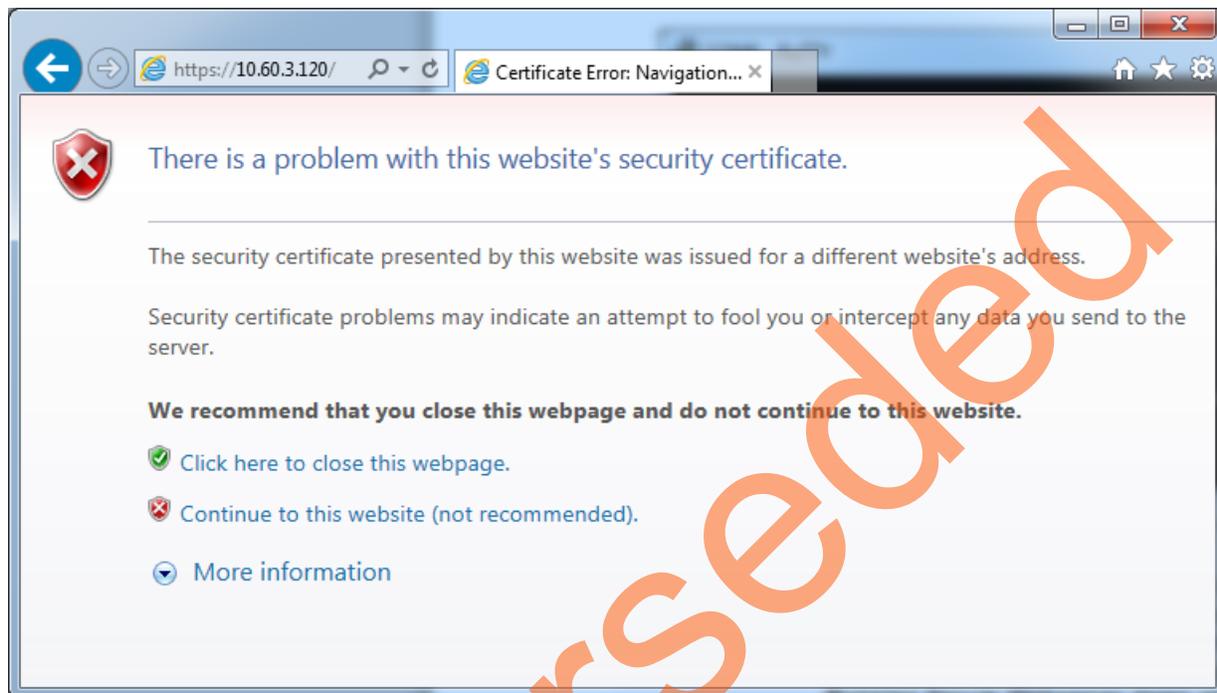


Figure 13 • Microsoft Internet Explorer showing Certificate Error Warning Message

2. Click **Continue to this website (not recommended)** to start secure communication with the Webserver. The Microsoft Internet Explorer displays the main menu of the Secure Webserver as shown in Figure 14.

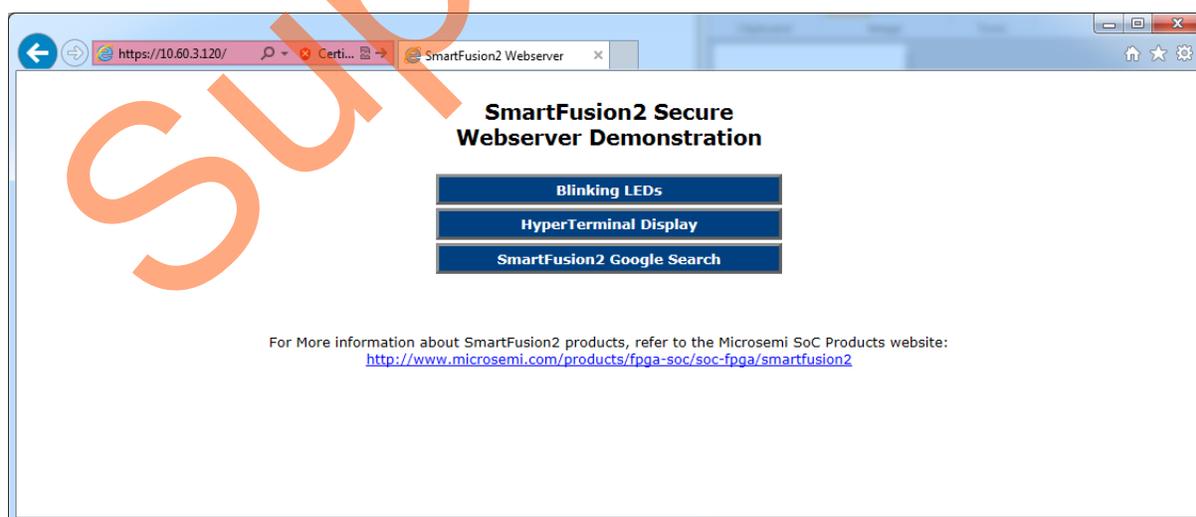


Figure 14 • Main Menu of Secure Webserver in Internet Explorer

## Running the Secure Webserver Demo with Mozilla Firefox

1. Open the Mozilla Firefox browser and type the URL (for example, <https://10.60.3.120>) in the address bar. The browser shows a warning message as shown in Figure 15.

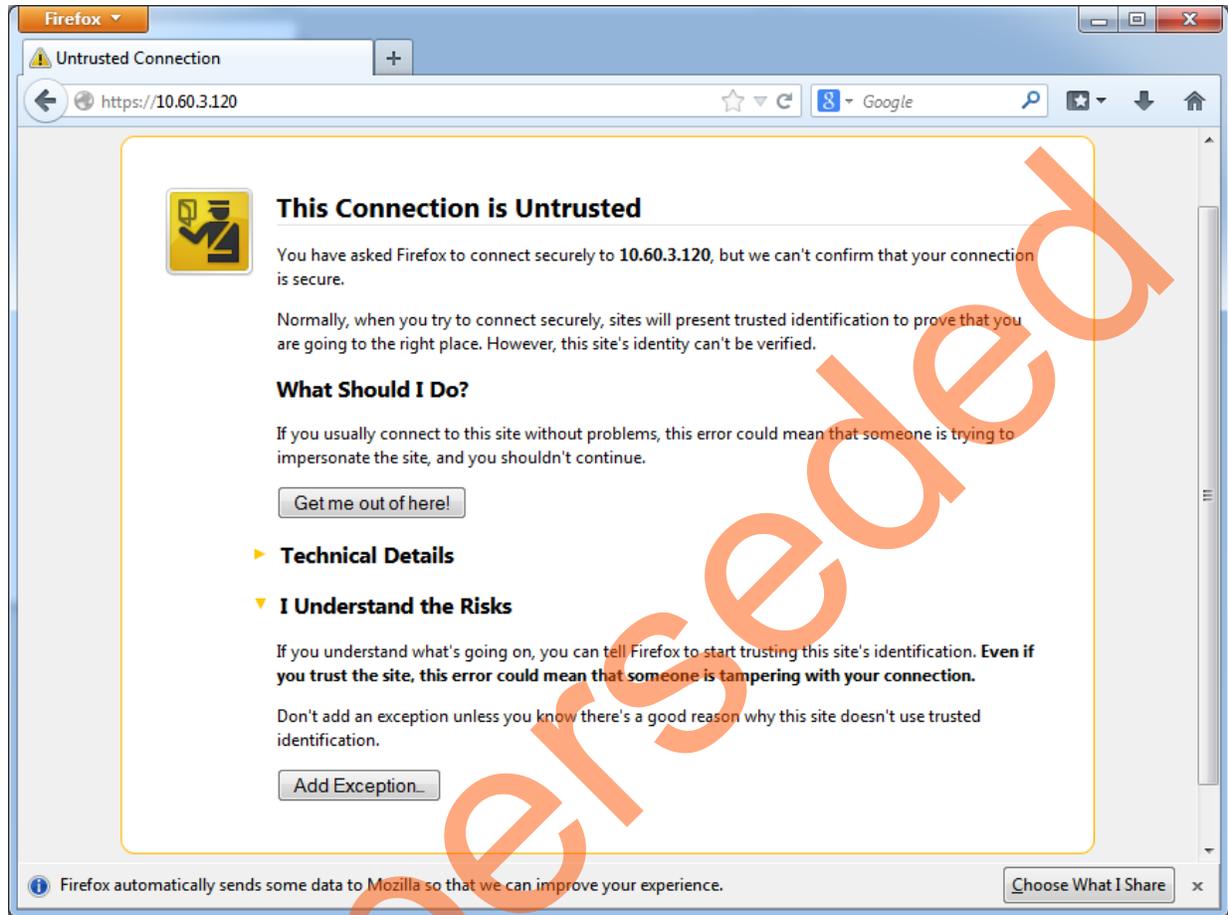
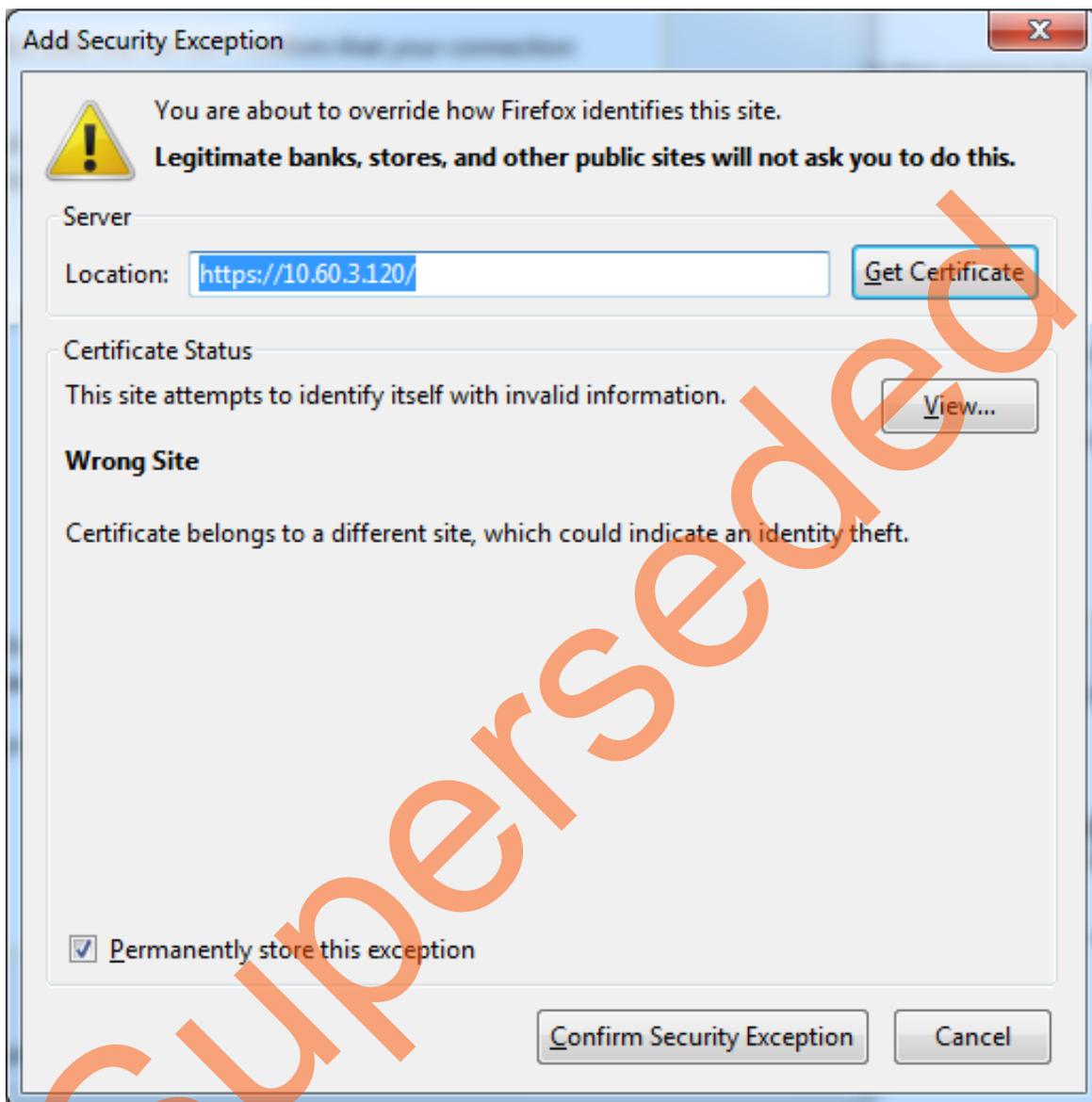


Figure 15 • Mozilla Firefox showing Warning Message

2. Select **I Understand the Risks** and click **Add Exception...**

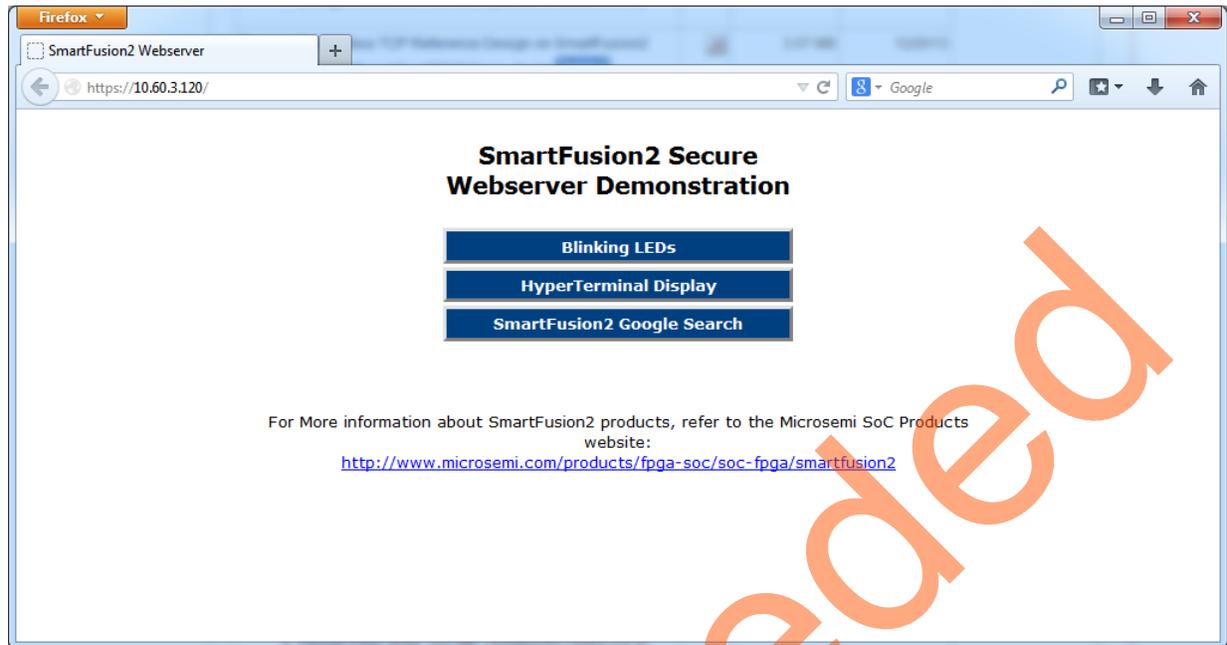
3. Click **Confirm Security Exception** in **Add Security Exception** window as shown in Figure 16, to start secure communication with the Webserver.



**Figure 16 • Add Security Exception Window**

**Note:** Adding security exception for the IP Address is required for first-time browsing only.

The Mozilla Firefox browser displays the main menu as shown in Figure 17.



**Figure 17 • Main Menu of the Secure Webserver in Mozilla Firefox**

The main menu has the following options:

- Blinking LEDs
- HyperTerminal Display
- SmartFusion2 Google Search

**Note:** These options can be verified using either Microsoft Internet Explorer or Mozilla Firefox web browsers. In this demo, the options are demonstrated using Mozilla Firefox web browser.

## Blinking LEDs

1. Click **Blinking LED's** on the main menu. You can observe a running LED pattern on the SmartFusion2 board. The webpage gives an option to enter the values to blink the LEDs manually as shown in Figure 18.

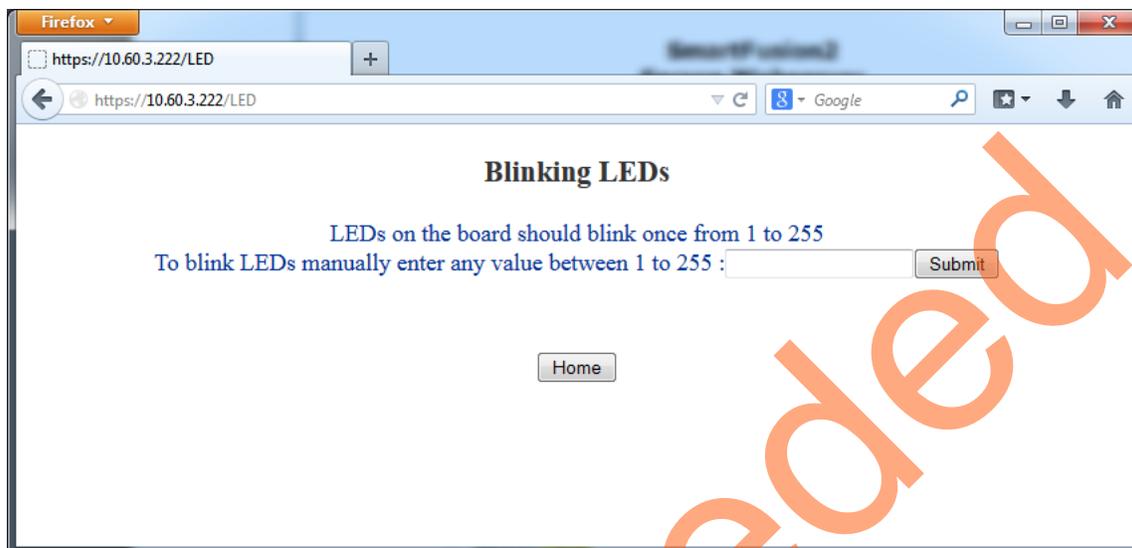


Figure 18 • Blinking LEDs Page

2. Enter any number between 1-255 to lit the LEDs manually. For example, if you enter 1, blinking LED1 goes OFF. If you enter 255, all the eight blinking LEDs go OFF.
3. Click **Home** to go back to the main menu.

**Note:** SmartFusion2 Advanced Development Kit has Active Low LEDs.

## HyperTerminal Display

1. Click **HyperTerminal Display** on the main menu. Figure 19 shows a webpage that gives an option to enter a string value.

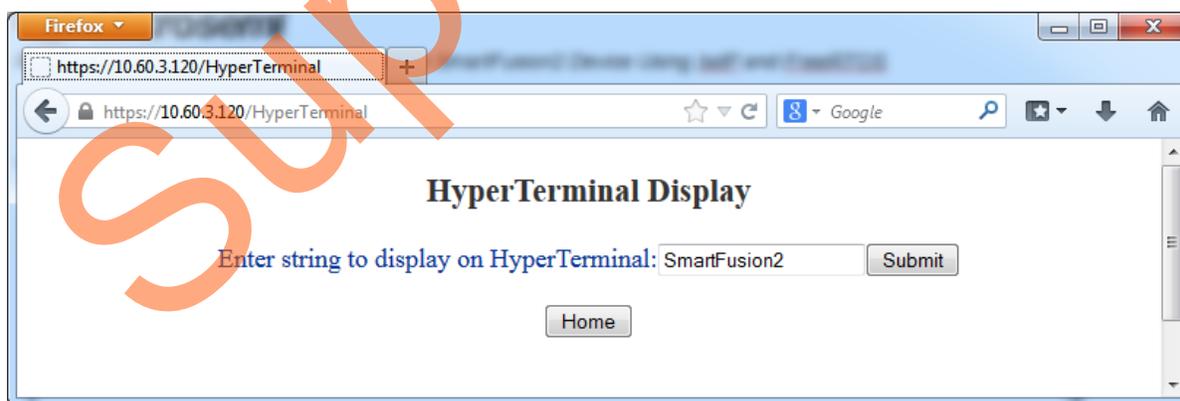


Figure 19 • HyperTerminal Display Page

The entered string is displayed on PuTTY as shown in Figure 20.

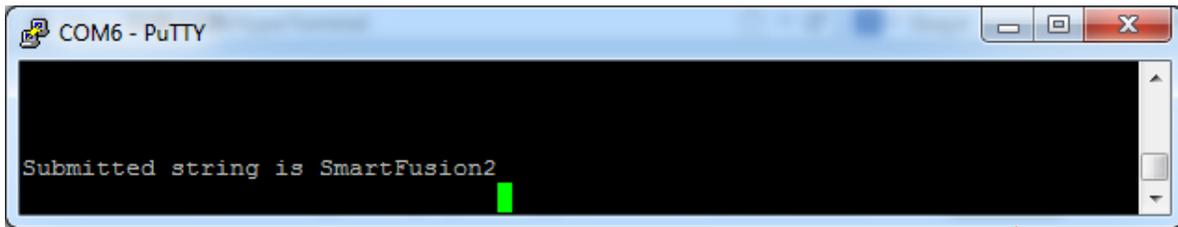


Figure 20 • String Display on PuTTY

2. Click **Go Back One Page** (arrow button) or **Home** to go back to the main menu.

### SmartFusion2 Google Search

1. Click **SmartFusion2 Google Search** on the main menu.

**Note:** Internet connection is required with proper access rights to get to the SmartFusion2 Google Search page.

Figure 21 shows a webpage with Google search.

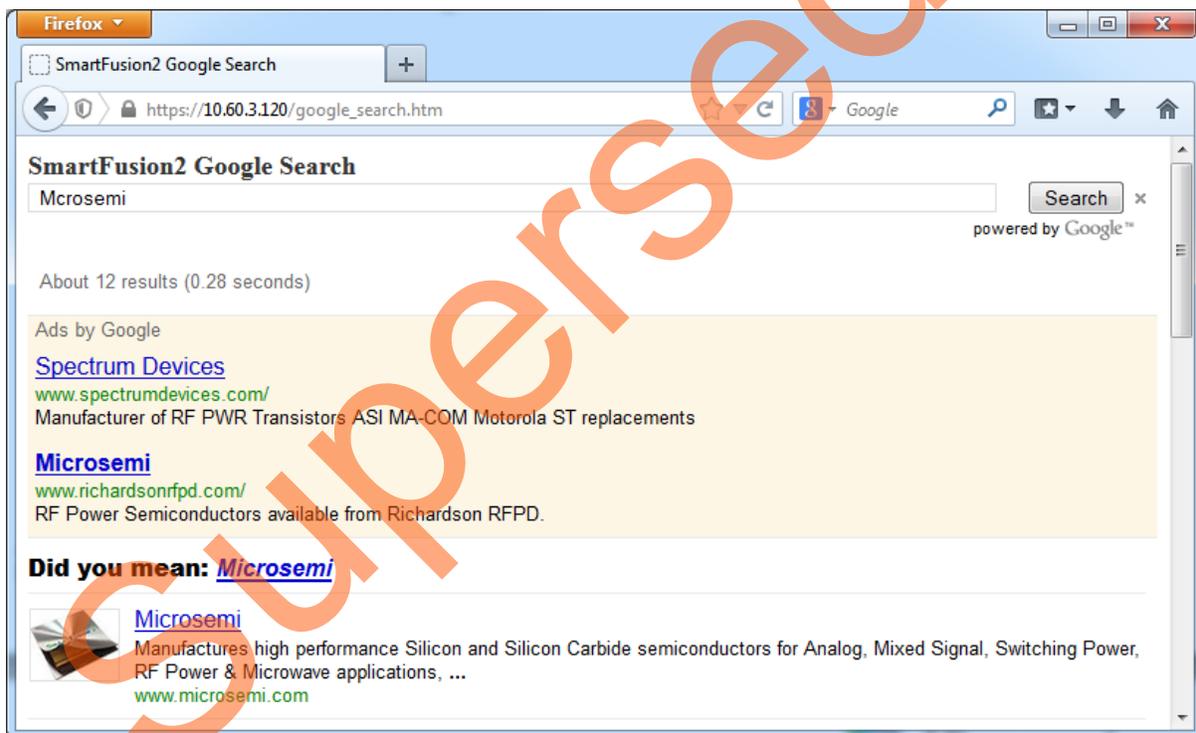


Figure 21 • SmartFusion2 Google Search Page

2. Click **Home** to go back to the main menu.

# 1 – Appendix 1: Board Setup for Running the Secure Webserver

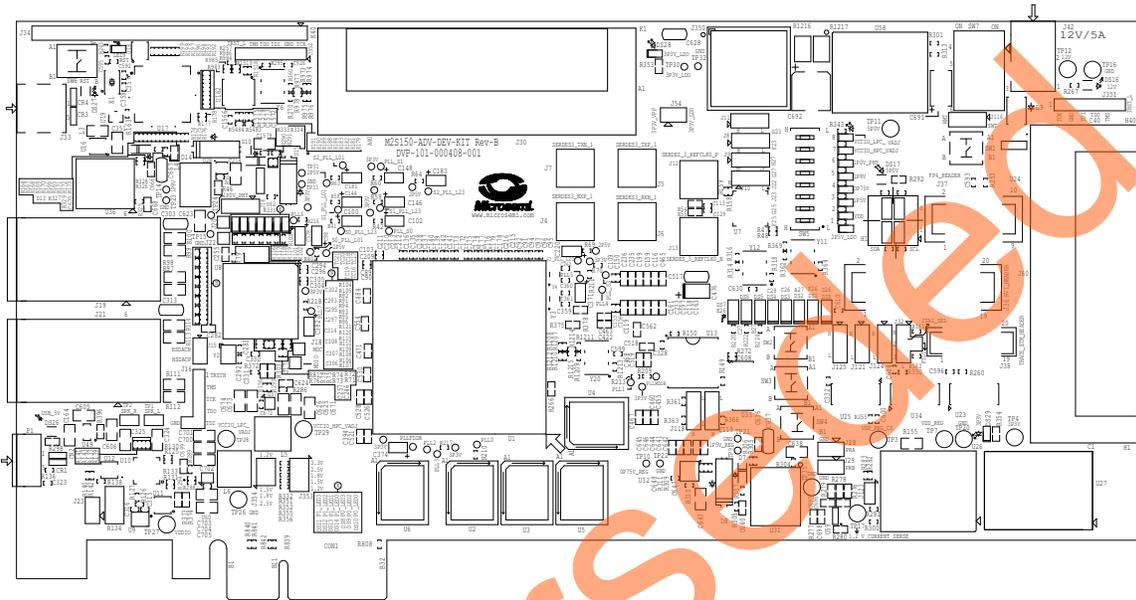
Figure 1 shows the board setup for running the demo on the Advanced Development Kit Board.



Figure 1 • SmartFusion2 Advanced Development Kit Setup

## 2 – Appendix 2: Jumper Locations

Figure 1 shows the jumper locations in the SmartFusion2 Advanced Development Kit board.



**Figure 1 • Jumper Locations in Advanced Development Kit Board**

**Note:**

- Jumpers highlighted in red are set by default.
- Jumpers highlighted in green must be set manually.
- The location of the jumpers in Figure 1 are searchable.

## 3 – Appendix 3: Running the Design in Static IP Mode

1. To run the design in Static IP mode, right-click the **Webserver\_TCP\_MSS\_CM3\_app** in the **Project Explorer** window of SoftConsole project and select **Properties** as shown in [Figure 1](#).

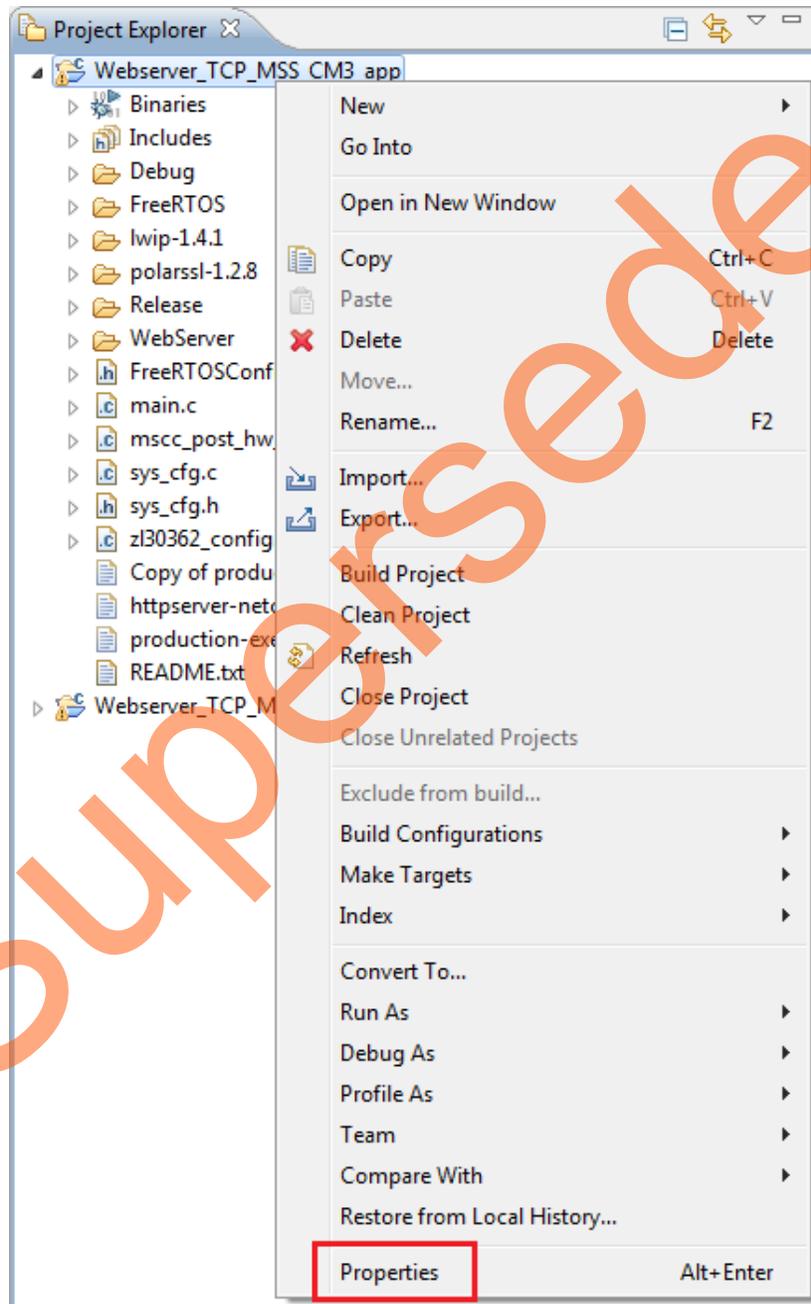


Figure 1 • Project Explorer Window of SoftConsole Project

Figure 2 shows removing the symbol `NET_USE_DHCP` in Tool Settings tab of the Properties for `Webserver_TCP_MSS_CM3` window.

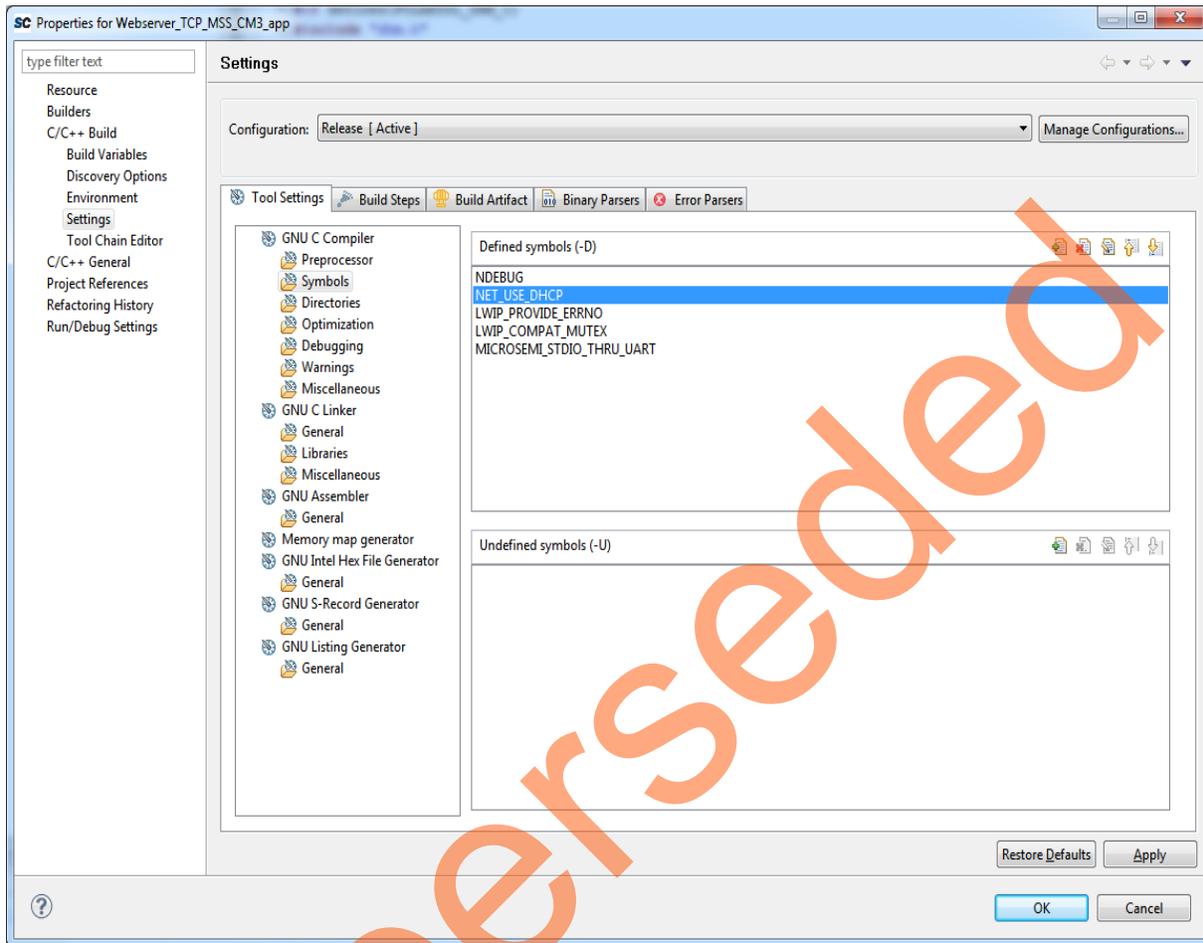


Figure 2 • Project Explorer Properties Window

If the device is connected in **Static IP** mode, the board static IP address is 169.254.1.23, then change the host TCP/IP settings to reflect the IP address. Figure 3 shows Host PC TCP/IP settings.

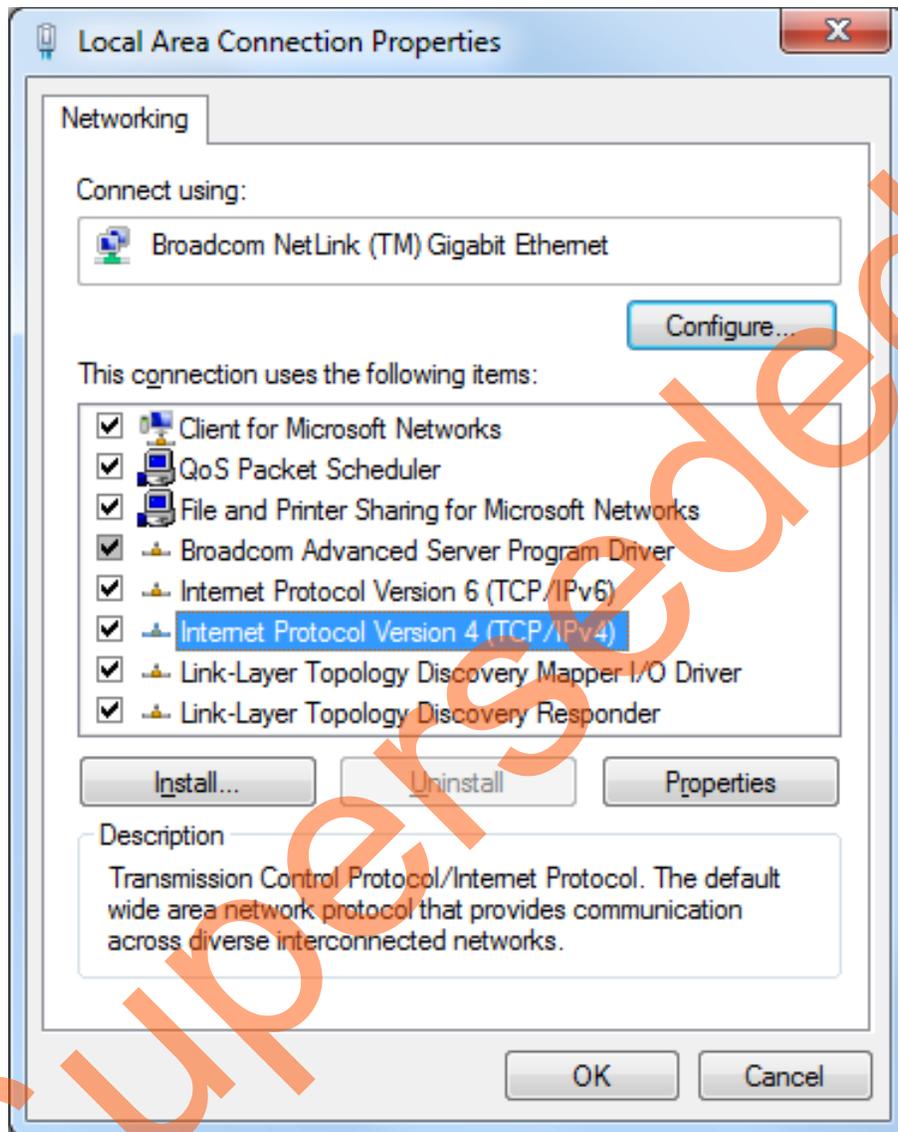
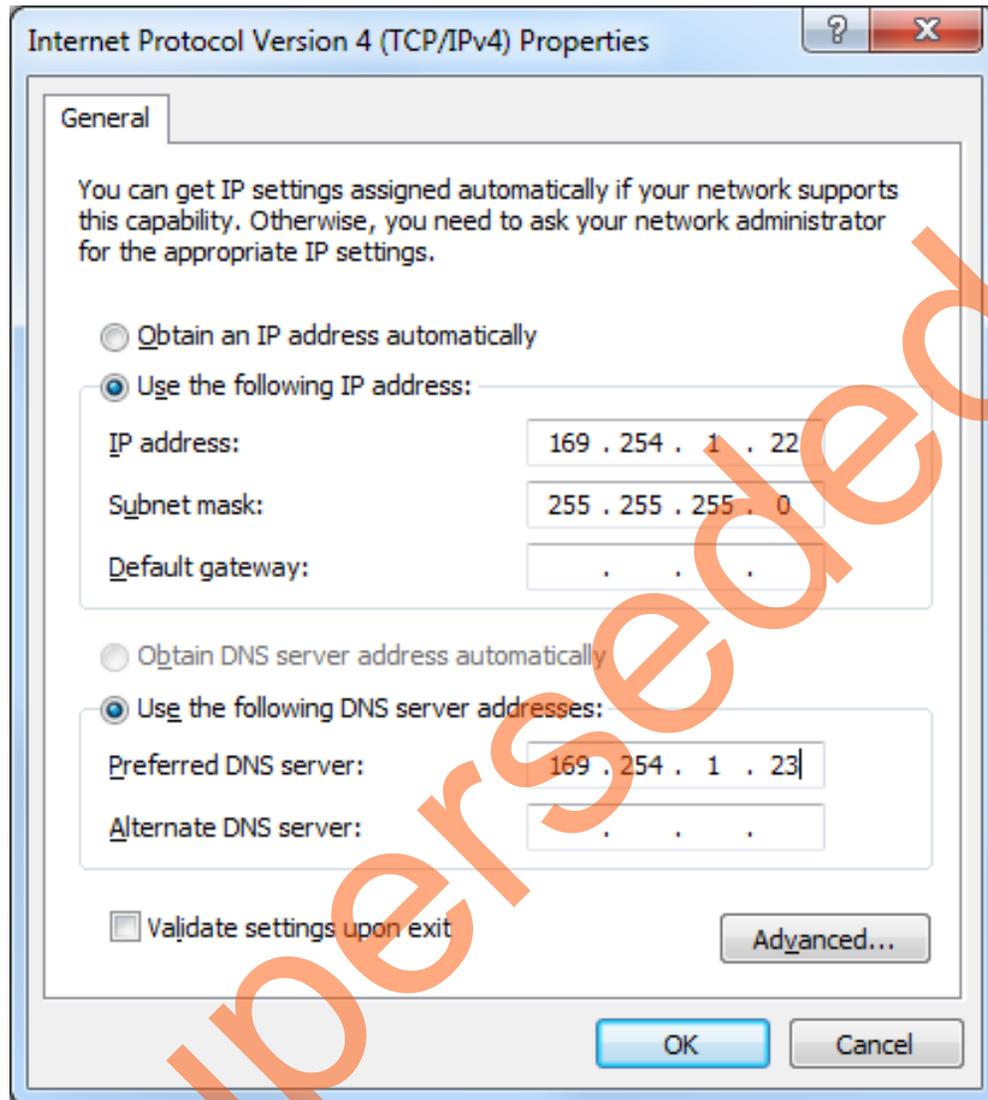


Figure 3 • Host PC TCP/IP Settings

Figure 4 shows Static IP address settings.



**Figure 4 • Static IP Address Settings**

Once these settings are made, build the design. Refer to "Running the Demo Design" section on page 1-17 to execute the design in static IP mode, if the SmartFusion2 device is already programmed with `secure_Webserver_tcp_top.stp` file.

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## A – List of Changes

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The following table lists critical changes that were made in each revision of the chapter in the demo guide.

<b>Date</b>	<b>Changes</b>	<b>Page</b>
Revision 2 (September 2014)	Updated the document for Libero v11.4 software release (SAR 60685).	NA
Revision 1 (April 2014)	Initial release.	NA

The revision number is located in the part number after the hyphen. The part number is displayed at the bottom of the last page of the document. The digits following the slash indicate the month and year of publication.

Superseded

## B – Product Support

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Microsemi SoC Products Group backs its products with various support services, including Customer Service, Customer Technical Support Center, a website, electronic mail, and worldwide sales offices. This appendix contains information about contacting Microsemi SoC Products Group and using these support services.

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Microsemi SoC Products Group staffs its Customer Technical Support Center with highly skilled engineers who can help answer your hardware, software, and design questions about Microsemi SoC Products. The Customer Technical Support Center spends a great deal of time creating application notes, answers to common design cycle questions, documentation of known issues, and various FAQs. So, before you contact us, please visit our online resources. It is very likely we have already answered your questions.

### Technical Support

Visit the Customer Support website ([www.microsemi.com/support/search/default.aspx](http://www.microsemi.com/support/search/default.aspx)) for more information and support. Many answers available on the searchable web resource include diagrams, illustrations, and links to other resources on the website.

### Website

You can browse a variety of technical and non-technical information on the SoC home page, at [www.microsemi.com](http://www.microsemi.com).

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Highly skilled engineers staff the Technical Support Center. The Technical Support Center can be contacted by email or through the Microsemi SoC Products Group website.

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You can communicate your technical questions to our email address and receive answers back by email, fax, or phone. Also, if you have design problems, you can email your design files to receive assistance. We constantly monitor the email account throughout the day. When sending your request to us, please be sure to include your full name, company name, and your contact information for efficient processing of your request.

The technical support email address is [soc\\_tech@microsemi.com](mailto:soc_tech@microsemi.com).

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