TE1BD is a bias board for Microsemi dual drivers and modules, providing easy control over four pairs of gate and drain voltages.

![Diagram of TE1BD Bias Board](image)

**Figure 1. TE1BD Bias Board**

**Application Notes:**

1. Test points for gate and drain voltages are labeled as Vg1a, Vg1b, Vg2a, Vg2b and Vd1a, Vd1b, Vd2a, Vd2b.

2. Drain current can be measured over two test points denoted by Id’s. There is 0.1 Ohm resistor in between these two test points. (i.e. \( I_d = \frac{(V_d - V(id))}{0.1} \))
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