

EnforCIT

Defense-in-Depth Solutions for Protecting Critical Information

EnforCIT® IP cores execute on a wide variety of FPGAs and can be used in stand-alone format or combined with other cores to provide additional firmware protection capabilities. Current levels of support are shown below. Support is added on a continual basis.

	Cyclone III LS	Virtex 4	Virtex 5	Virtex 6	Spartan 6	Series 7	SmartFusion®2	Arbitrary FPGAs
AES (fast and hard)	☑	☑	☑	☑	☑	☑	☑	✓
ECAU	☑	☑	☑	☑	☑	☑	☑	✓
ECC	☑	☑	☑	☑	☑	☑	✓	✓
RNG	✓	☑	☑	☑	☑	☑		✓
SHA2 / HMAC	☑	☑	☑	☑	☑	☑	☑	✓
XTS	☑	☑	☑	☑	☑	☑	☑	✓
SEMC	☑	☑	☑	☑	☑	☑	☑	✓
Secure RAM	☑	☑	☑	☑	☑	☑		✓
Clock Monitor	☑	☑	☑	☑	✓	☑	✓	✓
JTAG		☑	☑	✓	✓	✓	✓	
Key Module	☑	☑	☑	☑	☑	✓		✓
Bitstream Auth		✓	✓	☑		☑		
Glitch Detect			☑	☑				
Secure Logger	☑	☑	☑	☑	☑	☑	☑	✓
Authentication	☑	☑	☑	☑	☑	☑		✓
Security Monitor							☑	

- ☑ Fully qualified support
- ✓ Partial/prospective support

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