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Line Card/Transport Interface

Part No.	Description	BW (Hz)	Inputs	Input Frequency	Diff. Outputs	CMOS Outputs	Output Frequency	Low-Jitter APLLs	GP Clock Gen	Jitter (ps$_{RMS}$)	Pkg size (mm)
ZL30347	Single Ch 1588-SyncE DPLL - 1GbE	0.5m–400	2 D/SE	N x 8K, SyncE	2	6	N x 8K, SDH, SyncE	1-Int-N	2	1.0	9 x 9
ZL30365	Quad channel combined IEEE 1588 ToP and SyncE device	5–890	8 D/SE	1 Hz–750 MHz	8	8	1 Hz–750 MHz	4	0	0.67	13 x 13
ZL30367	Dual channel combined IEEE 1588 ToP and SyncE device	5–890	9 D/SE+2 SE	1 Hz–750 MHz	6	6	1 Hz–750 MHz	3	0	0.67	13 x 13

Timing Card/System Controller/Single Board/SETS with Transport Interface

Part No.	Description	DPLLs	BW (Hz)	Inputs	Input Frequency	Embedded PPS & EPP2S	Diff. Out-puts	CMOS Out-puts	Output Frequency	Low-Jitter APLLs	GP Clock Gen	Jitter (ps$_{RMS}$)	Pkg size (mm)
ZL30342	Single Ch 1588-SyncE DPLL - 1GbE	1 NCO	0.1–890	3 SE	N x 8K, SyncE		1	2	N x 8K, SDH, SyncE	1-Int-N	1	1.0	9 x 9
ZL30343	Dual Ch 1588-SyncE DPLL - 1GbE	2 NCO	0.1–890	8 SE	N x 8K, SyncE		2	6	N x 8K, SDH, SyncE	1-Int-N	2	1.0	9 x 9
ZL30361	Any-Freq. Single Ch 1588-SyncE PLL/NCO - 1GbE/10GbE	1 NCO	0.1–896	11	1 Hz–750 MHz		6	6	1 Hz–750 MHz	3	0	0.67	13 x 13
ZL30362	Any-Freq. Quad Ch 1599-SyncE PLL/NCO - 1GbE/10GbE	4 NCO	0.1–896	11	1 Hz–750 MHz		8	8	1 Hz–750 MHz	4	0	0.67	13 x 13
ZL30363	Any-Freq. Dual Ch 1588-SyncE PLL/NCO - 1GbE/10GbE	2 NCO	0.1–896	11	1 Hz–750 MHz		8	8	1 Hz–750 MHz	4	0	0.67	13 x 13
ZL30364	Any-Freq. Triple Ch 1588-SyncE PLL/NCO - 1GbE/10GbE	3 NCO	0.1–896	11	1 Hz–750 MHz		8	8	1 Hz–750 MHz	4	0	0.67	13 x 13
ZL30721	Single Ch 1588-SyncE PLL/NCO Small Footprint - 10GbE *	1 NCO	0.1–10	2 D/SE + 1 SE	8 kHz–1250 MHz		3	6	<1 Hz–1035 MHz	1	0	0.26	5 x 10
ZL30722	Single Ch 1588-SyncE PLL/NCO Small Footprint - 10GbE	1 NCO	0.1–500	2 D/SE + 1 SE	8 kHz–1250 MHz		3	6	<1 Hz–1035 MHz	1	0	0.26	5 x 5
ZL30723	Dual Ch 1588-SyncE PLL/NCO Small Footprint - 10GbE	2 NCO	0.1–500	4 D/SE + 1 SE	8 kHz–1250 MHz		6	12	<1 Hz–1035 MHz	2	0	0.26	5 x 10
ZL30701	Any-Freq. Single Ch 1588-SyncE PLL/NCO - 10GbE/40GbE/100GbE	1 or (1 NCO)	0.1m– 470	5 D/10 SE	0.5 Hz–900 MHz	•	6	14	0.5 Hz–900 MHz	2 or 3	1	0.19	10 x 10
ZL30702	Any-Freq. Dual Ch 1588-SyncE PLL/NCO - 10GbE/40GbE/100GbE	2 or (2 NCO)	0.1m– 470	5 D/10 SE	0.5 Hz–900 MHz	•	6	14	0.5 Hz–900 MHz	2 or 3	1	0.19	10 x 10
ZL30703	Any-Freq. Triple Ch 1588-SyncE PLL/NCO - 10GbE/40GbE/100GbE	3 or (3 NCO)	0.1m– 470	5 D/10 SE	0.5 Hz–900 MHz	•	6	14	0.5 Hz–900 MHz	2 or 3	1	0.19	10 x 10
ZL30704	Any-Freq. Quad Ch 1588-SyncE PLL/NCO - 10GbE/40GbE/100GbE	4 or (4 NCO)	0.1m– 470	5 D/10 SE	0.5 Hz–900 MHz	•	6	14	0.5 Hz–900 MHz	2 or 3	1	0.19	10 x 10
ZL30771	Any-Freq. Single Ch 1588-SyncE PLL/NCO - 10GbE/40GbE/100GbE 5G CU, DU, and RU systems	1 or (1 NCO)	0.1m-470	10 D/10 SE	0.5 Hz–900 MHz	•	8	16 + 2	0.5 Hz–1045 MHz	2	1	0.19	11x11
ZL30772	Any-Freq. Dual Ch 1588-SyncE PLL/NCO - 10GbE/40GbE/100GbE 5G CU, DU, and RU systems	2 or (2 NCO)	0.1m-470	10 D/10 SE	0.5 Hz–900 MHz	•	8	16 + 2	0.5 Hz–1045 MHz	2	1	0.19	11x11
ZL30773	Any-Freq. Triple Ch 1588-SyncE PLL/NCO - 10GbE/40GbE/100GbE 5G CU, DU, and RU systems	3 or (3 NCO)	0.1m-470	10 D/10 SE	0.5 Hz–900 MHz	•	8	16 + 2	0.5 Hz–1045 MHz	2	1	0.19	11x11

* Low-jitter operation from any 10MHz TCXO

Abbreviations:
D= Differential. SE= Single-ended. GP= General purpose. NCO= Numerically controlled oscillator. ToP= Timing over packet. SyncE= Synchronous Ethernet. SS= SONET/SDH. FC= Frequency conversion/T4 DPLL.

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