

# Delivering Sub-Microsecond Accurate Time to Linux Applications Around the World



**Paul Skoog**

Product Marketing Manager

[pskoog@symmetricom.com](mailto:pskoog@symmetricom.com)

# Agenda

- Precise Timing in Trading Applications
- The Four Sources of Time Error (w/ sample data)
- Using PTP for Precise Time Synchronization
- Solving The Problems (w/ sample data)
- Conclusions

# Symmetricom at a Glance

**Undisputed global leader of highly-precise timekeeping technology, instruments and solutions**

- Headquarters in San Jose, California
- Publicly traded on NASDAQ (SYMM)
- Focused offerings for Communications, Government, Enterprise and Power Utility market segments
- Incumbency with major customers in over 100 countries
- Large ecosystem of partners including integrators, NEMs, test and silicon vendors



# Our Leading & Diverse Customers

## Communications



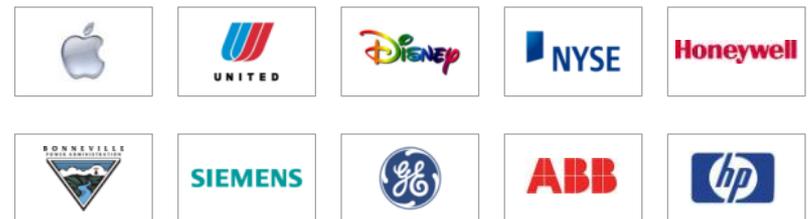
## Government



## Power



## Enterprise

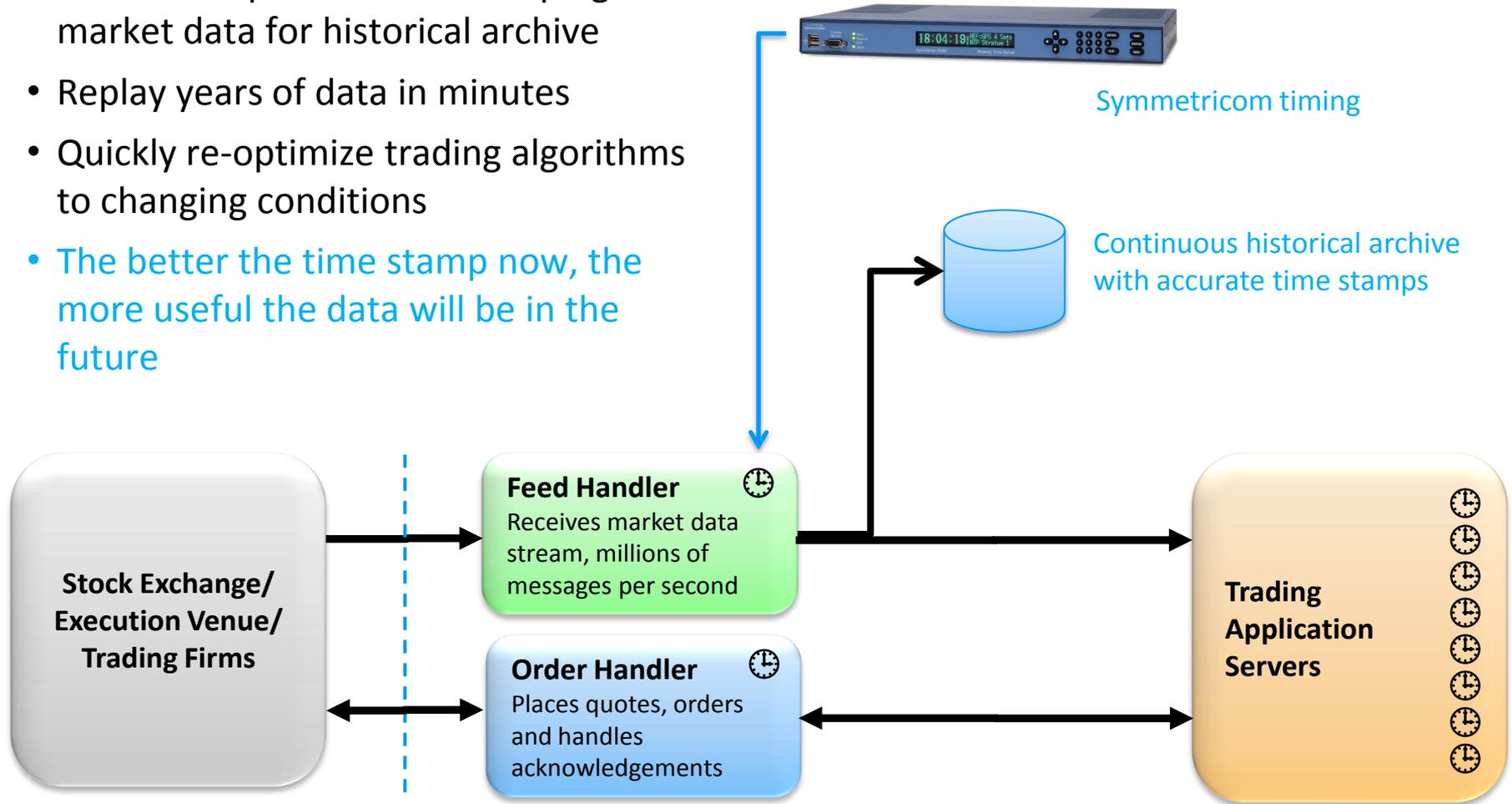


# Precise Timing in Trading Applications



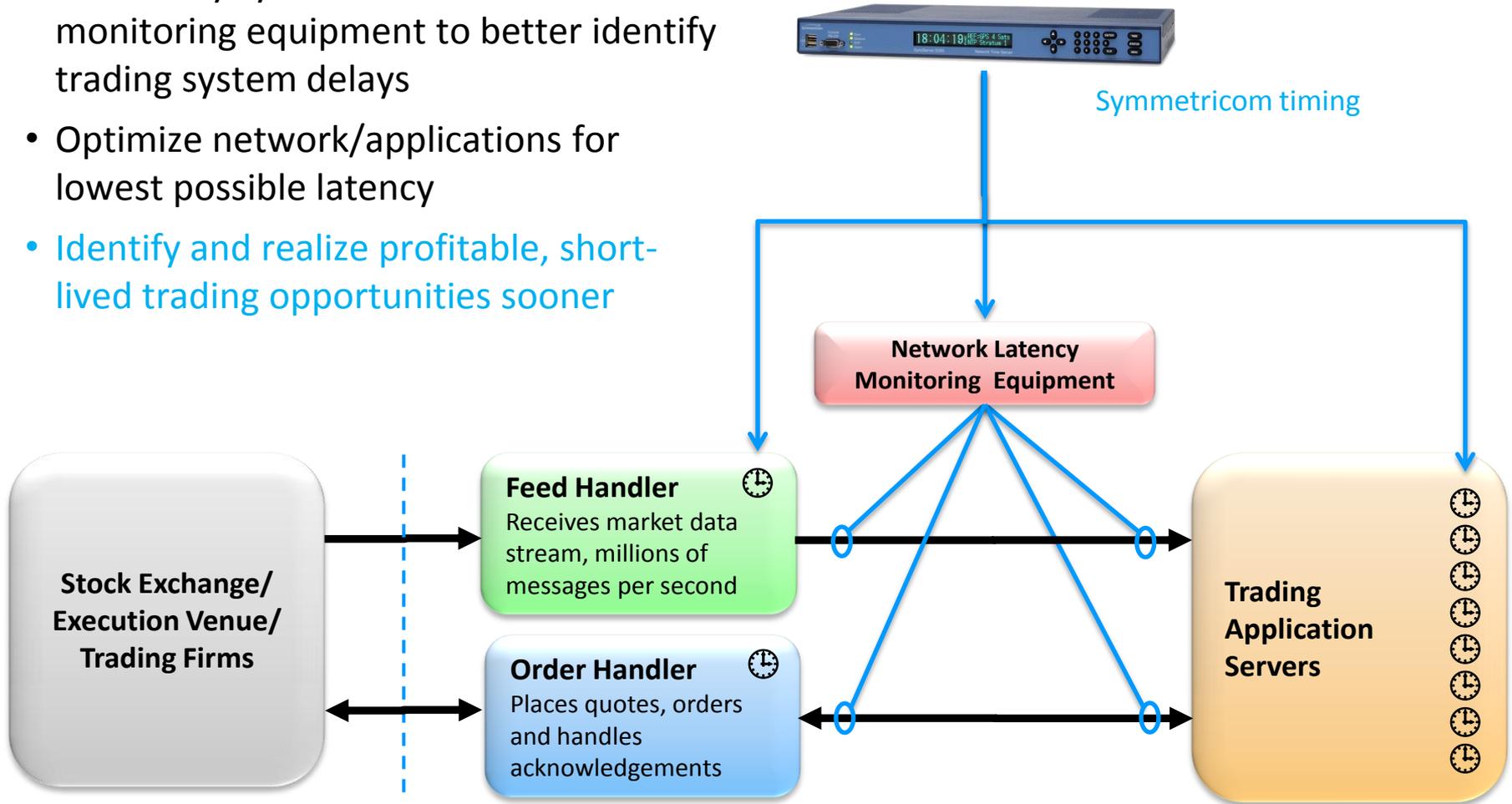
# Optimized Algorithms = Optimal Financial Performance

- Accurate & precise time stamping of market data for historical archive
- Replay years of data in minutes
- Quickly re-optimize trading algorithms to changing conditions
- The better the time stamp now, the more useful the data will be in the future



# Network Delay = Lost Opportunity

- Accurately synchronize server clocks and monitoring equipment to better identify trading system delays
- Optimize network/applications for lowest possible latency
- Identify and realize profitable, short-lived trading opportunities sooner



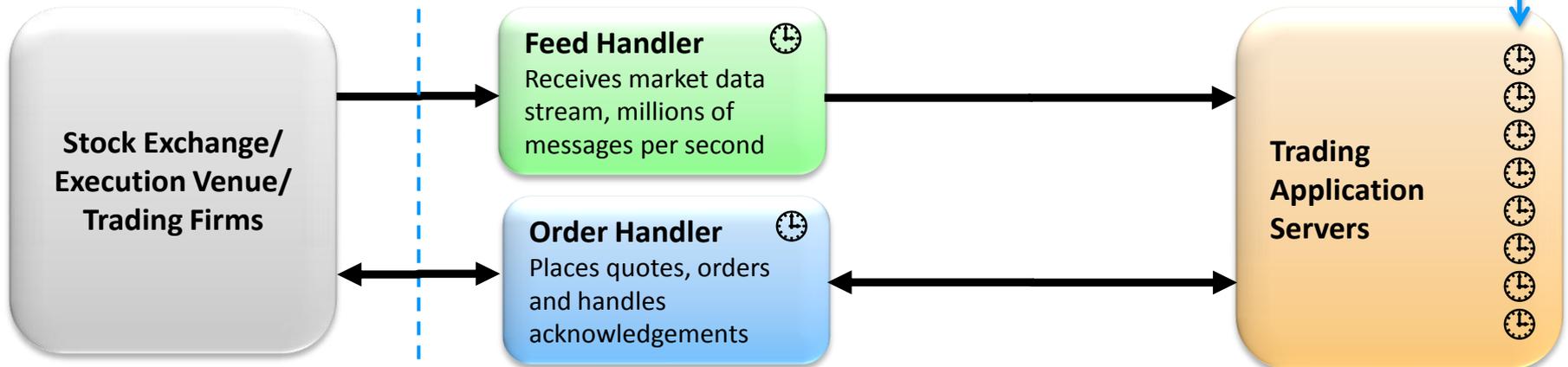
# Synchronization Errors = High Financial Risk

- Market data distributed across many servers handling very large & complex computations
- Accurate & precise time synchronization critical in:
  - Trade decision making/profits
  - Risk management/losses
  - Performance metrics



Symmetricom timing

Accurately and reliably synchronize clocks in servers in a single rack and in servers around the world



# The Four Sources of Time Error



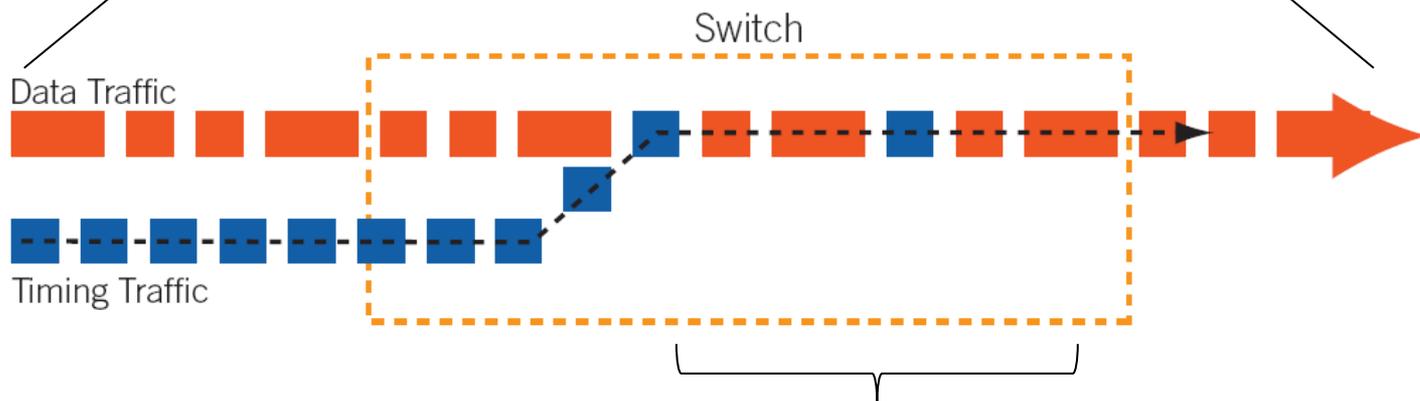
Accurate time transfer is all about....

- Correcting time **offsets** between clocks by exchanging timing packets
- Compensating for time packet transit **delays** between clocks
- Compensating for time packet transit delay **variation** (aka PDV, or packet arrival time jitter)

# Sources of Time Offset Errors

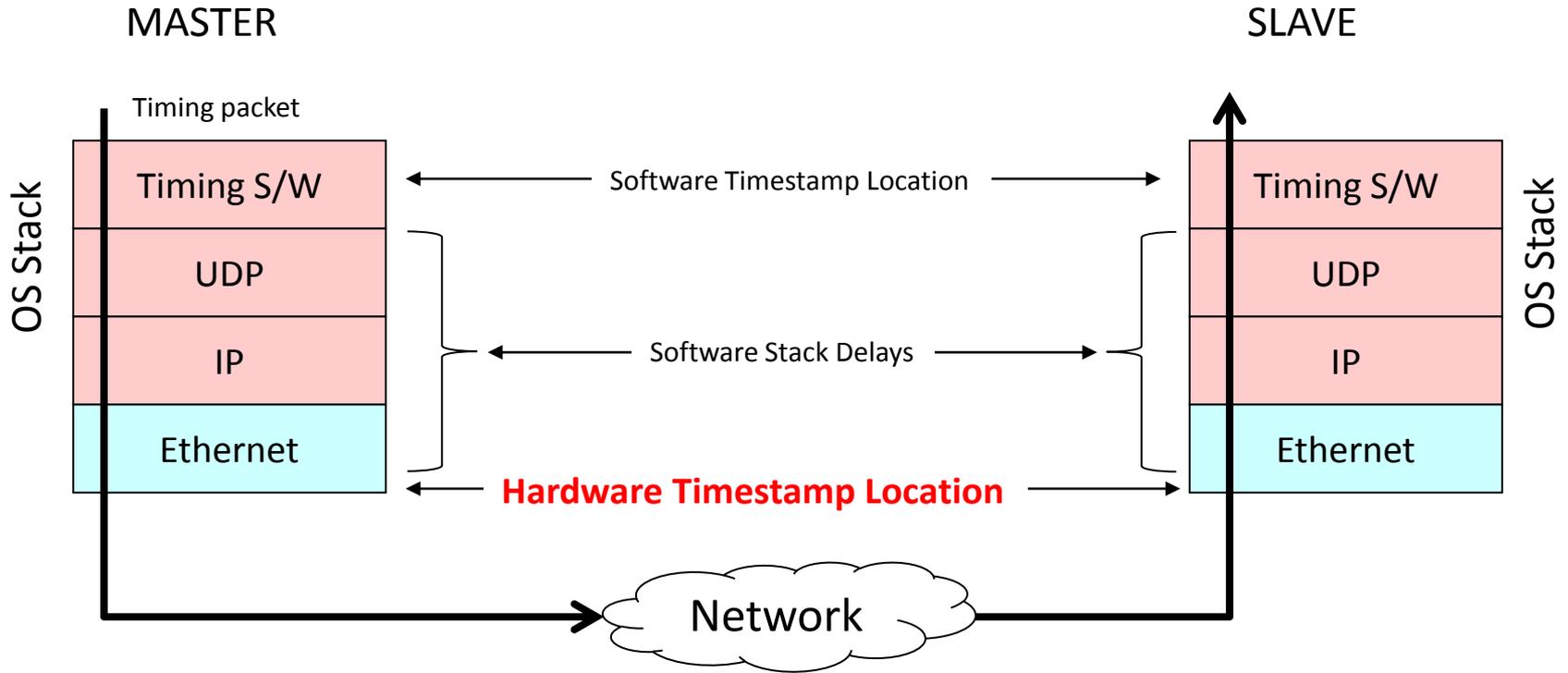
- Delays
  - Time transfer path delays
    - Network delays – queuing & delays inside switches
    - Linux stack delays
  - Linux clock call delays
  
- Clock instability
  - Inexpensive/unstable server oscillators
  - Temperature sensitivity

# Network Path Delays



Delays not uniform and may exist in only one direction between master and slave

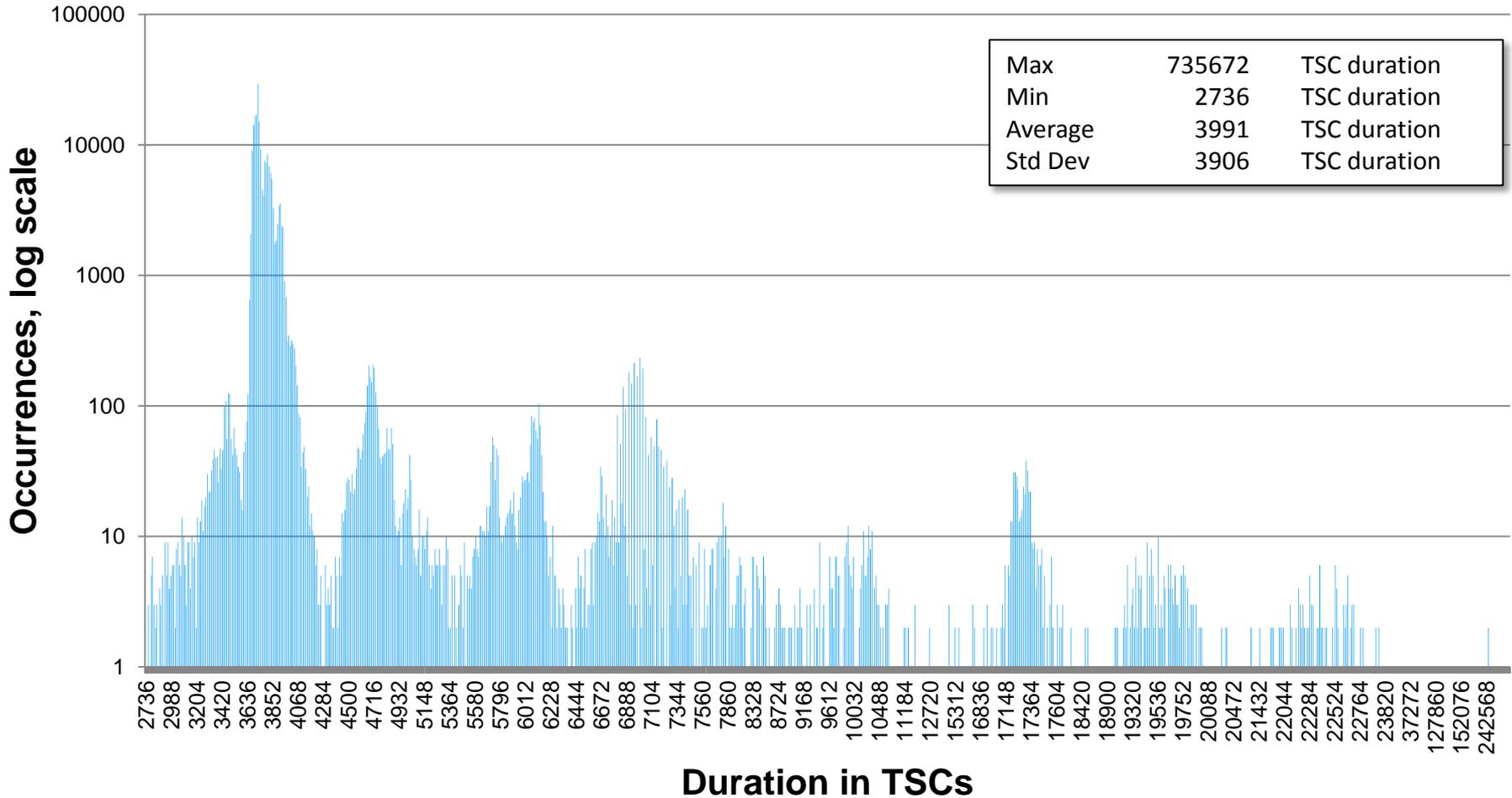
# Linux Stack Delays



Hardware time stamping eliminates OS stack delays

# Linux clock\_gettime Call Delays

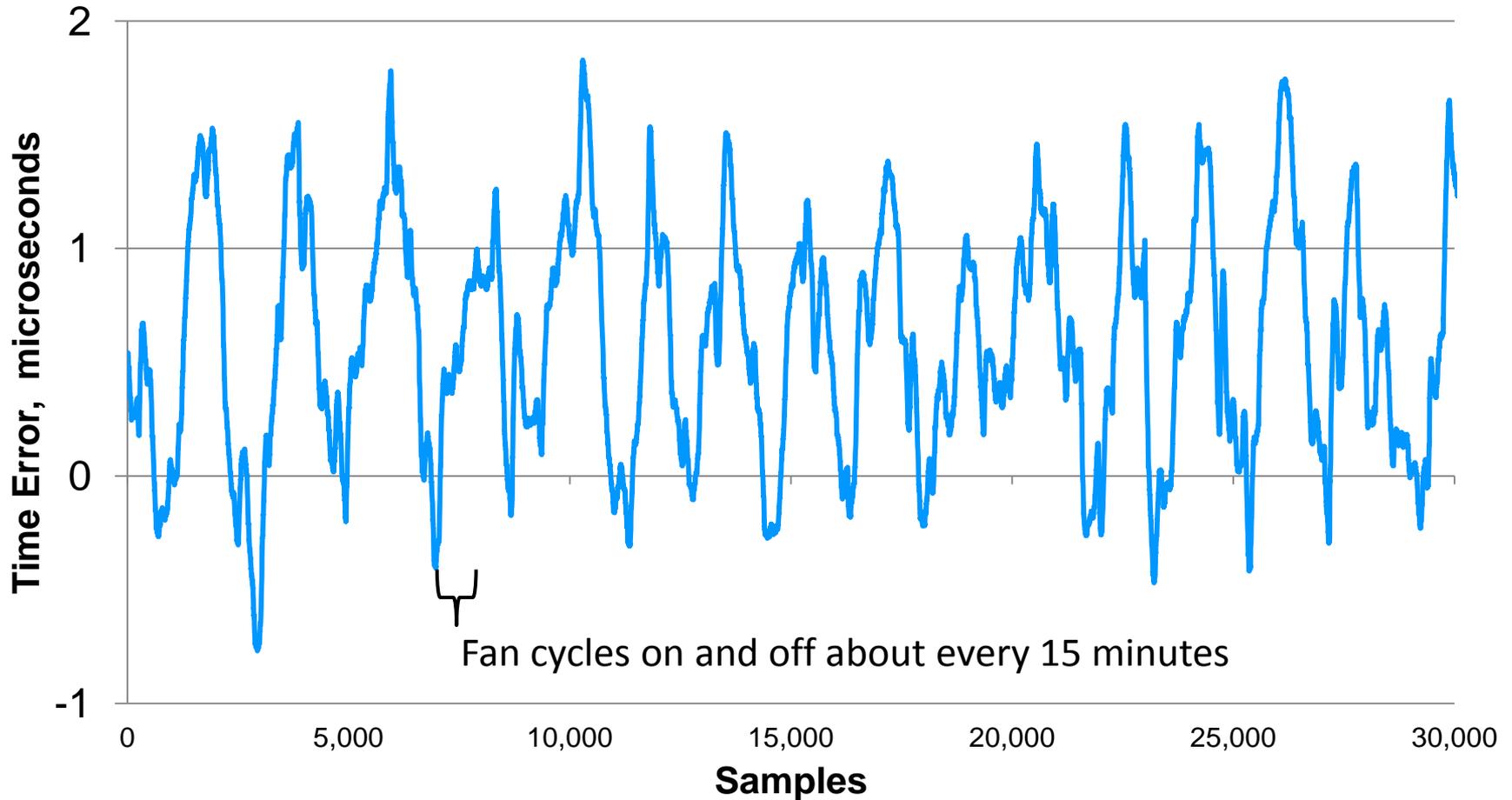
## Histogram of Linux clock\_gettime Call Duration in TSCs (200,000 measurements)



*Time Stamp Counter (TSC) representative of a certain number of CPU counter cycles*

# Server Oscillator Stability Errors

## Fan/Temp Cycling Changing Clock Time\*

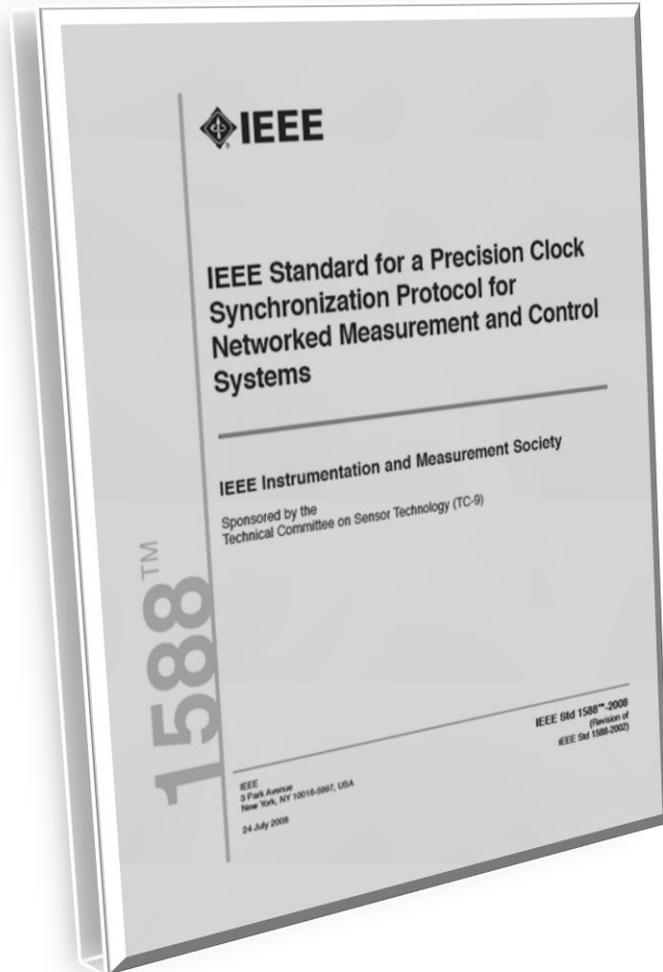


*\*This is on an unloaded server, errors worse on a server under load*

# Using PTP for Precise Time Synchronization



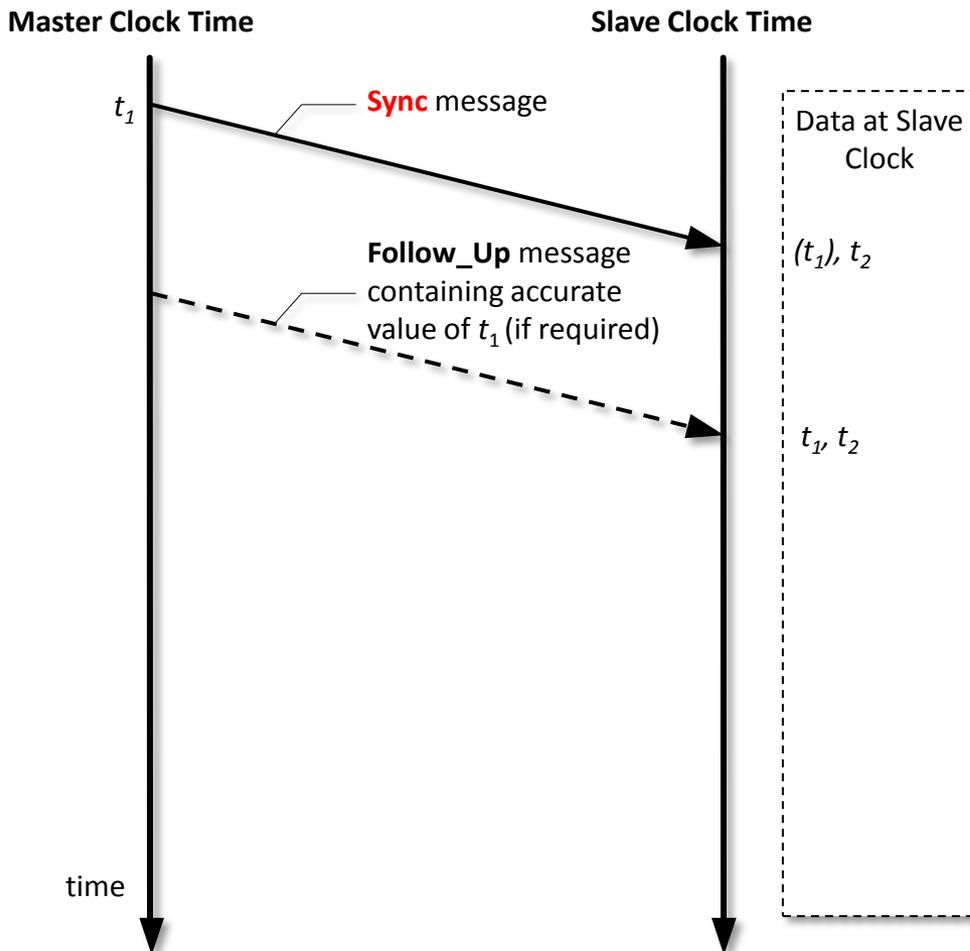
# What is the Precision Time Protocol (PTP)?



- Protocol specification for distributing precise time over packet networks
- Defined in IEEE Standard 1588
  - First version (2002) targeted LAN applications
  - Second version (2008) expanded applicability to cover broader applications/networks
- Time is carried in “event messages” transmitted between a Grandmaster Clock and a Slave Clock
- Runs over Ethernet and/or IP networks
- Commonly referred to as:
  - PTP (Precision Time Protocol) or PTP v.2
  - IEEE1588-2008 or IEEE1588 v.2

# PTP Packet Exchange

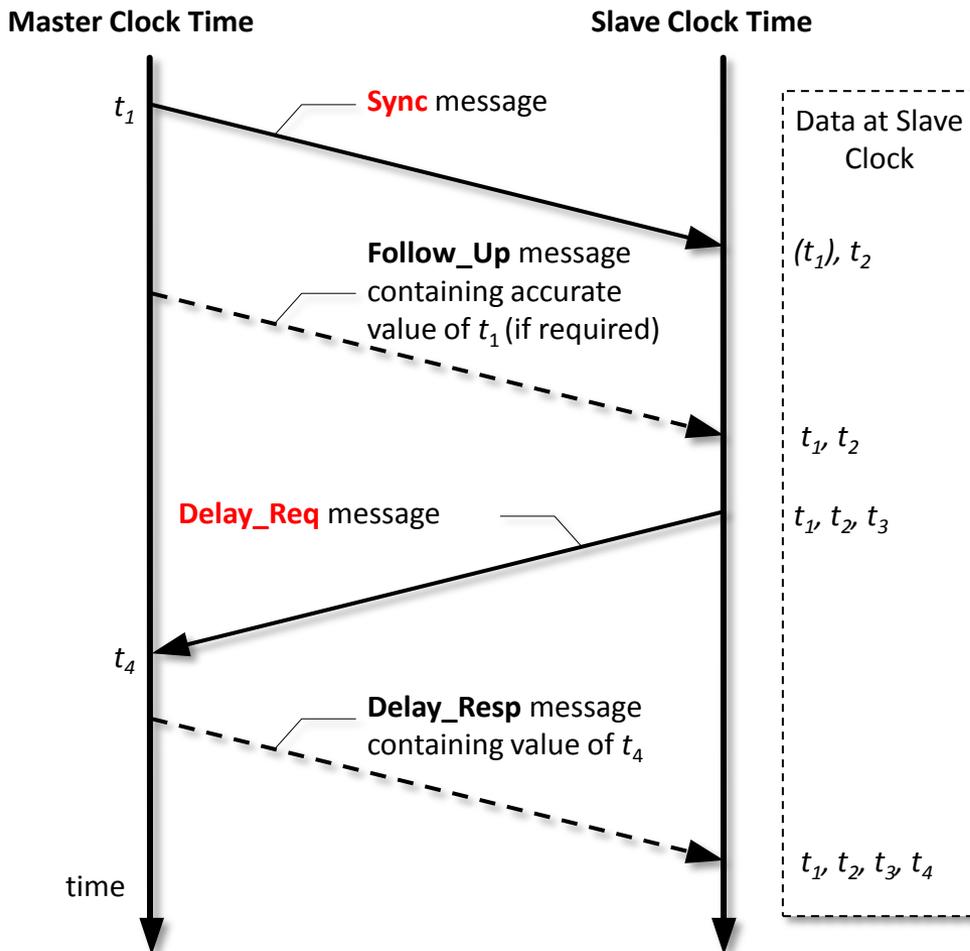
- PTP defines an exchange of time stamped messages over a packet network



- **Packet Exchange:** Master and Slave exchange time stamped *Sync* & *Delay\_req* event messages

# PTP Packet Exchange

- PTP defines an exchange of time stamped messages over a packet network



- **Packet Exchange:** Master and Slave exchange time stamped *Sync* & *Delay\_req* event messages
- Four key timestamps are collected
- **Time offset** calculation requires all four timestamps:

$$SlaveTimeOffset = \frac{(t_1 - t_2) + (t_4 - t_3)}{2}$$

- assumes symmetric path delays (i.e. the forward path delay is equal to the reverse path delay)

# Specific Benefits of PTP

- Higher packet exchange rates...
  - Allows for improved packet filtering and clock steering with servo loops to overcome PTP packet arrival time jitter
- Hardware time stamping
  - Vendors have generally adopted hardware time stamping to eliminate OS stack delays
- What PTP is NOT...
  - No clock steering algorithms as part of the PTP standard
  - Clock steering algorithms are vendor supplied, results vary widely...

# Solving the four sources of time error...

(network delays, stack delays, clock call delays, clock instability)



# Symmetricom End-to-End Accurate and Reliable Timing Solution

## GPS referenced SyncServer® S350 PTP Grandmaster for each venue

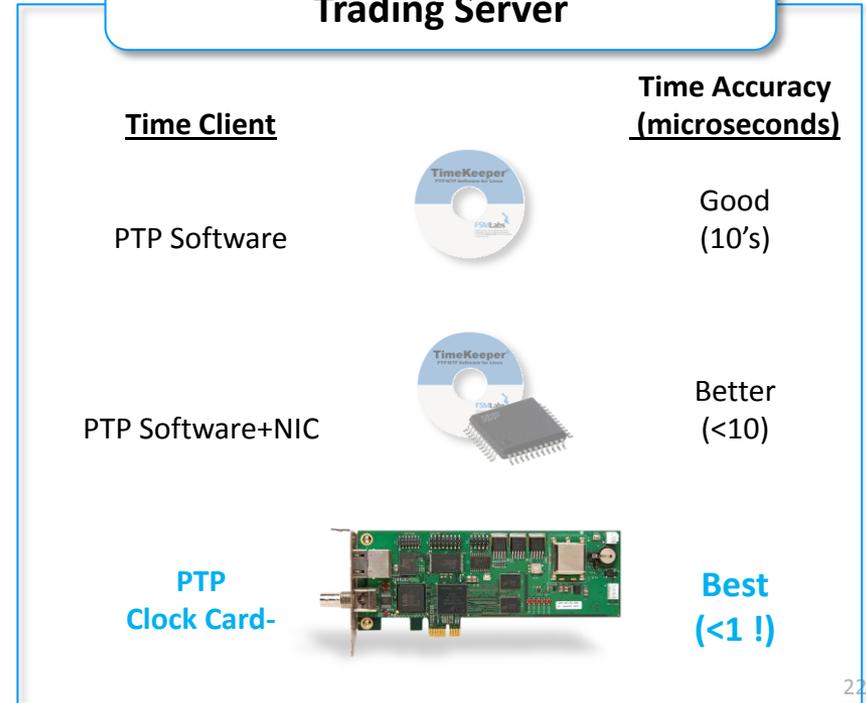
- Accurate to 50 nanoseconds to UTC
- Accurate time available around the planet



**Feed Handler, Order Handler,  
Trading Server**

## PTP Clock Card

- Superior clock hardware on PCIe bus compared to server clock
- Best possible time accuracy and precision



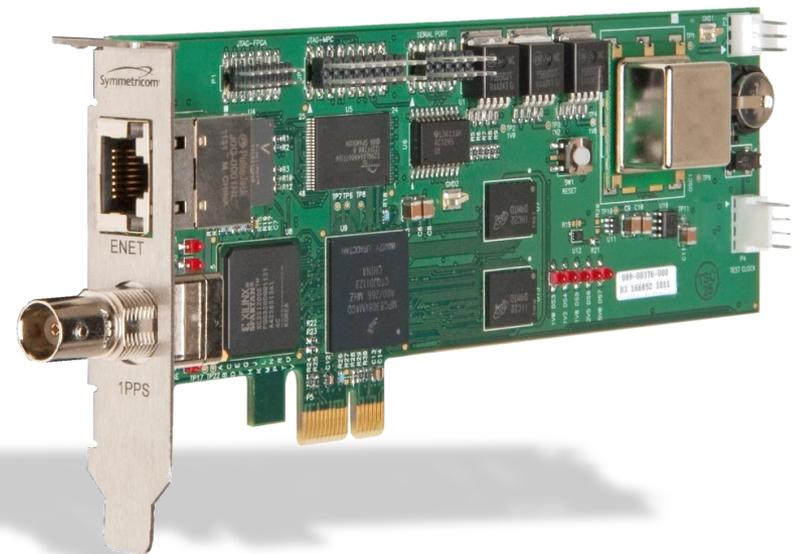
# The Newest PTP Clock for High Speed Trading

*Symmetricon SyncPoint PCIe-1000 PTP Clock Card  
end-to-end nanosecond timing for high speed trading systems*

Accuracy

- Ultra accurate PTP Clock Card (10 ns to a master in benchmark test)
- Absolute time accuracy at SW application ~600 ns to UTC
- Writes time directly to memory of host server
- Software can read time >1 million times per second and get monotonically advancing time

Speed



# SyncPoint™ PCIe-1000 PTP Clock Card Advantages

10 ns precision hardware time stamping eliminates server delay related errors

Gigabit Ethernet for easy interoperability with trading network

1PPS output to measure *real world* time accuracy to PTP grandmaster

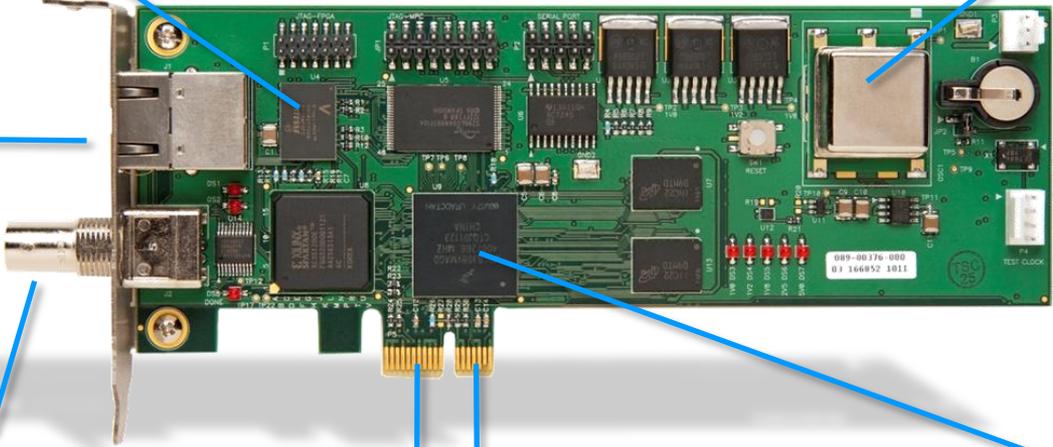
Small x1 PCIe card fits any PCIe slot, not the most precious slots

Writes time directly to server memory to be read > 1million times per second

High performance OCXO standard for very stable time during the trading day and server to server time sync

8 ns accuracy to PTP grandmaster is the best accuracy available today in a clock card. Great server to server sync.

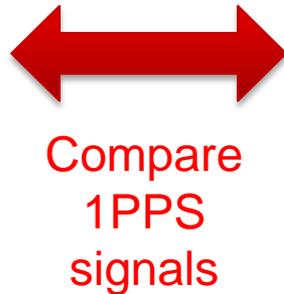
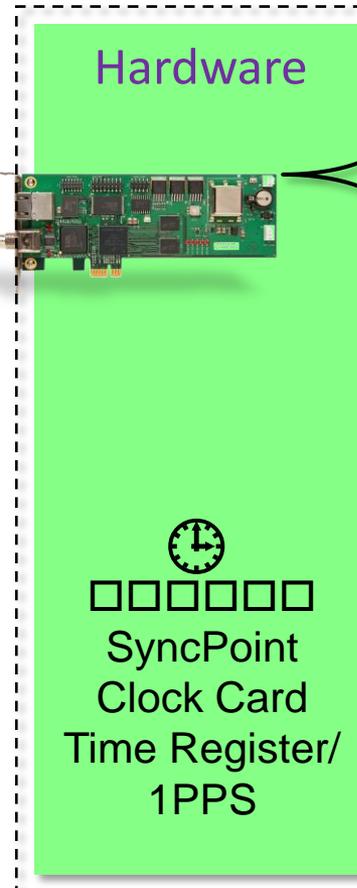
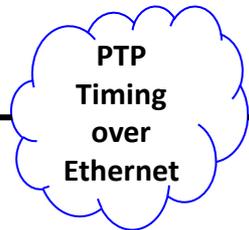
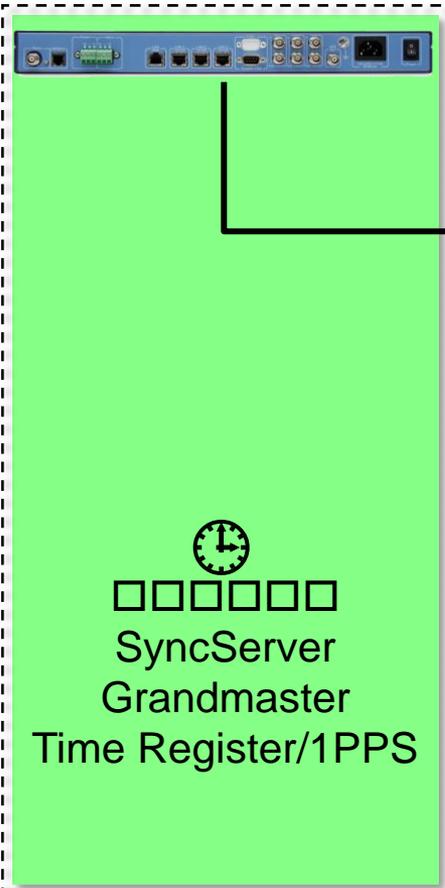
State-of-the-art timing algorithms keeps time accurate over busy trading networks



# Solving Path Delays & Oscillator Instability

SyncServer S350  
PTP Grandmaster

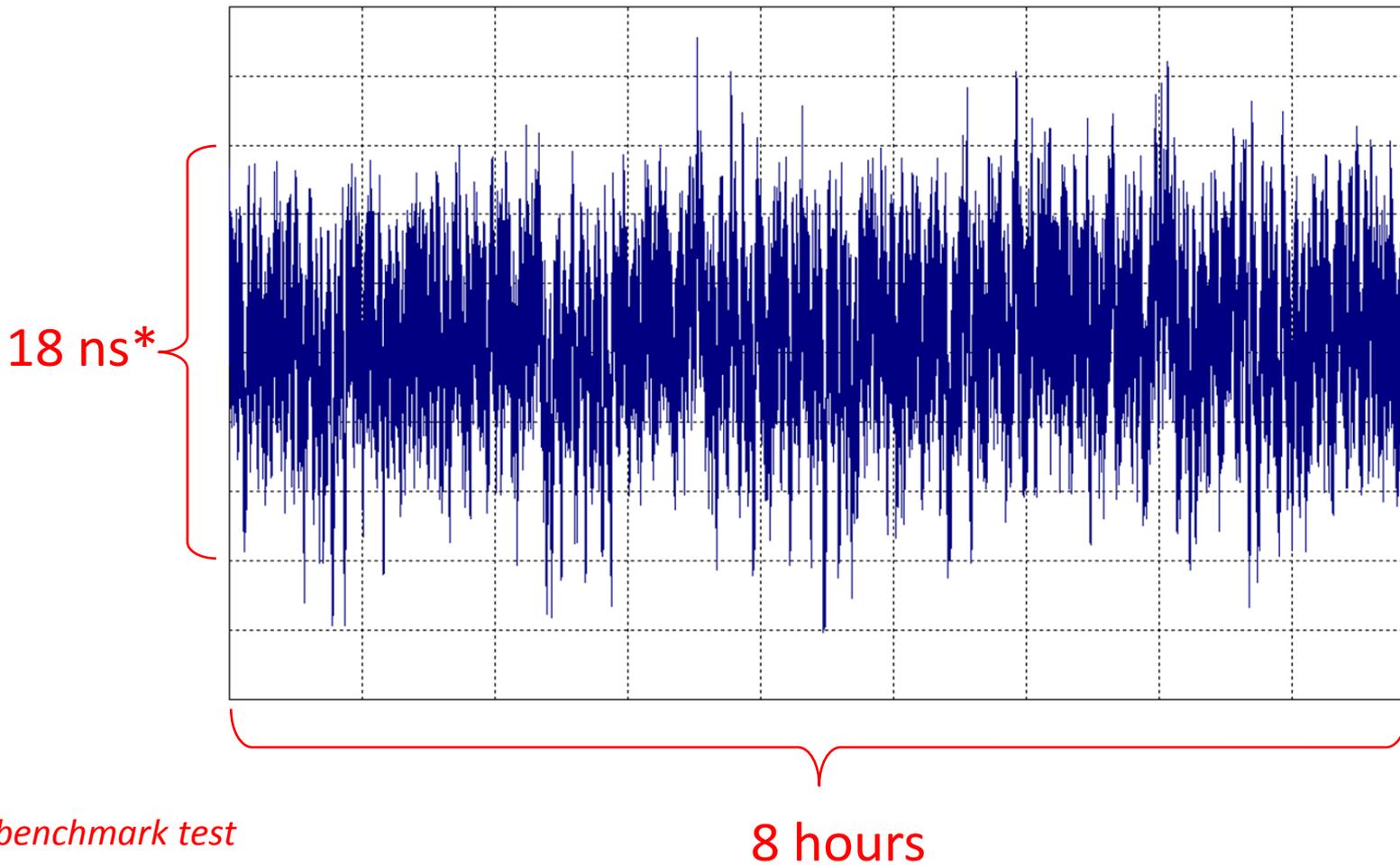
Compute Server



## Advantages

- Supports 32 PPS with packet pre filtering and excellent clock steering algorithms to overcome timing packet jitter
- OCXO for stable time base
- Hardware time stamping to eliminate OS stack delay

# SyncServer to SyncPoint PCIe-1000 Accuracy

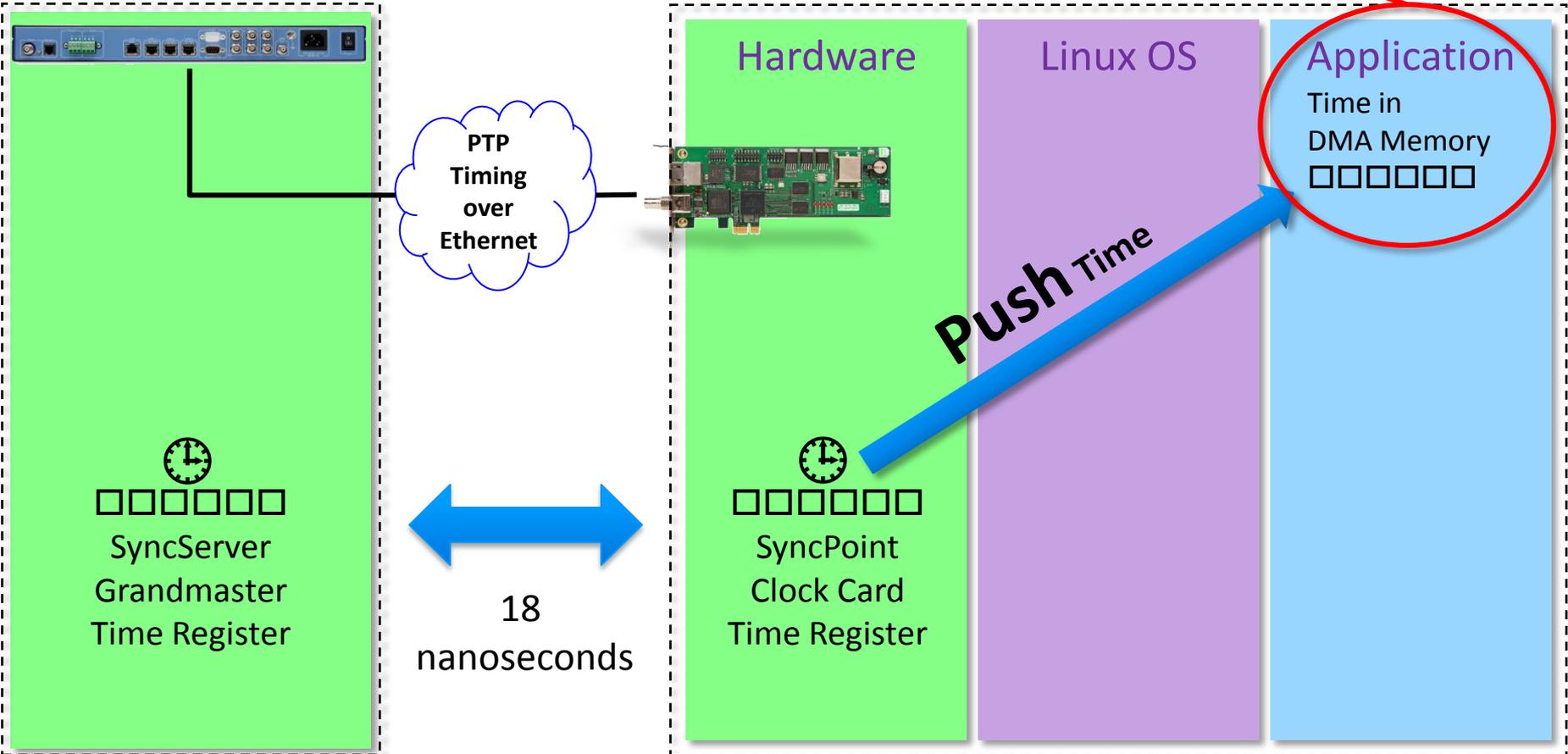


# Moving Time To Memory

SyncServer S350  
PTP Grandmaster

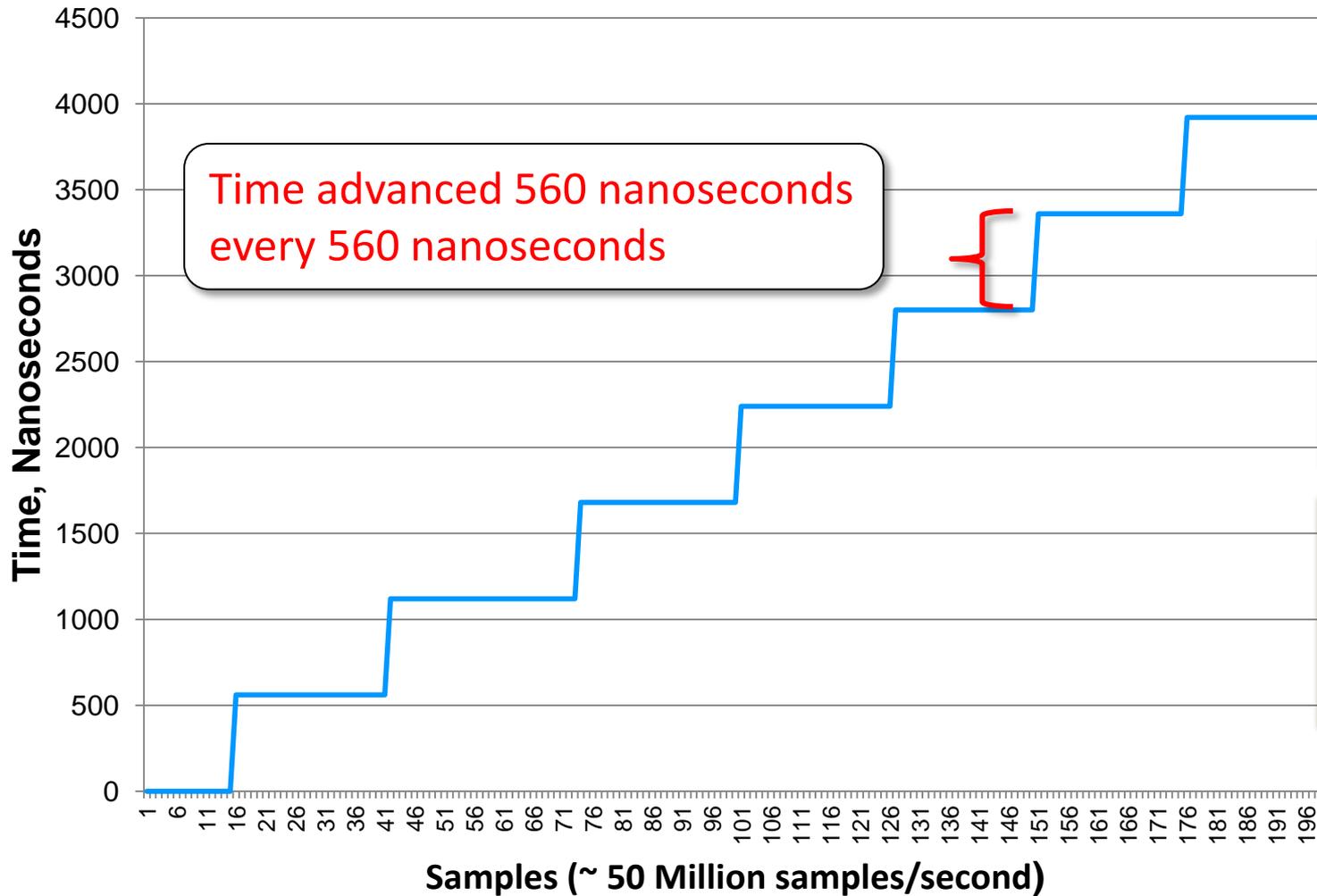
Compute Server

Speed & Accuracy?



# Oversampling Time from DMA Memory

## High Speed Over Sampling the DMA Time Register



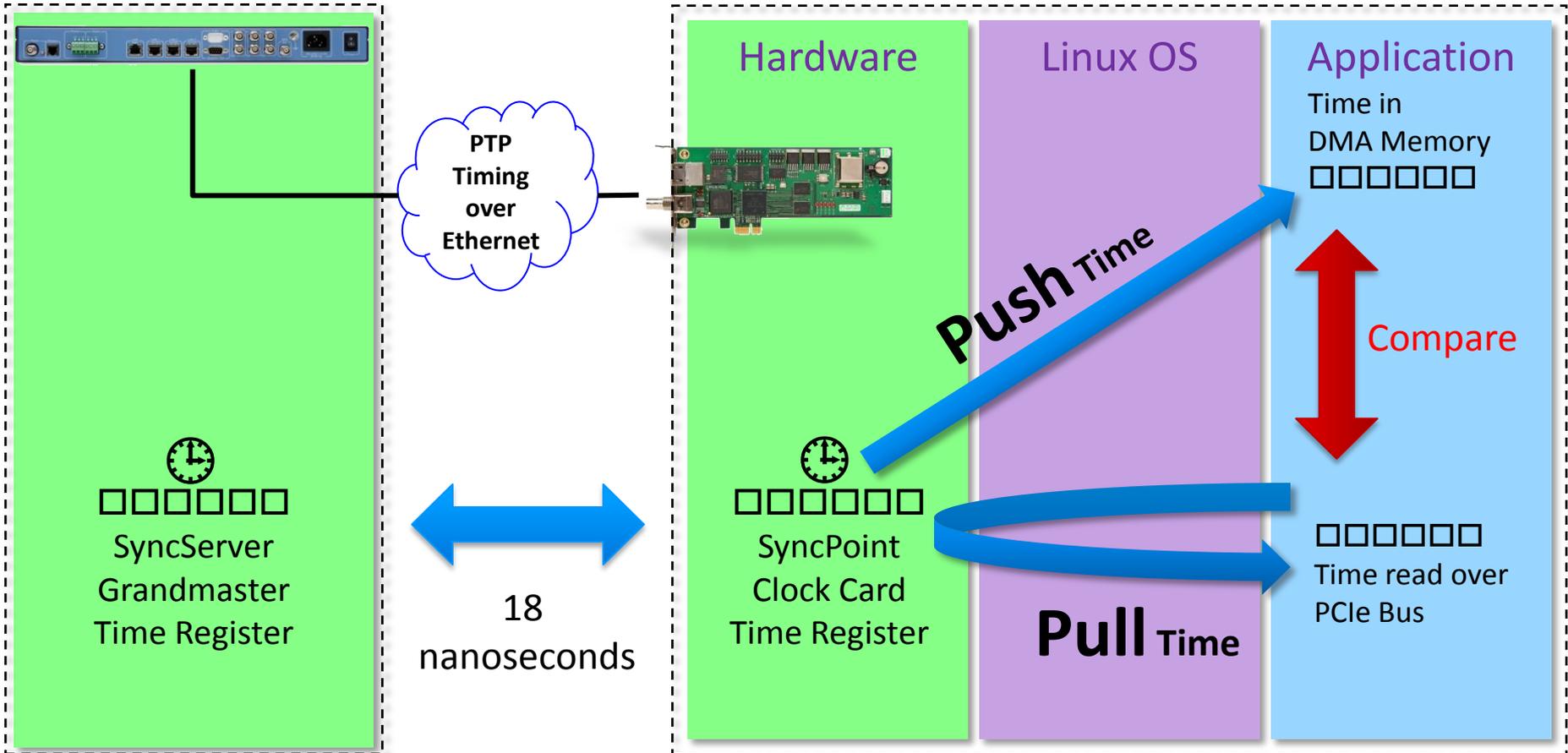
Less than 20 ns delay in reading the time – very consistent

But is it accurate....?

# How to Verify DMA Time Accuracy

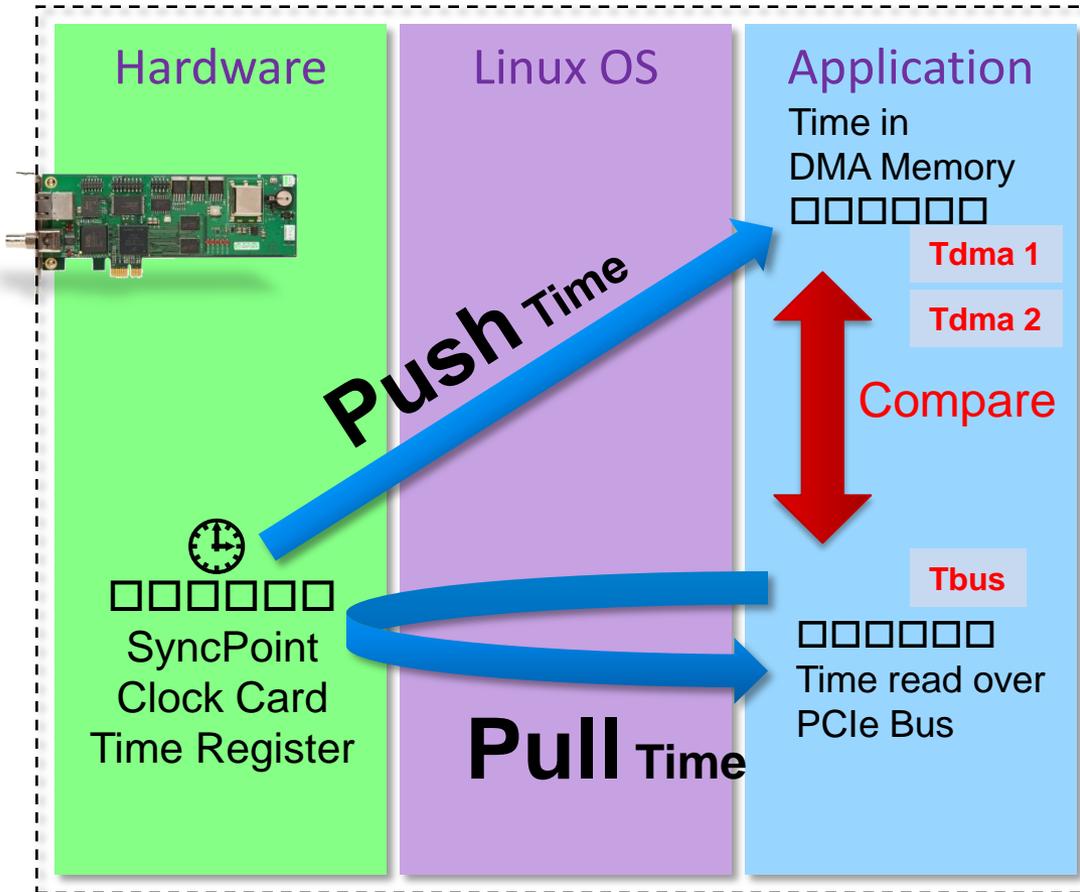
SyncServer S350  
PTP Grandmaster

Compute Server



# Using “Happened-Before”<sup>\*</sup> Analysis...

Compute Server



Process:

1. Read DMA Time: Tdma1
2. Pull PCIe Bus Time: Tbus
3. Read DMA Time: Tdma2

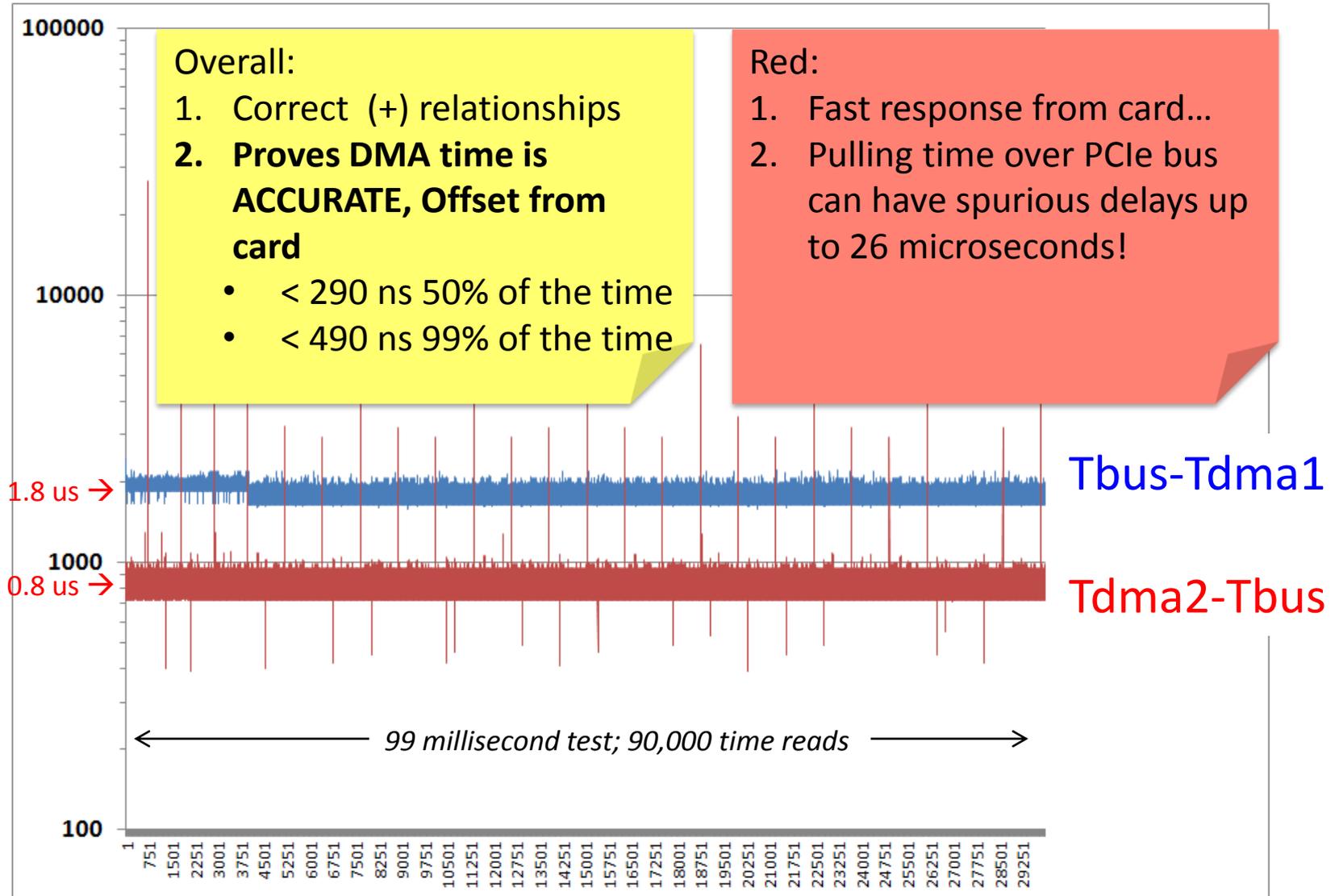
$$T_{dma1} < T_{bus} < T_{dma2}$$

Compute delta times to verify time accuracy range of DMA time stamps

*\*Time reads should be positively monotonic if registers synchronized*

# “Happened-Before” results...

Log scale, nanoseconds



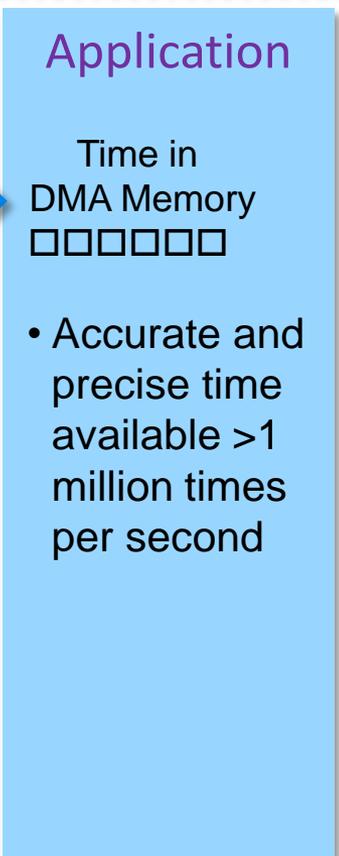
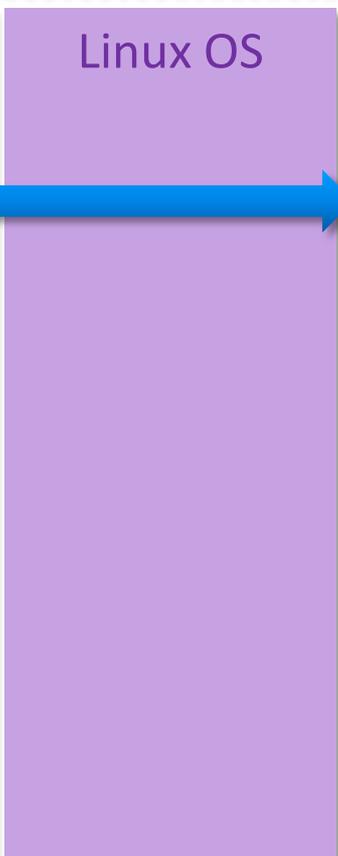
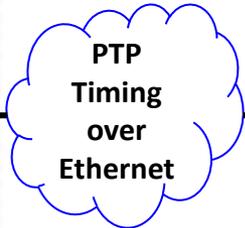
# The Four Timing Problems Solved

SyncServer S350  
PTP Grandmaster

Compute Server



- Precise and Accurate time available globally via GPS
- Optional Atomic oscillator back-up



- Technology to overcome network delays
- Stable OCXO time base
- Hardware time stamping eliminates OS stack delay

Time in DMA Memory  
□□□□□□

- Accurate and precise time available >1 million times per second

# Conclusions

- Verified Symmetricom end-to-end accurate time to  $< 600$  ns UTC to Linux applications using DMA time reads



**SyncServer S350**  
**PTP Grandmaster**

- SyncPoint PCIe-1000 PTP clock card with hardware time stamping, OCXO oscillator and DMA time writes...
  - Improves...
    - Time **accuracy** for Linux applications
    - Time **availability** for Linux applications
  - Reduces...
    - Network delay related errors
    - Stack delay related errors
    - Poor server clock related errors



**SyncPoint**  
**PCIe-1000**  
**PTP Clock Card**

# Thank You

A link to the  
whitepaper will be  
sent to you



Symmetricom, Inc.  
2300 Orchard Parkway  
San Jose, CA 95131-1017  
Tel: +1 408-428-7907  
Fax: +1 408-428-6960

www.symmetricom.com

Speaker: Paul Skoog  
Product Marketing Manager  
pskoog@symmetricom.com