

**Microsemi Corporation**

12/11/2013

**Change Notification No: CN1309**

**CN Change Level: Minor**

**Subject:** Typical program and verify time updated for A2F200 and A2F500

Dear Customer,

Typical program and verify time for SmartFusion cSoCs have been modified for A2F200 and A2F500 devices in “Table 4-3: Typical Programming and Erase Times” due to documentation error in Revision 11 of the datasheet. The updated typical program and verify time are shown in the following table, no change to product fit, form, function and reliability. A2F060 and FlashROM typical programming and erase times have been added to the table.

**Table 4-3 • Typical Programming and Erase Times**

	FPGA Fabric (seconds)			eNVM (seconds)			FlashROM (seconds)		
	A2F060	A2F200	A2F500	A2F060	A2F200	A2F500	A2F060	A2F200	A2F500
Erase	21	21	21	N/A	N/A	N/A	21	21	21
Program	28	35	48	18	39	71	22	22	22
Verify	2	6	12	9	18	37	1	1	1

**Contact Information:** Microsemi SoC Products Group

For any clarifications or questions, please contact Microsemi SoC Tech Support

Technical Support

Web: [www.microsemi.com/soc/mycases](http://www.microsemi.com/soc/mycases)

Phone (NA): 800.262.1060

Phone (Int'l): +1 650.318.4460

Email: [soc\\_tech@microsemi.com](mailto:soc_tech@microsemi.com)

Regards,

Microsemi Corporation



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