

July 19, 2013

Subject: An update to PCN 1206

Dear Microsemi Customer,

Microsemi is revising addendum B of PCN1206, which was issued on June 26, 2012. The initial target implementation (WS and FT Correlation) schedule has been delayed. Please see below for the new implementation schedule.

Table 1 Antifuse

Product Family	End of Current Year
AX	FYQ114
SXA, SX	FYQ413
ACT series	FYQ314
eX	FYQ314
A40MX/42MX	FYQ313

Table 2 Flash FPGAs

Product Family	End of Current Year
ProASIC3 series (A3P/E, A3PN, and A3PL)	FYQ413
IGLOO series (AGL/e, AGLN, and AGLP)	FYQ214
Mixed Signal - SmartFusion and Fusion	FYQ114
ProASIC ^{Plus} (APA)	FYQ214

Regards,

Microsemi Corporation

Please refer to the enclosed supporting documentation (PCN1206, Addendum A and Addendum C) for further details. For any questions, please contact Microsemi SoC Technical Support by email at soc_tech@microsemi.com.

PCN1206: http://www.microsemi.com/soc/documents/PCN1206_Add_ASEM.pdf

Addendum A: ASE (Malaysia) Overview Report:

http://www.microsemi.com/soc/documents/PCN1206_AddendumA_ASE_overview.pdf

Addendum C: Affected Part Number List:

http://www.microsemi.com/soc/documents/PCN1206_AddendumC_affected-partnumber-list.pdf

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