ACT 2 Family FPGAs

The Ordering Information was updated to include RoHS information.

<table>
<thead>
<tr>
<th>Part Number</th>
<th>A1225</th>
<th>2500 Gates</th>
</tr>
</thead>
<tbody>
<tr>
<td></td>
<td>A1240</td>
<td>4000 Gates</td>
</tr>
<tr>
<td></td>
<td>A1280</td>
<td>8000 Gates</td>
</tr>
</tbody>
</table>

### ACT2 Family FPGAs

- **Part Number**: A1225, A1240, A1280
- **Die Revision**: A = 1.0 8m CMOS Process
- **Speed Grade**
  - Blank = Standard Speed
  - D1 = Approximately 15% faster than Standard
  - D2 = Approximately 25% faster than Standard
- **Package Type**
  - PL = Plastic J-Leaded Chip Carrier
  - PQ = Plastic Quad Flat Pack
  - CQ = Ceramic Quad Flat Pack
  - PG = Ceramic Pin Grid Array
  - TQ = Thin (1.4 mm) Quad Flat Pack
  - VQ = Very Thin (1.0 mm) Quad Flat Pack
- **Application (Temperature Range)**
  - C = Commercial (0 to +70°C)
  - I = Industrial (-40 to +85°C)
  - M = Military (-55 to +125°C)
  - B = MIL-STD-883
- **Package Lead Count**: 176
- **Lead-Free Packaging**
  - Blank = Standard Packaging
  - G = RoHS Compliant Packaging
- **RoHS Compliance**
  - Blank = Standard Packaging
  - G = RoHS Compliant Packaging

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