



December 1, 2005

Subject: ACTgen calculates wrong divider value for CCC input frequency for ProASIC[®]3 and ProASIC3E Families

Dear Customer,

This notice is to inform you that in some cases, when generating CCC/PLL macros using ACTgen, the software will fail to generate the correct CCC/PLL output frequencies. This will be fixed in Libero[®] Integrated Design Environment (IDE) v7.0 and Designer v7.0, which will be available in December, 2005. More information is available at <http://actel.com/kb/article.aspx?id=KI51919>. The affected products are listed in Table 1.

Table 1 • ProASIC3/E Products

Device	Package
A3P250	FG144ES
A3P250	FG256ES
A3P250	PQ208ES
A3P250	VQ100ES
A3PE600	FG256EAS
A3PE600	FG484EAS
A3PE600	PQ208EAS
A3PE600	FG256ES
A3PE600	FG484ES
A3PE600	PQ208ES

For questions, please contact the Actel Technical Support hotline at tech@actel.com.

Regards,

Actel Corporation