

Migrating from Engineering Silicon to Production Devices for the Axcelerator Family

Introduction

The Axcelerator family, one of the most recent antifuse offerings from Actel, combines higher density and performance with features like embedded SRAM and expanded I/O standards. To facilitate early design prototyping, Actel has provided users with Engineering Silicon (ES) devices.

With the release of Designer R1-2003 SP3 and pre-production Axcelerator silicon, the ES devices will no longer be available as a valid option. As the production units have enhanced routing and processes, the ES designs are not directly transferable and require a few quick steps to migrate from one device to another. The purpose of this technical brief is to clearly explain the steps involved.

Migration Flow

The following steps summarize the conversion process from ES to production parts:

- 1. Export the EDIF netlist and the Physical Design Constraint (PDC) file from your ES design
- 2. Start a new Designer project, selecting the production part in the Device Selection Wizard
- 3. Import the EDIF netlist and the PDC into the new project
- 4. Compile, then run place-and-route using the Incremental Fix mode

Step 1

To export a PDC file:

1a. From the File Menu, select Export, and click Auxiliary Files from the Export sub-menu. This displays the Export Auxiliary Files dialog box appears, as shown in Figure 1

- File name: Type your file name
- Save as type: Select PDC

Export Auxiliary Files		? ×
Save in: 🖃 SYSTEMFILE	S (C:)	- 🗈 💣 🎟 -
3.62	🚞 COMPAQ	🚞 Gordon's Work
🚞 4_8_0_19_main	🚞 die	🧰 gs
l 🐼 Actel	🚞 die2	🗋 honeywell_d311
💫 Actelprj	Documents and Settings	🗀 honeywell_US
🗋 apsdos	🚞 flexim	🗋 HyperLynx60
🗀 BP	🚞 FOUND.000	🚞 IBIS
CAPTURE	🚞 FullShot99	😣 Libero
		•
File name: ES_mig.pdd		Save
Save as type: PDC Files (*.	pdc)	Cancel

Figure 1 • Export Auxiliary Files



1b. Click Save. An Export Physical Design Constraints dialog box appears, as shown in Figure 2.



Figure 2 • Export Physical Design Constraints (PDC)

• Select "Placement Constraints. All the I/O, Placement & Routing constraints"

1c. Click OK.

To export an EDIF Netlist (EDN) file:

1d. From the File Menu, select Export, and click Netlist from the Export sub-menu. This displays Export Netlist Files dialog box appears, as shown in Figure 3.

Export Netlist Files		? ×
Save in: 🖃 SYSTEM	FILES (C:)	← 🗈 📸 🖬 -
3.62	🗀 COMPAQ	🚞 Gordon's Work
🚊 4_8_0_19_main	🧰 die	🧰 gs
🍪 Actel	🚞 die2	🚞 honeywell_d311
🔒 Actelprj	🚞 Documents and Setting	gs 🚞 honeywell_US
🗋 apsdos	🚞 flexim	🗀 HyperLynx60
🗀 BP	🚞 FOUND.000	🗀 IBIS
🗀 CAPTURE	🚞 FullShot99	😒 Libero
•		<u> </u>
File name: ES_mig.	edn	Save
Save as type: EDIF File	es (*.edn)	Cancel

Figure 3 • Exporting Your EDN File

- File name: Type your file name
- Save as type: Select EDIF Files (*.edn)

1e. Click Save.

Step 2

To start a New Design:

2a. Click the Start New Design button in the main window, or in the File menu, click New. This displays the Setup Design dialog box, as shown in Figure 4.

Setup Design	
Design Name	ES_mig
Select Family	Axcelerator
Working Directory	C:\\\ Browse
Oł	Cancel Help

Figure 4 • Setup Design

2b. Setup Design

- Enter a Design Name. The design name is used in reports and as the default name when saving or exporting files
- Select an Actel product family from the drop down menu list (Axcelerator in this case)
- Specify a working directory (Do not use spaces in the directory name)

2c. From the Tools Menu, click Device Selection

The Device Selection Wizard window appears, as show in Figure 5. The ES option is no longer available. Select the production part, click on Next to fill in the operating conditions as you would for the ES part and continue with "Step 3" on page 4.

Family Axcelerator		
Die	Package	
AX125 AX250 AX500 AX1000 AX2000	484 FBGA 676 FBGA 729 BGA 896 FBGA	
Speed: -3	Die Voltage 1.5	
Cancel < Back	Next >	Help

Figure 5 • Device Selection Wizard



Step 3

To import a source file:

3a. In the File menu, click Import Source Files. This displays the Import Source Files dialog box, as shown in Figure 6.

	S	ource Files		Туре		Add
2					-	Modify
3						
1						Delete
5						
,					-	
3						
)					-	

Figure 6 • Import Source Files

3b. Click the Add button. The Add Source Files dialog appears, as shown in Figure 7.

Add Source	Files designer		<u>?×</u>
i accum.ed i counter.ed i zxc.edn	n In		
File name:	accum.edn		Import
Files of type:	EDIF Files (*.ed*)	•	Cancel

Figure 7 • Add Source File

3c. Select your EDIF netlist and click Import. The File is added to the Import Source Files dialog box, as shown in Figure 8.

	Source Files	Туре	_ +	Add
1	C:\Actelprj\SAMPLES\designer\accum.edn	edn		
2				Modify
3				
4				Delete
5				
6				
7				
8				
9				

Figure 8 • Import Source Files with EDIF File Added

To import a PDC file:

3d. From the File menu, click Import Auxiliary Files. The Import Auxiliary Files dialog appears, as shown in Figure 9.

Import A	Auxiliary Files		X
	Auxiliary Files Type	-	Add
1			
2			Modify
3			
4			Delete
5			
7			
8			
9		•	
	OK Cancel Help		

Figure 9 • Importing a PDC File



3e. Click the Add button. The Add Auxiliary Files dialog box appears, as shown in Figure 10.

Add Auxiliar	y Files			? ×
Look in: 🔁	designer	• + 6	Þ 📥 🎟	
🔊 zxc.pdc				
File name:	zxc.pdc		Imp	ort
Files of type:	Physical Design Constraint Files (*.p	dc) 💌	Can	

Figure 10 • Selecting the PDC File

Filter for your PDC file by selecting Physical Design Constraint Files (*.pdc) from the Files of type drop-down list box as shown on Figure 10.

3f. Select the PDC file and click Import. The file is added to the Import Auxiliary Files dialog box, as shown in Figure 11.

	Auxiliar	y Files	Туре		Add
C:VActelprj	\SAMPLES\designer\z>	c.pdc	pdc		
					Modify
					Delete
_				_	
				_	
_					

Figure 11 • PDC File Added to the Import Auxiliary Files

3g. Click OK. The PDC file is imported into Designer. Any errors appear in the Log Window.

Note: File names or paths with spaces may not import into Designer. Rename the file or path, removing the spaces, and re-import.

Step 4

To layout your design.

4a. There are three ways to initiate the Layout command:

- In the Tools menu, click Layout
- Click the Layout button in the Design Flow
- Click the Layout icon in the toolbar

4b. Set your Layout Options by checking Run Place, Incrementally, and Lock Existing Placement (Fix). The Layout Options dialog box should be as shown in Figure 12.

Layout Options	×
Timing-Driven	OK
🔽 Run Place	Cancel
Incrementally	
Lock Existing Placement (Fix)	Help
Low High	
🔽 Run Route	
Configure	

Figure 12 • Axcelerator Layout Options

4c. Click OK. Layout runs. Status messages appear in the Log window. The designer should then re-verify the timing. This is especially true for asynchronous designs.

Conclusion

By following the simple steps listed above, designers will have no difficulty transferring their Axcelerator ES designs to production devices and making the best use of what the family has to offer.

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