

Smart Battery Management Applications

Developed in the late 1990s to de-couple charging technology from battery chemistry and support platform independence for interchangeable battery modules, Smart Batteries are popping up in everything from laptop computers and cell phones to cordless drills and hand held vacuum cleaners. Several companies now sell off-the-shelf devices that implement standardized Smart Battery Chargers and Smart Battery Management circuits; but new battery chemistries and emerging application spaces are constantly pushing and stretching the fixed functionality of these devices to their limits. The Actel Fusion™ FPGA opens the door for a wave of innovation in Smart Battery management.

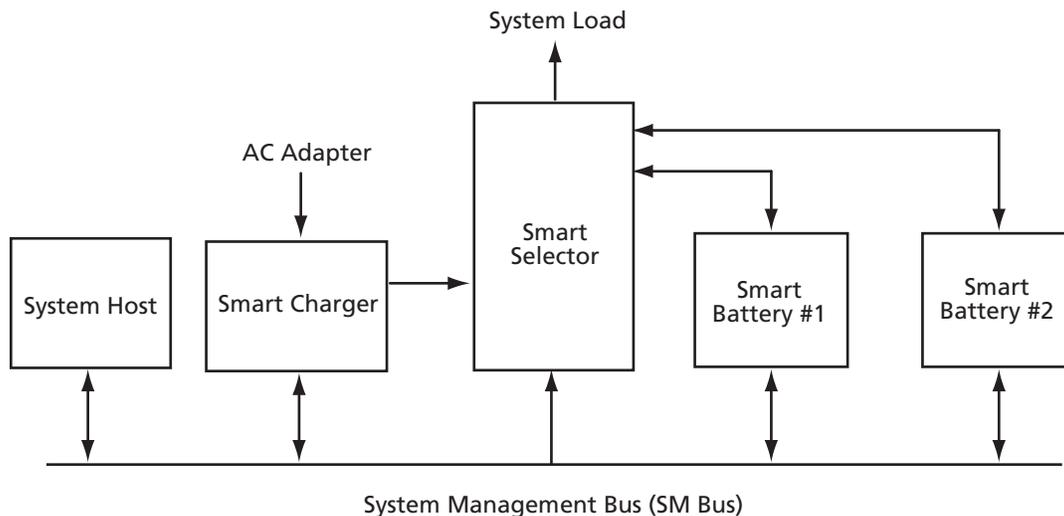


Figure 1 • Smart Battery System Diagram

Illustrated in [Figure 1](#), the Smart Battery concept defines interfaces, a data set, and behaviors of the Smart Battery, Battery Selector, Smart Charger, and Host elements in a Smart Battery System. This application brief focuses on requirements for a Smart Battery and illustrates how Actel Fusion devices can be used to implement all of the necessary functions while leaving room for individual customization.

[Figure 2](#) on [page 2](#) shows a typical Smart Battery implementation using off-the-shelf components.

A Smart Battery generally contains one or more secondary battery cells, an analog monitoring chip, a digital controller chip, various discrete diodes, transistors, passive components, and a redundant safety monitor chip. All are used to monitor voltage, current, and temperature of the cells and manage proper discharge and charging of the battery pack within desired safety limits.

The Actel Fusion FPGA provides integration of analog monitor and digital control capabilities. Some manufacturers may prefer to maintain a separate second "safety chip" for redundancy. The Fusion device has sufficient resources to allow extensive flexibility in implementation.

pull the gate of a power MOSFET to ground. They are ideal for control of the various MOSFET switches required in most Smart Battery implementations.

Unlike most off-the-shelf battery management devices, the Fusion device integrates an RC oscillator, thus eliminating the need for an external crystal oscillator. Coupled through one of the Fusion integrated PLLs and No-Glitch Multiplexer (NGMUX), this unique feature allows you to conserve power in the battery out state by switching to a significantly slower processing clock without external components.

For Smart Battery applications, the Fusion Real-Time Counter (RTC) can be used as an interval timer to periodically wake-up the device and update battery status when the controller is in standby mode. Additionally, this circuitry includes an activity detector that can be used to wake up the device whenever an external event, such as a button press or bus activity, is detected.

Lastly, the Fusion analog section contains an on-board voltage regulator to provide the 1.5 V core voltage for the Fusion device. Running the core of the device at 1.5 V cuts active power dissipation by half. This regulator has an internal shut-down mechanism associated with the RTC circuit described above. The shut-down circuit allows the FPGA logic to initiate standby mode by shutting down the regulator. Then the RTC can wake up the device by starting up the regulator when either an external event occurs, or when the RTC count reaches a programmed value.

Digital Functions

Digital logic in the FPGA fabric can be used to implement the standard System Management Bus (SM Bus) interface, wake-up initialization logic, shut-down state saving logic, and various state-machines or an embedded processor to sequence ADC conversions and perform the various calculations necessary to correctly monitor and control charging and discharging of the Smart Battery. Furthermore, FPGA logic can support unique design features like: battery authentication, additional safety features, unusual multi-cell configurations, or additional features not found in off-the-shelf battery controllers.

Nonvolatile Memory (NVM)

For a Smart Battery application, the NVM is used to store variable states before entering standby or power-down sleep mode and for initializing them whenever the controller wakes. The NVM may also be used to implement ROM coded state machines or to store micro-code instructions for an embedded processor.

Nonvolatility

Nonvolatility is a key feature of the Fusion device that makes it suitable for Smart Battery applications. The Fusion device is truly live at power-up. The Flash-based FPGA fabric retains its programmed state even when power is removed (standby or sleep modes) and is ready for action immediately when core voltage reaches the turn-on threshold. This means that the Fusion FPGA can reliably control MOSFET states during the power-up ramp, and can respond to SM Bus commands and begin active monitoring within microseconds of a wake-up event.

Summary

The Actel Fusion FPGA melds all of the features needed to implement a standard Smart Battery monitor and controller. It handles measurements of analog voltage, current, and temperature. Fusion FPGAs include nonvolatile storage for battery state variables and processor code. In the Actel Fusion FPGA, flexible digital logic gates for state machines and embedded processor functions are fused together into a fully featured, single-chip solution that leaves plenty of room for creativity and innovation.

List of Changes

The following table lists critical changes that were made in the current version of the document.

Previous Version	Changes in Current Version (51900123-1/6.06*)	Page
51900123-0/12.05	The low power modes of operation were updated and clarified.	N/A

Note: *The part number is located on the last page of the document.

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