

LCD Backlight Control

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Introduction

Cell phones and other mobile products increasingly incorporate color LCD panels. Because these devices run on battery power with finite battery life, it is becoming more common to use a white LED that can emit bright light for backlighting while consuming little power. As a result, world demand for white LEDs is rapidly increasing. Monolithic power systems (MPS) and Microsemi are meeting these needs by bringing to market, a Fixed Frequency LCD Backlight Control System that realizes low power consumption, high efficiency, and high precision.

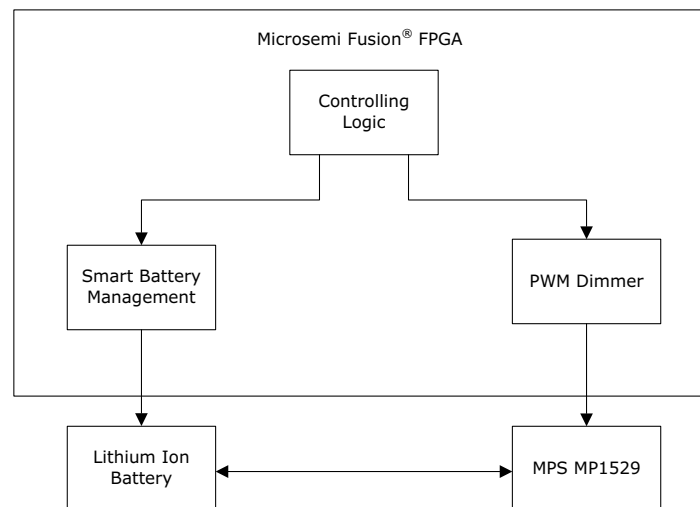


Figure 1 • Fixed Frequency LCD Backlight Control System

MPS MP1529 Fixed Frequency White LED Driver

The MP1529 is a step-up converter plus three current sources for driving two 6-LED backlight strings to 30 mA and a 4-LED flash string to 150 mA from a single Lithium-Ion battery. Three current sources drive in three modes. In backlight mode, two strings of 6 LEDs are lit with up to 30 mA of current. In the backlight plus preview mode, the 4 flash LEDs are lit with the preview current. In flash mode, the flash LEDs are lit with up to 150 mA of current and the two backlight strings are turned off. There are three pins to independently set the backlight, preview, and flash current.

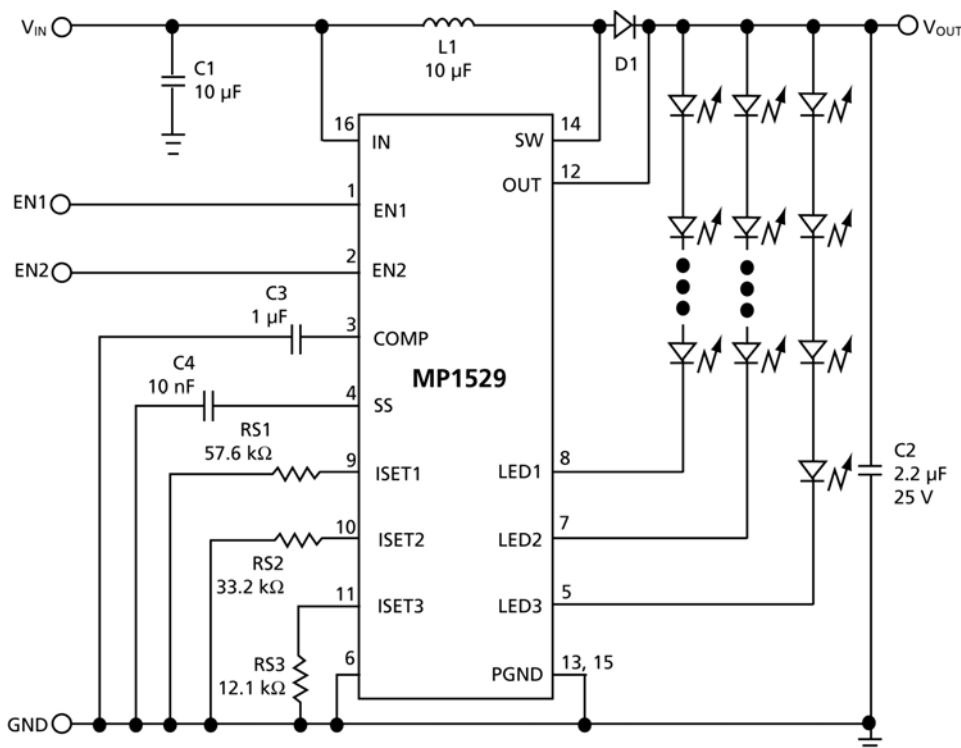


Figure 2 • Typical Application Circuit

Microsemi Fusion Mixed-Signal FPGAs

Microsemi provides all the benefits of a single chip, Flash-based FPGA in Fusion devices, plus a configurable bit analog to digital converter, analog inputs, large embedded Flash memory, numerous control features for power management, thermal management, and other applications.

Microsemi Fusion with MPS Backlight Control

Microsemi Fusion FPGAs offer a one-chip solution that supports integrated smart battery management and PWM generation for LCD backlighting control. The integrated features of Fusion also provide greater flexibility for miniaturization of portable end products. When used in conjunction with its robust power management capabilities, the Fusion FPGA supports the designers need for low power (sleep mode) while maintaining level 0 live at power-up.

Figure 3 shows the block diagram in which the PWM signal is generated through a PWM IP and passed to an appropriate I/O Bank for external interfacing with MP1529.

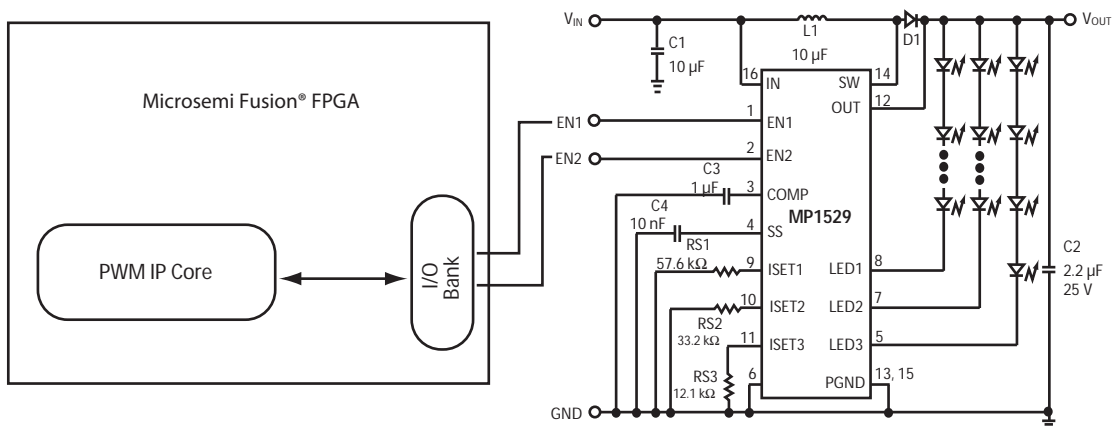


Figure 3 • Fixed Frequency White LED Dimmer

Conclusion

Microsemi Fusion has all of the features needed to implement a Fixed Frequency LCD Backlight Control System. The reprogrammable nature of the FPGA fabric enables designers to freely choose appropriate control logic. The embedded Flash memory and Flash-based FPGA fabric enable a simple single chip solution. The user interface and IP allow customers to implement their applications simply and quickly. All these features tied to Flash FPGA logic, the Fixed Frequency LCD Backlight Control System can be completely customized by the user, offering flexibility unmatched by any other solution, all at a competitive price.

List of Changes

The following table shows important changes made in this document for each revision.

Revision	Changes	Pages
Revision 1 (January 2016)	Non-technical updates.	NA
Revision 0 (June 2006)	Initial release.	NA

**The part number is located on the last page of the document.*



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