GENERAL AGENDA

07:30 – 08:00	*Registration*
08:00 – 08:15	*Kickoff – Welcome*; Siobhan Dolan Clancy
08:15 – 09:15	*Corporate Overview*; Mr Russ Garcia *Microsemi Technology Overview & System Solutions*; Mr Jim Aralis
09:15 – 09:30	*Break – Split into 2 tracks*
09:30 – 10:15	*Presentation Tracks*
10:15 – 10:30	Coffee Break
10:30 – 12:00	*Presentation Tracks* continue
12:00 – 01:15	Lunch
01:15 – 03:30	*Presentation Tracks* continue
03:30 – 03:45	Coffee Break
03:34 – 05:15	*Presentation Tracks* continue
05:15 – 06:15	*General Q&A Session* – Wrap up
06:15 – 07:30	*Cocktail Reception & Networking Event*

Power & Mixed Signal Track

09:30 – 10:15	***Power System Overview***; Mr Patrick Franks
10:15 – 10:30	Coffee Break
10:30 – 11:20	***Space Power Product Update***; Mr Jim Larrauri
11:20 – 12:00	***Analog Mixed Signal Space Product Update***; Mr Bertrand Irissou
12:00 – 01:15	Lunch
01:15 – 02:00	***Microsemi's Approach to Radiation Hardening***; Mr Pierre Irissou
02:00 – 02:45	***Flex Circuit Packaging for Solar Diodes and GaN Transistors***; Mr Tracy Autry
02:45 – 03:30	***Integration of Common Spacecraft Interfaces & Support Functions***; Mr Bruce Ferguson
03:30 – 03:45	Coffee break
03:45 – 04:15	***Distribution Issues between Central Power and POL Architectures***; Mr Brian Wilkinson
04:15 – 04:45	***Designing Radiation -Tolerant Power Supplies for Space FPGAs***; Mr Hans Schmitz
04:45 – 05:15	***Microsemi Reference Design using GaN Demonstrating Performance, Efficiency & Size Benefits***; Mr Patrick Franks

Digital Track

09:30 – 10:15	***FPGA Product Update***; Miss Minh U Nguyen
10:15 – 10:30	Coffee Break
10:30 – 11:00	***Technology Roadmap for Space-Flight FPGAs***; Mr Ken O'Neill
11:00 – 11:30	***Temperature Effect on RT ProASIC3 FPGAs Total Dose***; Mr JJ Wang
11:30 – 12:00	***Next Generation Packaging Technology for Space FPGAs***; Mr Raymond Kuang
12:00 – 01:15	Lunch
01:15 – 01:45	***Microelectronic Simulation and Packaging for Space Applications***; Mr Mike Flatley
01:45 – 02:15	***Space FPGA Common Questions & Solutions***; Mr Bassam Youssef
02:15 – 02:45	***Designing Radiation-Tolerant Power Supplies for Space FPGAs***; Mr Hans Schmitz
02:45 – 03:30	***Distribution Issues between Central Power and POL Architectures***; Mr Brian Wilkinson
03:30 – 03:45	Coffee break
03:45 – 04:15	***Integration of Common Spacecraft Interfaces & Support Functions***; Mr Bruce Ferguson
04:15 – 04:45	***Programming Software Qualification for Space FPGAs***; Mr Solomon Wolday
04:45 – 05:15	***An Approach to Error Correction Code for Large Memory Arrays for Space***; Mr Mike Flatley