



Libero® SoC v11.8 SP4

Release Notes

Introduction

The Libero® system on chip (SoC) v11.8 SP4 is a service pack release for the Libero SoC v11.8 to support the development of safety-critical applications for the IGLOO®2 and SmartFusion®2 families.

Before starting new designs with PolarFire® SoC, PolarFire, IGLOO2, SmartFusion2, and RTG4™ family devices, download and install the latest version of the [Libero SoC Design Suite](#).

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1. Libero SoC Software Release Notes

This document contains important information about the Libero system on chip (SoC) v11.8 SP4 service pack release.

1.1 Customer Notification (CN) and Customer Advisory Notification (CAN) Support

Libero SoC v11.8 SP4 includes changes that address certain important issues.

For more information about these issues, refer to microsemi.com/company/quality/product-notifications/cn/asic-soc-fpga.

1.1.1 Timing Paths May be Missing from Static Timing Analysis (STA) for SmartFusion2 and IGLOO2 Families

Ongoing software quality testing on Libero SoC has found minor Static Timing Analysis (STA) coverage issues, preventing complete analysis of the path through combinational cells for specific scenarios for the SmartFusion2 and IGLOO2 product families. Libero SoC v11.8 SP4 addresses these issues, allowing SmartTime to produce a complete static timing analysis for the SmartFusion2 and IGLOO2 families.

For more information, refer to CN20022.

2. Known Issues and Limitations

Note: Unless stated otherwise, known issues from Libero SoC v11.8, v11.8 SP1, v11.8 SP2, and v11.8 SP3 also apply to Libero SoC v11.8 SP4. Review the [Libero SoC v11.8 Release Notes](#), [Libero SoC v11.8 SP1 Release Notes](#), [Libero SoC v11.8 SP2 Release Notes](#), and [v11.8 SP3 Release Notes](#) for Known Issues in Libero SoC v11.8, v11.8 SP1, v11.8 SP2, and v11.8 SP3.

3. System Requirements

For information about operating system support and minimum system requirements, see the [System Requirements](#) web page.

For Linux OS setup instructions, see [How to Set Up Linux Environment for Libero User Guide](#).

3.1 Operating System Support

Supported

- Windows 7, Windows 8.1, and Windows 10
- RHEL 5 (see note), RHEL 6, RHEL 7, CentOS 5 (see note), CentOS 6, and CentOS 7
- SuSE 11 SP4 (Libero only. FlashPro Express, SmartDebug, and Job Manager are not supported.)

Note: RHEL 5 and CentOS 5 do not support programming using FlashPro5.

Not Supported

- 32-bit operating systems
- Windows XP
- Support for the following operating systems will cease with the next major Libero SoC release:
 - RedHat Enterprise Linux 5.x through 6.5
 - CentOS 5.x through 6.5

4. Download Libero SoC Software

Click the following links to download Libero SoC v11.8 SP4 on Windows and Linux operating systems. Installation requires administrator privileges to the system.

- [Windows](#)
- [Linux](#)
- [MegaVault](#)

Note: There are no new cores for the Libero SoC v11.8 SP4 release, and all Libero SoC v11.8 SP1 cores are compatible with v11.8 SP4. Use the v11.8 SP1 MegaVault link above for this release.

Libero SoC v11.8 SP4 is a full release and can be installed on its own.

After successful installation, clicking **Help-> About Libero** will show Version: 11.8.4.8.

4.1 Downloading SoftConsole 3.4/4.0/5.1

Libero SoC v11.8 SP4 is compatible with SoftConsole v3.4 SP1, SoftConsole v4.0, and SoftConsole v5.x.

- [SoftConsole Download](#)

5. Revision History

Revision	Date	Description
A	04/2021	Initial Revision

6. Microchip FPGA Technical Support

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6.1 Customer Service

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- From North America, call **800.262.1060**
- From the rest of the world, call **650.318.4460**
- Fax, from anywhere in the world, **650.318.8044**

6.2 Customer Technical Support

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You can communicate your technical questions through our Web portal and receive answers back by email, fax, or phone. Also, if you have design problems, you can upload your design files to receive assistance. We constantly monitor the cases created from the web portal throughout the day. When sending your request to us, please be sure to include your full name, company name, and your contact information for efficient processing of your request.

Technical support can be reached at soc.microsemi.com/Portal/Default.aspx.

For technical support on RH and RT FPGAs that are regulated by International Traffic in Arms Regulations (ITAR), log in at soc.microsemi.com/portal/DPortal.aspx, go to the **My Cases** tab, and select **Yes** in the ITAR drop-down list when creating a new case. For a complete list of ITAR-regulated Microchip FPGAs, visit the ITAR web page.

You can track technical cases online by going to [My Cases](#).

6.3 Website

You can browse a variety of technical and non-technical information on the Microchip FPGA Products Group [home page](#), at www.microsemi.com/soc.

6.4 Outside the U.S.

Customers needing assistance outside the US time zones can either contact technical support at (<https://soc.microsemi.com/Portal/Default.aspx>) or contact a local sales office.

Visit [About Us](#) for [sales office listings](#) and [corporate contacts](#).

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- Local Sales Office
- Embedded Solutions Engineer (ESE)
- Technical Support

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PIS_TABLE - Variable missing PIS_EXAMPLE - Variable missing PIS_NOTES - Variable missing

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