

iW ARINC818-2 IP core is compatible with the ARINC818-2 specification. The core supports AXI4-Lite register interface, Streaming Video Tx interface, Streaming Video Rx interface, Transceiver interface and User interface. The ARINC818-2 IP core can be implemented on any transceiver based FPGA.

Applications

- Used as ARINC818-2 Transmitter/ Receiver/ Transmitter & Receiver
- Used in aerospace devices for digital video transmission.

Highlights

- Compliant with ARINC818-2 Specification
- Supports compile time video resolution configuration
- IP Parameter can be configured as per the customer provided Interface Control Document (ICD)
- Supports configurable ADVB video format
- Following transmission medium are supported:
 1. Optical
 2. Copper

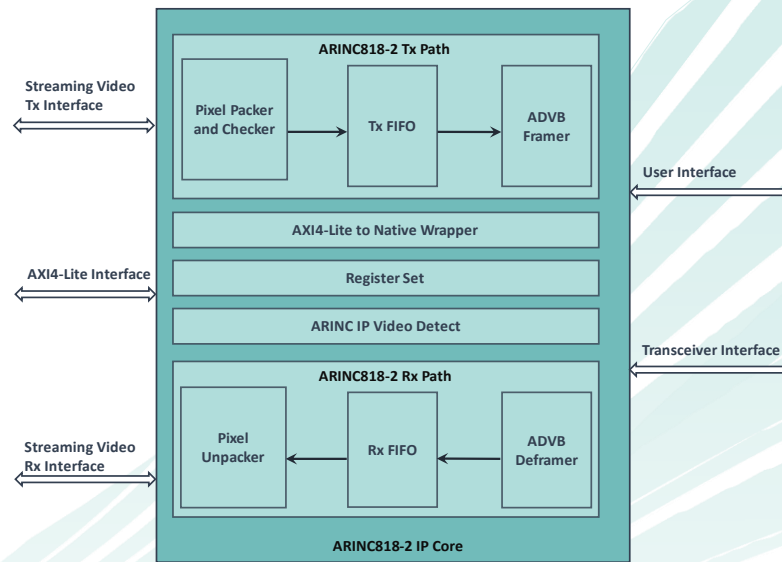
Features

- Supports Progressive and Interlaced Video Formats
- Supports Monochrome, RGB, RGBA and YCbCr Color Formats
- Supports all Pixel Aspect Ratio
- Supports 8-bit Components, four components per transmission word
- Supported Pixel Array Order: Left to Right, Top to Bottom
- Supports Line Synchronous Mode (Line Synchronous to external clock C3)
- Supports Streaming Video interface for Video Transmission & Reception
- Supports 32-bit AXI4 lite interface towards ARINC818-2 IP Core
- Supports user interface

Device Utilization Summary

Device Family	4LTU	DFF	RAM1K20/RAM1K18
PolarFire	3032	2330	14

iW ARINC818-2 block diagram



<https://www.iwavesystems.com/product/arinc818-2-ip-core/>

Deliverables

- RTL source code or Netlist
- IP example design
- IP datasheet
- Integration Manual
- Linux/BareMetal driver reference
- User Manual

Licensing Options

- Non-Transferable: Single Project/Product Netlist License – Single Site
- Non-Transferable: Multi Project/Product Netlist License – Single Site
- Non-Transferable: Single Project/Product RTL Source Code License – Single Site
- Non-Transferable: Multi Project/Product RTL Source Code License – Single Site

Technical Support

iWave provides comprehensive support during your system integration & validation.

- The Client may open a new support incident by emailing to a technical support engineer
- iWave's response time shall be within 24 hours of the initial call, with the details of the action plan to resolve
- Support assistance shall be delivered by telephone, email and/or remote assistance via a web meeting
- iWave shall provide remote debugging support irrespective of the time zone/ region

iWave Systems, a leading FPGA design house enhances your design productivity by providing an extensive suite of proven, optimized and easy-to-use FPGA IP Cores along with reference designs to complement and quicken your applications development. Our extensive suite of IP Cores covers all key markets and applications. Along with the rich set of FPGA IP cores, iWave also offers custom FPGA designs tailored to meet the client specifications which includes RTL Design, Integration of iWave's or 3rd Party IP Cores on our FPGA SOMs with Carrier Card / Custom Hardware / Off-the-Shelf Evaluation Kits to provide end-to-end solutions targeting Low-Power, High-Performance and Optimized Designs.

iW ARINC818-2 FPGA IP

The IP can be ordered online from the iWave Website <http://www.iwavesystems.com>

Or from our Local Partners in your region <http://www.iwavesystems.com/about-us/business-partner.html>