

## CoreSF2Reset v3.0 Release Notes

These release notes accompany the production release of CoreSF2Reset v3.0. This document provides details about the features, enhancements, system requirements, supported families, implementations, and known issues and workarounds.

#### **Features**

- Sequences reset signals in a SmartFusion<sup>®</sup>2 device.
- Interacts with CoreSF2Config to ensure that the reset releases are coordinated with the initialization of the
  peripheral blocks in a SmartFusion2 device, including the microcontroller subsystem double data rate
  (MDDR) controller, fabric DDR (FDDR) controller, and high speed serial interface (also known as
  SERDESIF), when any of these are in use.

## **Delivery Types**

CoreSF2Reset is licensed in two ways: Obfuscated and register transfer level (RTL).

#### **Obfuscated**

Complete RTL code is provided for the core. This enables the core to be instantiated with SmartDesign. Simulation, synthesis, and layout can be performed within Libero® System-on-Chip (SoC). The RTL code for the core is obfuscated.

#### **RTL**

Complete RTL source code is provided for the core.

## Supported Families

• SmartFusion2

### Supported Tool Flows

Use Libero v11.0 software or later with CoreSF2Reset v3.0 release.

#### Installation Instructions

CoreSF2Reset is available through the Libero SoC IP Catalog. The core can be downloaded from a remote web-based repository to the local vault. Once installed in Libero SoC, the core can be instantiated, configured, connected, and generated using the SmartDesign tool. The System Builder tool automatically instantiates and connects CoreSF2Reset when constructing a design.

### **Known Issues and Workarounds**

There are no known issues in this release.



# Release History

Table 1 provides the release history of CoreSF2Reset.

 Table 1. Release History of CoreSF2Reset

Version	Date	Changes
3.0	June 2013	Added support for two more SERDESIF blocks. Up to four SERDESIF blocks can now be supported.
1.0	March 2013	First production release



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