

# CoreMACFilter v2.0 Release Notes

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These release notes accompany the production release of CoreMACFilter v2.0. This document provides details about the features, enhancements, system requirements, supported families, implementations, and known issues and workarounds.

## Key Features

- Provides advanced peripheral bus (APB) interface for control and status register access
- Supports unicast, multicast, and broadcast type of packets
- Supports hash based address filtering for unicast and multicast packets
- Provides mechanism to the upper layer to reject or accept the frames

## Interfaces

APB interface for control and status register access.

## Delivery Types

CoreMACFilter requires a register transfer level (RTL) license to be used and instantiated. Complete RTL source code is provided for the core.

## Supported Families

- SmartFusion<sup>®</sup>2
- IGLOO<sup>®</sup>2

## Supported Tool Flows

- CoreMACFilter v2.0 requires Libero<sup>®</sup> System-on-Chip (SoC) software v11.3.
- Microsemi<sup>®</sup> SoC Products Group Libero software v11.3 can be used with CoreMACFilter.

## Installation Instructions

The CoreMACFilter CPZ must be installed into Libero software. This is done automatically through the Catalog update function in Libero, or the CPZ file can be manually added using the Add Core catalog feature. Once the CPZ file is installed in Libero, the core can be configured, generated, and instantiated within SmartDesign for inclusion in the Libero project.

Refer to the [Using DirectCore in Libero IDE User Guide](#) or [Libero SoC online help](#) for further instructions on core installation, licensing, and general use.

## Documentation

This release contains a copy of the *CoreMACFilter Handbook*. The handbook describes the core functionality and gives step-by-step instructions on how to simulate, synthesize, and place-and-route this core, and also implementation suggestions. Refer to the Libero online help for instructions on obtaining IP documentation.

For updates and additional information about the software, devices, and hardware, visit the Intellectual property pages on the Microsemi SoC Products Group website: [www.microsemi.com/soc](http://www.microsemi.com/soc).

## Supported Test Environments

Unit level test environment is not supported.

## Release History

Table 1 lists the release history for this document.

**Table 1.** Release History

Version	Date	Changes
2.0	July 2014	Initial release supports windows and Linux.

## Resolved Issues in the v1.0 Release

There is no software action request (SAR) resolved as this the first release.

## Discontinued Features and Devices

There are no discontinued features or devices.

## Known Limitations and Workarounds

- In Serial Gigabit Media Independent Interface (SGMII) mode of operation receive status counters may have unexpected values when address based filtering is enabled.



**Microsemi Corporate Headquarters**  
One Enterprise, Aliso Viejo CA 92656 USA  
Within the USA: +1(949) 380-6100  
Sales: +1 (949) 380-6136  
Fax: +1 (949) 215-4996  
E-mail: [sales.support@microsemi.com](mailto:sales.support@microsemi.com)

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