

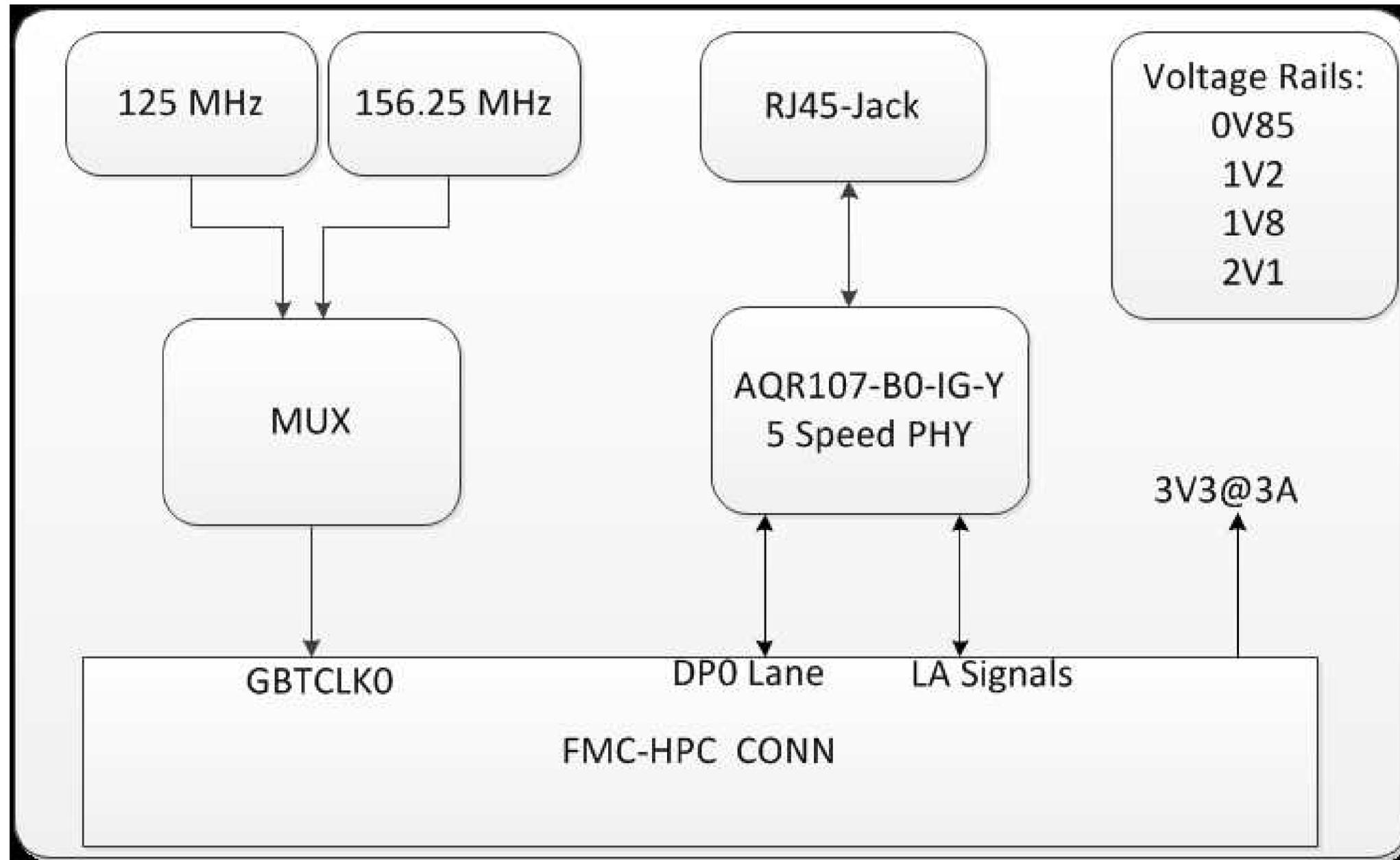
# 10G-Daughter Board

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04	CLOCK MULTIPLEXER
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06	POWER SUPPLIES-1: 0.85V
07	POWER SUPPLIES-2-3



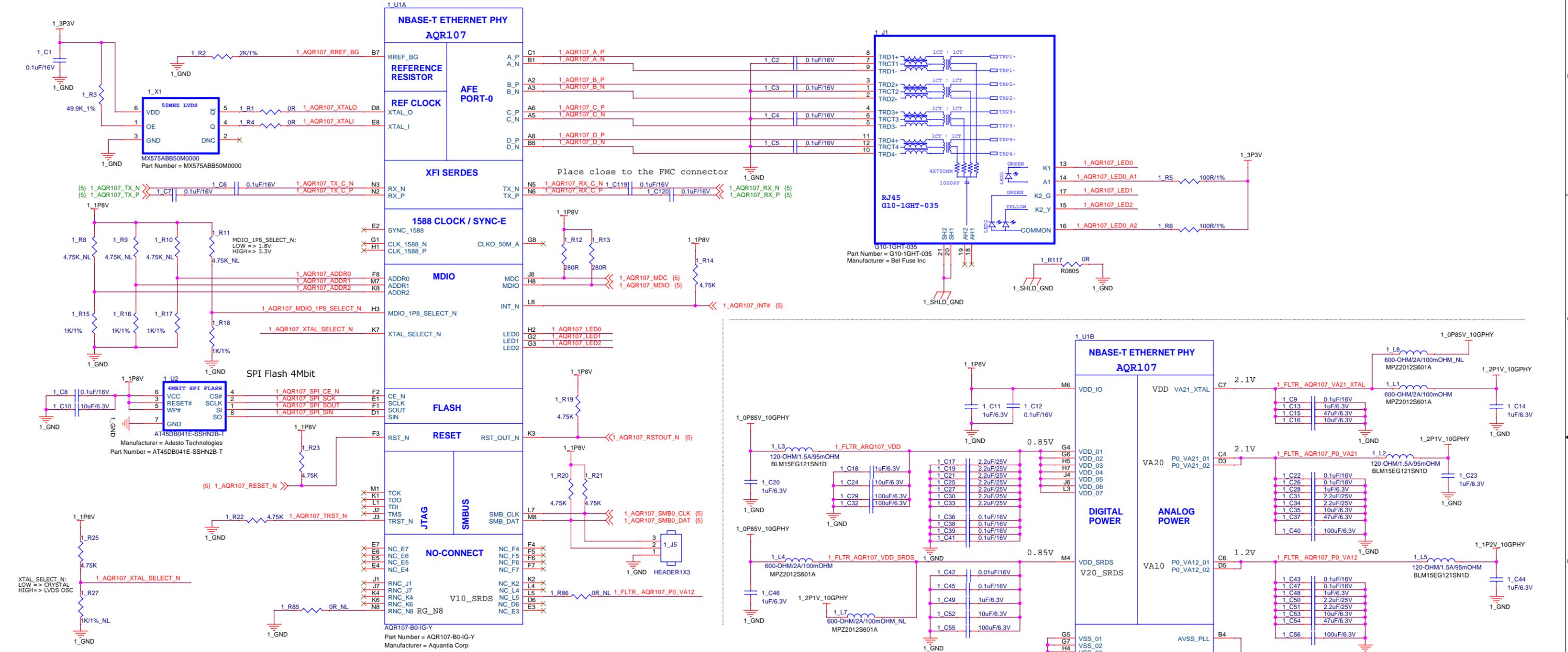
Project Name:		
10G-Daughter Board		
Rev	Part Number:	
1	MICROSEMI DVP-100-000513-001	PACTRON:
Originator:		
GHURU KUMARAVELU		
Size	Date:	
A	Thursday, November 29, 2018	
		Sheet 1 of 12

# 10G-Daughter Board BLOCK\_DIAGRAM

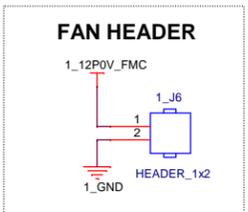
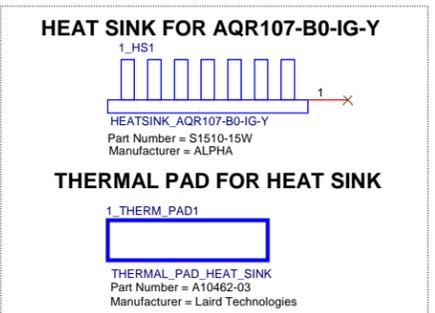


<b>TITLE</b>			
10G-Daughter Board			
Microsemi			
<b>SIZE</b>	<b>DOCUMENT NO.</b>	<b>REV</b>	
A	DVP-100-000513-001	1	
<b>DATE:</b> Thursday, November 29, 2018			<b>SH 2 OF 12</b>

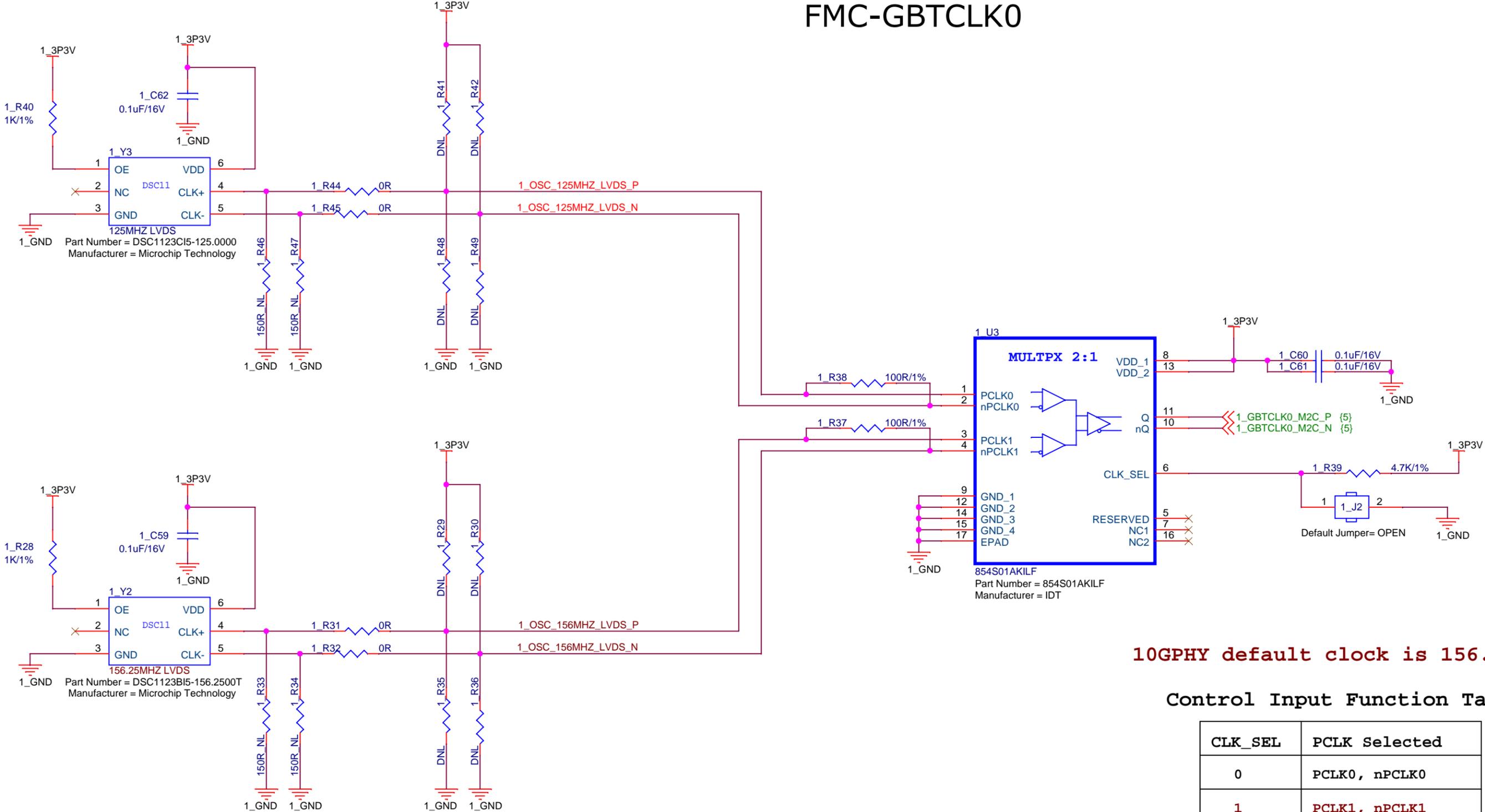
# 10G Ethernet 10G\5G\2.5G\1G\100M



REFDES	AQR107	AQR113
1_R89	LOAD	NO LOAD
1_R90	NO LOAD	LOAD
1_R91	NO LOAD	LOAD
1_L4	LOAD	NO LOAD
1_L1	LOAD	NO LOAD
1_R85	NO LOAD	LOAD
1_R86	NO LOAD	LOAD
1_R93	LOAD	NO LOAD
1_L26	NO LOAD	LOAD
1_L8	NO LOAD	LOAD



# DIFFERENTIAL-TO-LVDS CLOCK MULTIPLEXER FMC-GBTCLK0



10GPHY default clock is 156.25 MHz

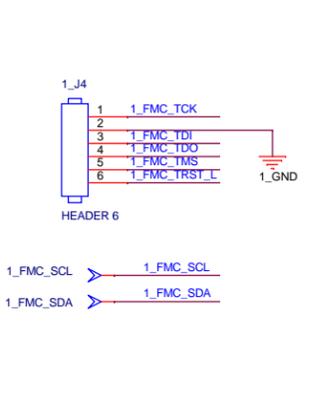
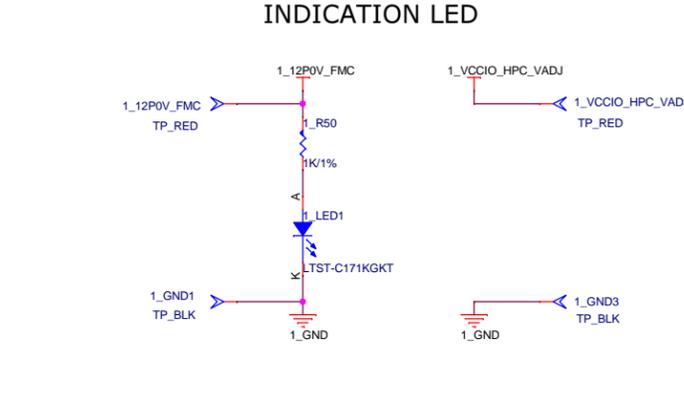
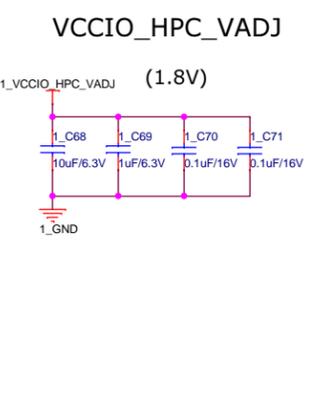
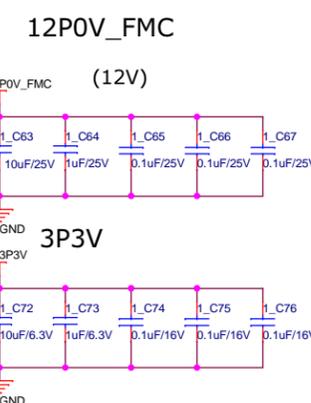
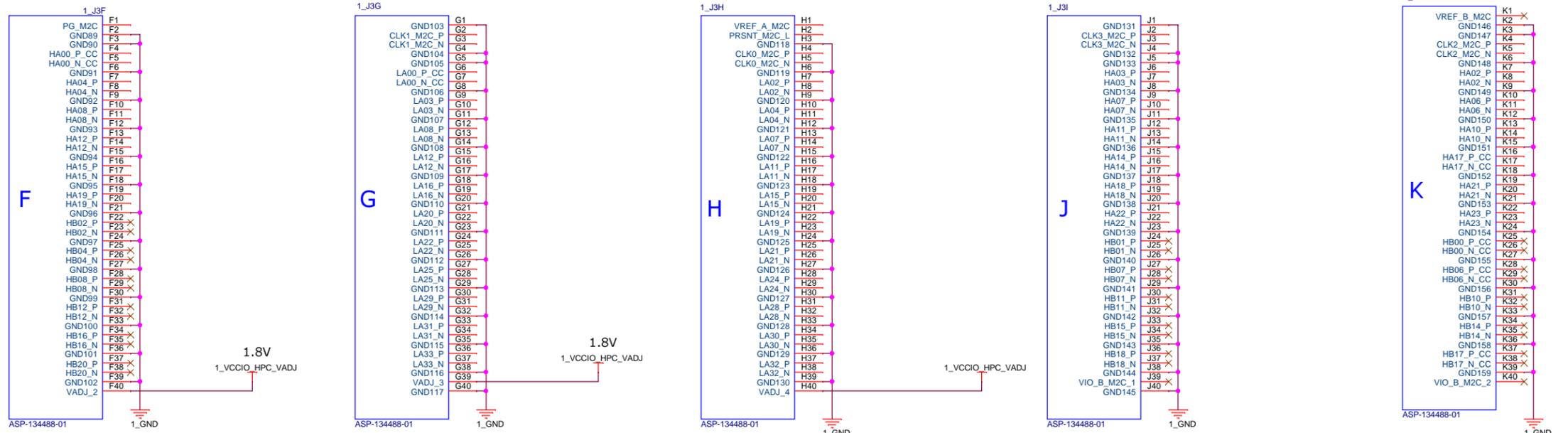
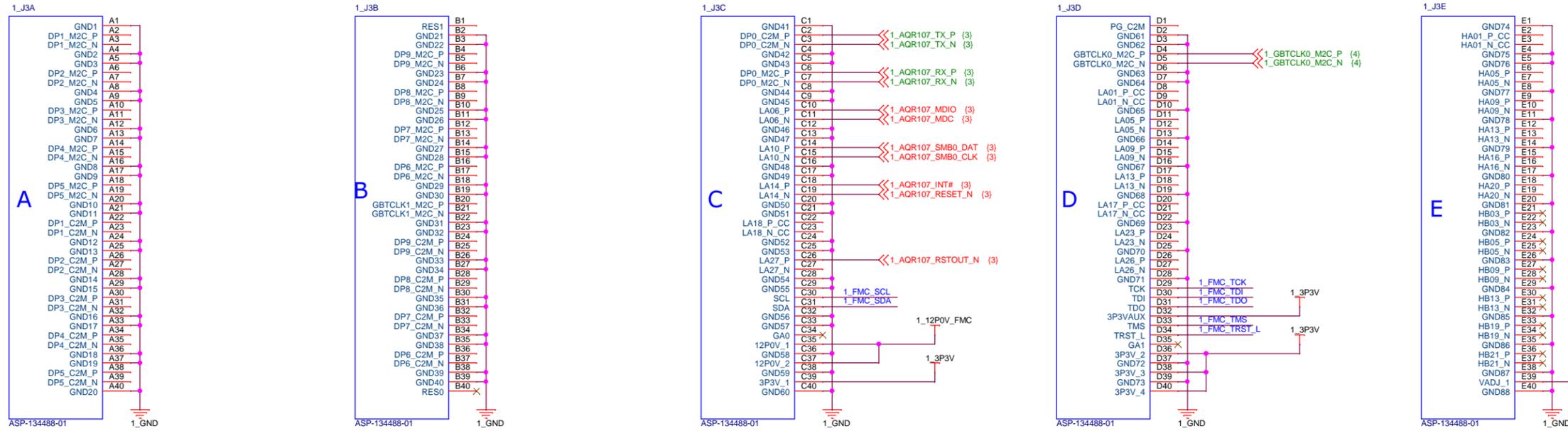
**Control Input Function Table**

CLK_SEL	PCLK Selected
0	PCLK0, nPCLK0
1	PCLK1, nPCLK1



TITLE: 10G-Daughter Board		
Microsemi		
SIZE: B	DOCUMENT NO. DVP-100-000513-001	REV: 1
DATE: Thursday, November 29, 2018	SH 4 OF 12	

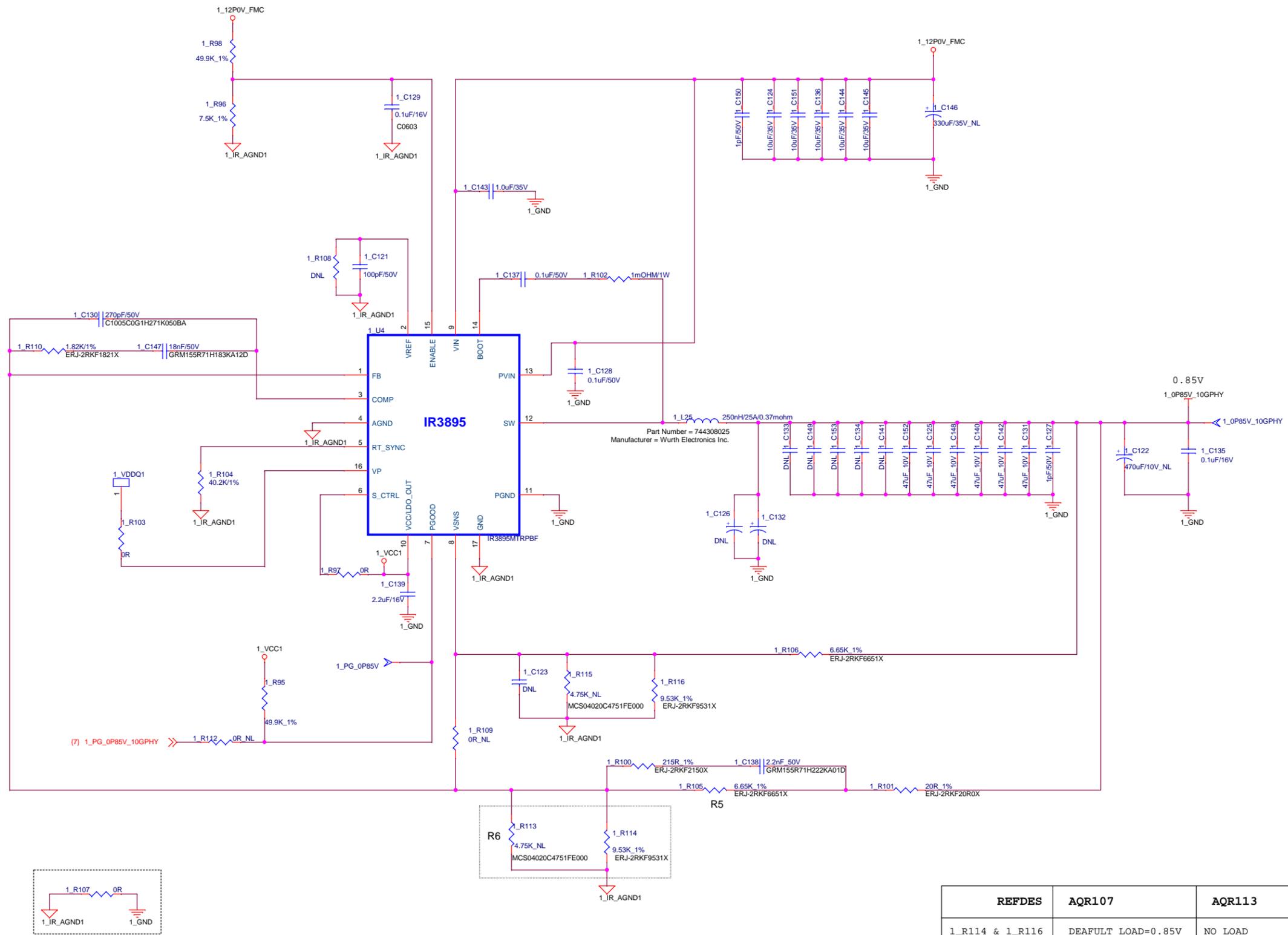
# FMC Male Connection



TITLE 10G-Daughter Board			
Microsemi			
SIZE C	DOCUMENT NO. DVP-100-000513-001	REV 1	
DATE: Thursday, November 29, 2018		SH 5	OF 12

# REGULATOR-1 For 10GPHY: 0.85V @ 16A

POWER SEQUENCE:  
10G PHY:0V85-->1V2-->2V1-->1V8 (100ms)

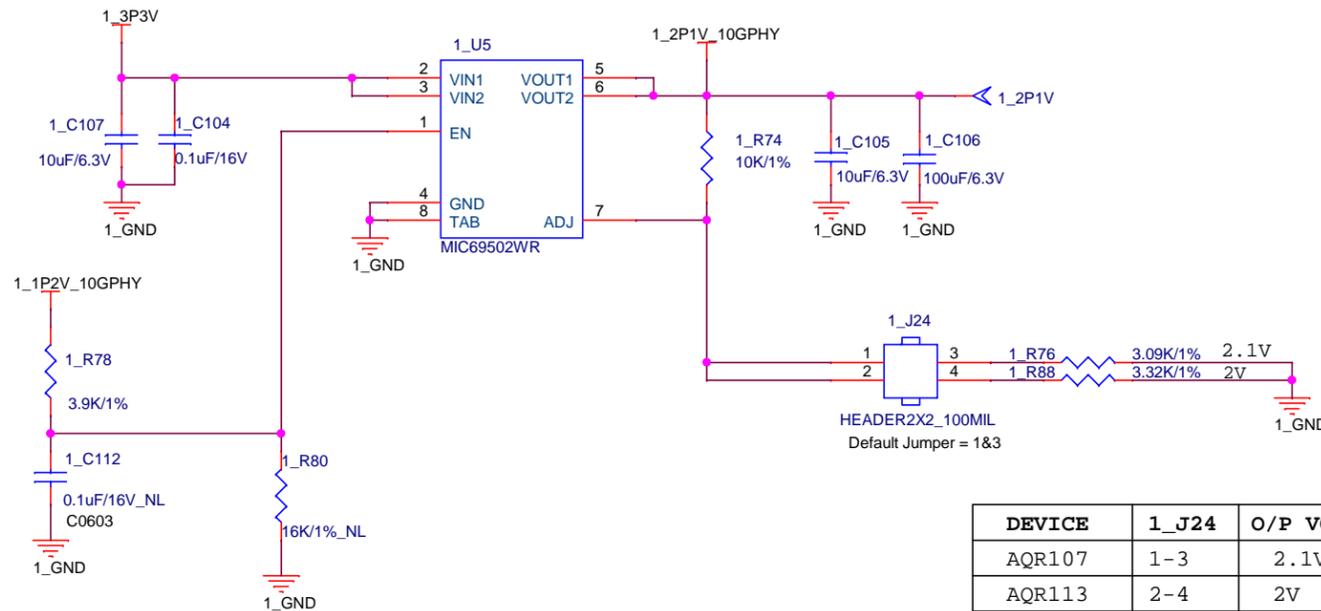


REFDES	AQR107	AQR113
1_R114 & 1_R116	DEAFULT LOAD=0.85V	NO LOAD
1_R113 & 1_R115	NO LOAD	DEAFULT LOAD=1.2V



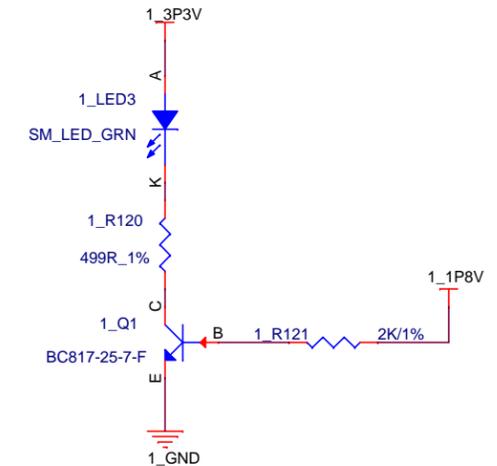
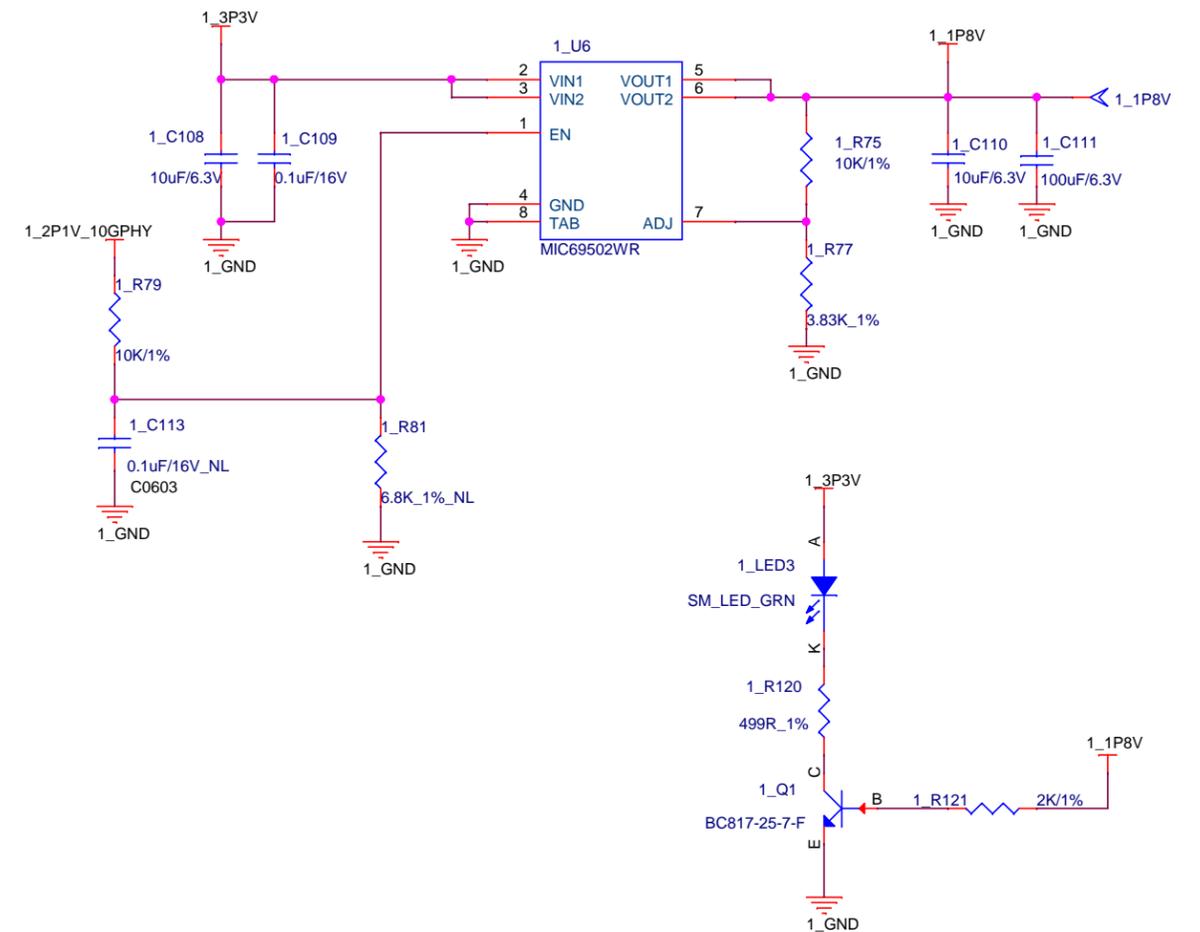
TITLE			
10G-Daughter Board			
Microsemi			
SIZE	DOCUMENT NO.	DVP-100-000513-001	REV
C			1
DATE:	Thursday, November 29, 2018		SH 6 OF 12

### REGULATOR-2 For 10G PHY: 2.1V @ 5A



DEVICE	1_J24	O/P VOLT
AQR107	1-3	2.1V
AQR113	2-4	2V

### REGULATOR-3 For 10GPHY: 1.8V @ 5A



DEVICE	1_J25	O/P VOLT
AQR107	1-3	1.2V
AQR113	2-4	1V

POWER SEQUENCE:  
10G PHY: 0V85-->1V2-->2V1-->1V8 (100ms)



TITLE			
10G-Daughter Board			
Microsemi			
SIZE	DOCUMENT NO.	DVP-100-000513-001	REV
Custom			1
DATE:	Thursday, November 29, 2018		SH 7 OF 12