Microsemi Corporation

October 4, 2019

Customer Notification No: CN19022

Subject

PolarFire FPGA HS_IO_PAUSE Issue

Description of Change

The gearboxes within the PolarFire IOD logic include a HS_IO_CLK_PAUSE signal. This signal is found to be sensitive to constraints in Libero SoC place and route iterations.

Reason for Change

Starting with Libero SoC v12.2, place and route algorithms along with specific IO cores have been updated to be correctly generated and constrained by LiberoSoC automatically for user applications.

Application Impact

In projects using Libero SoC v12.1 or earlier, missing constraint coverage of the IOD cores have manifested as improper HS_IO_CLK_PAUSE signal behavior in some placed and routed designs.

Action Required

Download the following IO interface cores from the IP catalog and regenerate the design with the updated cores.

Existing Libero projects using any of the following three cores will be required to migrate to the latest versions when the project is opened in Libero SoC v12.2

- PolarFire QDR 1.6.103
- PolarFire IOD Generic Receive Interfaces 1.4.105
- PolarFire IOD Generic Transmit Interfaces 1.3.103

Contact Information

If you have further questions about this subject, contact Microsemi Technical Support department by using the support portal at https://soc.microsemi.com/Portal/Default.aspx

Regards,

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Page 1 **of** 2

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Page 2 of 2