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The revision history describes the changes that were implemented in the document. The changes are listed by revision, starting with the most current publication.

1.1 Revision 4.0
- Added clocking and reset structure, see Clocking Structure, page 5 and Reset Structure, page 6.
- The demo was updated to support 1080p video input through HDMI 2.0 Rx port.

1.2 Revision 3.0
The demo was updated to support 4K resolution HDMI output.

1.3 Revision 2.0
Added the image panning feature in Running the Demo, page 10.

1.4 Revision 1.0
The first publication of this document.
This document describes how to run the imaging and video demo using the PolarFire Video Kit, Dual Camera sensor module, and a HDMI monitor. The demo design features a fully integrated solution developed using Microsemi Libero SoC software to help customers evaluate PolarFire in smart embedded vision applications and to build prototypes quickly. For more information, see Smart Embedded Vision page.

The demo demonstrates the following functions:

- MIPI CSI-2 RX to read the 4K dual camera input
- CFA (Color filter array) to RGB (Red, Green, Blue) conversion
- Display controller
- Picture in Picture (PIP)
- 4K image panning through 1920x1080 output via HDMI 1.4 TX port
- 1080p video input through HDMI 2.0 RX port
- 4K video output via HDMI 2.0 TX port
- Edge detection
- Image enhancements such as contrast, brightness, and color balance

**Note:** The solution includes a user-friendly GUI to control these image/video settings.

The PolarFire Video Kit (DVP-102-000512-001) features:

- A 300K LE FPGA (MPF300T, FCG1152)
- HDMI 1.4 transmitter (ADV7511) chipset and corresponding connector
- HDMI 2.0 with rail clamps, ReDrivers and corresponding connectors
- Dual camera sensor featuring IMX334 Sony image sensor
- Image sensor interface to support up to two MIPI CSI-2 cameras
- DSI Interface
- NVIDIA Jetson Interface (MIPI CSI-2 TX connector)
- A High Pin Count (HPC) FMC connector to connect to high-speed interfaces (like 12G-SDI and USXGMII)

For more information about this video kit, see https://www.microsemi.com/existing-parts/parts/150747.
The following figure highlights the PolarFire Video Kit features:

**Figure 1 • PolarFire Video Kit (DVP-102-000512-001)**

2.1 Design Requirements

The following table lists the hardware and software required to run the demo.

**Table 1 • Design Requirements**

<table>
<thead>
<tr>
<th>Design Requirement</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td><strong>Hardware</strong></td>
<td></td>
</tr>
<tr>
<td>-PolarFire VIDEO KIT</td>
<td>DVP-102-000512-001 REV 1.0</td>
</tr>
<tr>
<td>-Image Sensor module</td>
<td>LI-IMX334-MIPI-MICRO v1.0</td>
</tr>
<tr>
<td>-USB A to mini-B cable</td>
<td>Required for: -FPGA programming</td>
</tr>
<tr>
<td></td>
<td>-UART interface with the Video Control GUI</td>
</tr>
<tr>
<td>-HDMI cable</td>
<td>HDMI A Male to Male cable</td>
</tr>
<tr>
<td>-HDMI monitor</td>
<td>2 HDMI monitors are required:</td>
</tr>
<tr>
<td></td>
<td>-A 1920 x1080 60Hz resolution monitor for the HDMI 1.4 TX port.</td>
</tr>
<tr>
<td></td>
<td>-A 4K resolution monitor for the HDMI 2.0 TX port.</td>
</tr>
<tr>
<td>-Power Adapter</td>
<td>12V, 5A</td>
</tr>
<tr>
<td>-Host PC</td>
<td>A host PC with USB port</td>
</tr>
<tr>
<td><strong>Software</strong></td>
<td></td>
</tr>
<tr>
<td>-Program_Debug_v12.1_win.exe</td>
<td>This executable file installs FlashPro Express, used to program the FPGA.</td>
</tr>
</tbody>
</table>
2.2 Prerequisites

Before you start:

1. Download the programming job file from:
   http://soc.microsemi.com/download/rsc/?f=mpf_dg0849_liberosoc_jb
2. Download the GUI from:
   http://soc.microsemi.com/download/rsc/?f=mpf_dg0849_liberosoc_gui
3. Download and install the Program and Debug software from:

Note: On this web page, download the Program_Debug_v12.1_win.exe binary file. Installation of this executable file installs FlashPro Express and SmartDebug used for FPGA programming and debugging. FlashPro Express is used in this demo.

Note: The Program and Debug Software also installs the drivers on the host PC to detect the COM port for running the demo. Install this software whenever the demo is run on a different host PC.

2.3 Demo Resources

The mpf_dg0849_liberosoc_jb folder contains the following resources:

- A job file (JOB file): This file includes the FPGA bitstream and the SPI Flash image to be programmed.

For programming the FPGA using the job file, see Programming the PolarFire Device, page 7.
2.4 Clocking Structure

The clocks generated from the onboard oscillator are used in the demo design. Using the 27 MHz reference frequency from the onboard oscillator, PF_CCC_C0 generates a 150 MHz clock for the HDMI_RX, and a 50 MHz clock for the Mi-V processor subsystem and DDR Controller. Using the reference frequency of 200 MHz (DDR System clock), PF_CCC_C1 generates a 148.5 MHz clock for XCVR_ERM_C0 (HDMI) and Display Controller. Using the 148.5 MHz onboard oscillators, PF_XCVR_REF_CLK generates the following clocks:

- 148.5 MHz clock (LANE0_TX_CLK_R) for the 4K User Logic.

Using the reference frequency of 150 MHz, PF_CCC generates a 120 MHz clock for MIPI_RX_DECODER.

The following figure shows the clocking structure.

Figure 2 • Clocking Structure
2.5 **Reset Structure**

The Mi-V processor subsystem, XCVR, UART, ISP, and all other blocks are reset when the following signals are asserted:

- **DEVICE_INIT_DONE** from PF_INIT_MONITOR
- **DDR_CTRL_RDY** from DDR Controller
- **DDR_PLL_LOCK**
- **PLL_LOCK** of PF_CCC_C0

The following figure shows the reset structure.

*Figure 3 • Reset Structure*

2.6 **Installing the Demo GUI**

To install the GUI:

1. Extract the contents of the `mpf_dg0849_liberosoc_gui.rar` file and run the `setup.exe` file.
2. Click Yes for any message from User Account Control.
   The **Video Control GUI** installation wizard is displayed.
3. Confirm the installation directory locations for the GUI and the National Instruments products and click **Next**.
4. Accept the license agreement, and click **Next**.
5. Review the summary and click **Next**.
   The installation proceeds with a progress bar. After the installation, a confirmation message is displayed.
2.7 Setting Up the Demo

Setting up the demo involves the following steps:
1. Setting Up the Hardware, page 7
2. Programming the PolarFire Device, page 7

2.7.1 Setting Up the Hardware

Setting up the hardware involves interfacing the dual camera sensor module and the HDMI monitor with the PolarFire Video Kit, and verifying the jumper settings.

Follow these steps:
1. Connect the J1 connector of the dual camera sensor module to J38 interface of the video kit.
2. Connect the Full HD HDMI monitor to J2 (HDMI 1.4 TX port) of the video kit using the HDMI cable.
3. Connect the 4K HDMI monitor to J1 (HDMI 2.0 TX port) of the video kit.
4. Connect the host PC and the video kit through J12 of the video kit using the USB mini cable.
5. Connect the power supply cable to J20 of the video kit.
6. Ensure that the following jumper settings are set on the video kit.
7. Power-up the HDMI monitor.
8. Power-up the board using the SW4 slide switch.

The PolarFire dual camera video and imaging hardware is set up. See the following section to program the PolarFire device.

9. Connect the HDMI port of a HDMI output device such as laptop to the HDMI 2.0 RX port J35 connector on the video kit through an additional HDMI male to male cable. Make sure the HDMI output device uses a resolution of 1920 x1080p 60 Hz.
10. The video in the HDMI monitor is automatically switched to the video from HDMI input source.
11. Disconnect the HDMI RX cable to switch back to camera video.

2.7.2 Programming the PolarFire Device

This chapter describes how to program the PolarFire device with the job file using Flashpro Express. The job file is available at the following design files folder location:

<$Download_Directory>\mpf_dg0849_liberosoc_jb

Table 2 • Jumper and Switch Settings

<table>
<thead>
<tr>
<th>Jumper</th>
<th>Default Position</th>
<th>Functionality</th>
</tr>
</thead>
<tbody>
<tr>
<td>J15</td>
<td>Open</td>
<td>SPI Slave and Master mode selection. By default, SPI master.</td>
</tr>
<tr>
<td>J17</td>
<td>Open</td>
<td>100K PD for TRSTn. By default, 1K PD is connected.</td>
</tr>
<tr>
<td>J19</td>
<td>Pin 1&amp;2</td>
<td>Default: XCVR_VREF is connected to GND.</td>
</tr>
<tr>
<td>J28</td>
<td>Pin 1&amp;2</td>
<td>Default: Programming through the FTDI.</td>
</tr>
<tr>
<td>J24</td>
<td>Pin 2&amp;4</td>
<td>Default: VDDAUX4 voltage is set to 3V3.</td>
</tr>
<tr>
<td>J25</td>
<td>Pin 5&amp;6</td>
<td>Default: Bank4 voltage is set to 1V8.</td>
</tr>
<tr>
<td>J36</td>
<td>Pin 1&amp;2</td>
<td>Default: Board power up through the SW4.</td>
</tr>
<tr>
<td>SW4</td>
<td>OFF (Pin 2-3,5-6 Positions)</td>
<td>Power ON/OFF switch.</td>
</tr>
<tr>
<td>SW6</td>
<td>OFF</td>
<td>User slide switch. Default position: OFF.</td>
</tr>
<tr>
<td>J20</td>
<td>12 Volts Input</td>
<td>12V input to the board.</td>
</tr>
</tbody>
</table>
Follow these steps:

1. On the host PC, start the FlashPro Express software from its installation directory.
2. Select **New** or **New Job Project from FlashPro Express Job** from Project menu to create a new job project, as shown in Figure 4, page 8.

**Figure 4 • FlashPro Express Job Project**

3. Enter the following in the New Job Project from FlashPro Express Job dialog box:
   - **Programming job file**: Click **Browse** and navigate to the location where the job file is located and select the file. The default location is:
     `<$Download_Directory>\mpf_dg0849_liberosoc_jb`
   - **FlashPro Express job project location**: Select **Browse** and navigate to the location where you want to save the project.

**Figure 5 • New Job Project from FlashPro Express Job**

4. Click **OK**. The required programming file is selected and ready to be programmed in the device.
5. The FlashPro Express window appears as shown in Figure 6, page 9. Confirm that a programmer number appears in the Programmer field. If it does not, confirm the board connections and click **Refresh/Rescan Programmers**.
6. Click **RUN** to program the device. When the device is programmed successfully, a **RUN PASSED** status is displayed as shown in Figure 7, page 9. See Running the Demo, page 10.

**Figure 7 • FlashPro Express—RUN PASSED**

7. Close FlashPro Express (**Project > Exit**).
   
The PolarFire device and SPI Flash are programmed.
   
   Power cycle the board using switch SW4. After power cycling:
   
   • The Full HD HDMI monitor displays the camera feed from the 2 cameras as Picture in Picture.
   • The 4K HDMI monitor displays the camera feed from one of the cameras in 4K mode.
2.8 Running the Demo

The demo features receiving the MIPI Rx data from the two cameras with 4K resolution at 30 frames per second (fps). The frames from camera are stored in DDR and passed on to the display as per the display controller timing parameters. The image processing IPs are enabled on the display side of the video pipeline.

The demo also features HDMI Rx input at a resolution of 1920 x 1080p 60 Hz. The HDMI input video is displayed on the HDMI monitor upon connecting a HDMI source at the RX port.

In the demo design, two display controllers are used to provide two video outputs:

- Full HD output (1920x1080 60Hz resolution)
- 4K output (3840x2160 30Hz resolution)

The Full HD output supports PIP, panning, Edge Detection, and Image enhancements such as contrast, brightness, color balance, and alpha blending. The 4K output supports image enhancements. The GUI control for image enhancements is common for both the video outputs.

Running the demo involves verifying the imaging and video settings using the Video_Control GUI and then observing the result on the HDMI monitor.

To use the demo GUI:

1. Start the Video_Control GUI from the installation directory.
   The GUI is displayed as shown in the following figure.

   ![Video_Control GUI](image1)

2. Select the second largest COM port on the GUI and select the Connect option.

   ![Connecting the GUI and Video kit](image2)

3. The Connect button turns green indicating a successful connection.
4. Use the **Contrast** and **Brightness** sliders to adjust the contrast and brightness and observe the change on the HDMI monitor. The sliders are highlighted in the following figure.

**Figure 11 • Adjusting Contrast and Brightness**

5. Similarly, adjust the color balance of the image using the color balance sliders.

6. Adjust the Alpha slider. The alpha blending feature enables adjusting the transparency of the PIP image. When the alpha value is adjusted to minimum (0), the image disappears.

**Note:** Default image settings can be reverted by using the **Reset** option.

7. Switch to **Edge Detection** mode using the **Edge** option.

8. Select the **PIP Menu** to change the PIP settings.

9. Select the source of the PIP window between Camera 1 and Camera 2 using **PIP: Source Select**. The position of the PIP window can be moved anywhere within the screen by dragging the pink **Picture In Picture** box. The **Auto Mode Start** option moves the PIP window automatically. The speed of this movement can be controlled using the **Auto Mode Step** slider.
10. Close the **PIP Menu**.

11. In the main video control GUI window, select the **Panning** option to view a particular area of the main or the PIP image within a 4K image.

12. In the **Panning Menu**, use the **Image Select** to select the image to be panned (Main image or PIP image). Any area of the 4K camera feed can be viewed by dragging the pink box horizontally or vertically. The **Reset** option sets the view of the **Main image** and **PIP image** to its default center position.

13. Close the **Panning Menu** to return to the main GUI.

14. Close the GUI to exit from the demo.

This concludes the demo.