



A Leading Provider of Smart, Connected and Secure Embedded Control Solutions



Thursday Aug. 13, 2020



Supporting Content

www.microsemi.com/Mi-V "Renode Webinar Series"



Learn how to get started with the PolarFire SoC FPGA, the world's first RISC-V based SoC FPGA, to create fully deterministic, real-time systems alongside the Linux® operating system. We are holding a series of webinars to introduce you to the free Renode™ development platform from MI-V partner Antmicro that is available with our SoftConsole v6.0 software development environment. You will see demo applications, learn how to create projects, and find out how to set up and configure your own systems targeting the new SoC FPGA architecture.

Click here to register.



Webinar 1 (May 2): Discover Renode for PolarFire[™] SoC Design and Debug

In this introductory session, we will provide you with an overview of SoftConsole 6.0 with Renode[™] integration. We will introduce you to the Renode development framework and provide an overview of the platform and its features. You will also learn about the PolarFire[™] SoC architecture and how to use Renode to develop your application. Webinar 1: Discover Renode for PolarFire® SoC Design and Debug Webinar 2: How to Get Started with Renode for PolarFire SoC Webinar 3: Learn to Debug a Bare-Metal PolarFire SoC Application with Renode Webinar 4: Tips and Tricks for Even Easier PolarFire SoC Debug with Renode Webinar 5: Add and Debug PolarFire SoC Models with Renode Webinar 6: Add and Debug Pre-Existing Model in PolarFire SoC Webinar 7: How to Write Custom Models Webinar 8: What's New in SoftConsole v6.2 Webinar 9: Getting Started with PolarFire SoC Webinar 10: Introduction to the PolarFire SoC Bare-Metal Library Webinar 11: Handling Binaries Webinar 12: Simple Peripheral as Software Stimulus Webinar 13: Two Baremetal Applications on PolarFire SoC Webinar 14: Building and running Linux



Agenda

- Linux on Renode
- Updating Renode with new images





Assets 9

- Renode release 1.10.1 should be used for running Linux on PolarFire SoC
- It will be included in SoftConsole 6.4
- A standalone installer can be found at: <u>https://github.com/renode/renode/releases/tag/v1.10.1</u>
 - renode-1.10.1-1-x86_64.pkg.tar.xz 4.06 MB renode-1.10.1-1.f23.x86 64.rpm 6.74 MB renode-1.10.1.linux-portable.tar.gz 21.4 MB renode 1.10.1.dmg 7.27 MB renode 1.10.1.msi 9.91 MB renode_1.10.1.zip 9.41 MB renode 1.10.1 amd64.deb 6.75 MB Source code (zip) Source code (tar.gz)



A script is included to run the PolarFire SoC Linux demo [Renode install]/scripts/single-node/icicle-kit.resc

:name: Icicle Kit

:description: This is a sample script running HSS, U-Boot and Linux on Icicle Kit with PolarFire SoC

\$bin?=@https://dl.antmicro.com/projects/renode/icicle--hss.elf-s_3297936-bcb7ef60abc78a878995554160eaac1dea962e95
\$uboot?=@https://dl.antmicro.com/projects/renode/icicle--u-boot-s_5132448-194bf14572a9bc4b26727567065ede2ffd7f1201
\$vmlinux?=@https://dl.antmicro.com/projects/renode/icicle--vmlinux-s_8563992-fa2aad1e61ec38b411f6afb543503cb26601b1e2
\$fit?=@https://dl.antmicro.com/projects/renode/icicle--fitImage.fit-s_16976563-1d0e86ed4cc7c24e167ca899fd929d954956b478
\$emmc?=@https://dl.antmicro.com/projects/renode/icicle--emmc.img-s_26746880-3a6ef871bc8eb6fcfbda344e8c23fb534ef89961

```
i @scripts/single-node/polarfire-soc.resc
```

```
showAnalyzer mmuart1
machine SdhcCardFromFile $emmc mmc
macro reset
"""
   sysbus LoadBinary $fit 0x88300000
   sysbus LoadSymbolsFrom $vmlinux
   sysbus LoadSymbolsFrom $uboot
"""
runMacro $reset
```



Section below downloads image source files to a local cache

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\$vmlinux?=@https://dl.antmicro.com/projects/renode/icicle--vmlinux-s_8563992-fa2aad1e61ec38b411f6afb543503cb26601b1e2
\$fit?=@https://dl.antmicro.com/projects/renode/icicle--fitImage.fit-s_16976563-1d0e86ed4cc7c24e167ca899fd929d954956b478
\$emmc?=@https://dl.antmicro.com/projects/renode/icicle--emmc.img-s_26746880-3a6ef871bc8eb6fcfbda344e8c23fb534ef89961

> If you just want to see Linux boot without having to build this will allow you to do it

Then load the PolarFire SoC platform and show UART1

i @scripts/single-node/polarfire-soc.resc

showAnalyzer mmuart1



- Then load the downloaded emmc image into the mmc
- Create a reset macro to load the fit image into memory and load symbols from the other images

Run the macro

```
machine SdhcCardFromFile $emmc mmc
macro reset
"""
    sysbus LoadBinary $fit 0x88300000
    sysbus LoadSymbolsFrom $vmlinux
    sysbus LoadSymbolsFrom $uboot
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```



- **1.** Download images
- 2. Load the platform

3. Load the images

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\$uboot?=@https://dl.antmicro.com/projects/renode/icicle--u-boot-s_5132448-194bf14572a9bc4b26727567065ede2ffd7f1201
\$vmlinux?=@https://dl.antmicro.com/projects/renode/icicle--vmlinux-s_8563992-fa2aad1e61ec38b411f6afb543503cb26601b1e2
\$fit?=@https://dl.antmicro.com/projects/renode/icicle--fitImage.fit-s_16976563-1d0e86ed4cc7c24e167ca899fd929d954956b478
\$emmc?=@https://dl.antmicro.com/projects/renode/icicle--fitImage.fit-s_16976563-1d0e86ed4cc7c24e167ca899fd929d954956b478
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   sysbus LoadSymbolsFrom $vmlinux
   sysbus LoadSymbolsFrom $uboot
"""
runMacro $reset
```



• To run the demo and boot Linux execute "include @scripts/single-node/icicle-kit.resc"

Renode





• This will load the memory files and open the UART analysers





Type "start" or just "s" to run the emulation



 The HSS will print from UARTO, openSBI, U-Boot and the Linux console will output on UART1





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• To help speed up boot you can reduce the log level for the system by gloabally setting the log level

"logLevel 3"



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Updating Renode with new images



Updating Renode with new images

Below are the images that get downloaded with Renode

:name: Icicle Kit

:description: This is a sample script running HSS, U-Boot and Linux on Icicle Kit with PolarFire SoC

\$bin?=@https://dl.antmicro.com/projects/renode/icicle--hss.elf-s_3297936-bcb7ef60abc78a878995554160eaac1dea962e95
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\$fit?=@https://dl.antmicro.com/projects/renode/icicle--fitImage.fit-s_16976563-1d0e86ed4cc7c24e167ca899fd929d954956b478
\$emmc?=@https://dl.antmicro.com/projects/renode/icicle--emmc.img-s_26746880-3a6ef871bc8eb6fcfbda344e8c23fb534ef89961

 You can overwrite a variable by passing its name along with the new image name

e.g \$bin=@D:/linux_images/hss.elf



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Updating Renode with new images

You can pass the variable names

Renode



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(monitor) \$bin=@D:/linux_images/hss.elf

• Or update the original resc file

:name: Icicle Kit :description: This is a sample script running HSS, U-Boot and Linux on Icicle Kit with PolarFire SoC

\$bin?=@D:/linux_images/hss.elf \$uboot?=@https://dl.antmicro.com/projects/renode/icicle--u-boot-s_5132448-194bf14572a9bc4b26727567065ede2ffd7f1201 \$vmlinux?=@https://dl.antmicro.com/projects/renode/icicle--vmlinux-s_8563992-fa2aadle6lec38b411f6afb543503cb26601b1e2 \$fit?=@https://dl.antmicro.com/projects/renode/icicle--fitImage.fit-s_16976563-1d0e86ed4cc7c24e167ca899fd929d954956b478 \$emmc?=@https://dl.antmicro.com/projects/renode/icicle--emmc.img-s_26746880-3a6ef871bc8eb6fcfbda344e8c23fb534ef89961



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Agenda

- Linux on Renode
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Thank you!

Any questions?



Second Thursdays

Sep. 10 - Webinar 17: Real-Time (AMP Mode) on PolarFire SoC

