

101328C

FB500-81.6/27.6/8.4 Switched Filter Module

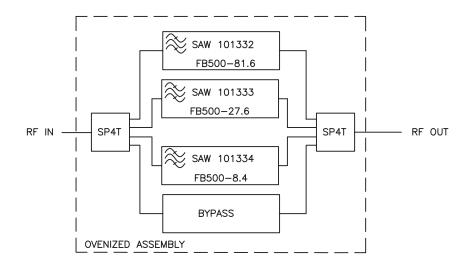
Specifications

Parameter	Symbol	Unit			
Filter Component			101332	101333	101334
Center Frequency	F ₀	MHz defined	500	500	500
Bandwidth	В	MHz defined	82	27.6	8.4
-3 dB Bandwidth	B ₃	MHz min	86.6	28.7	9.4
-40 dB Bandwidth	B ₄₀	MHz max	97.7	33.2	12.4
Delay	T ₀	µsec typ	0.354	1.012	1.576
Amplitude Ripple		dB _{P-P} max	1	1	1
Phase Ripple		deg _{P-P} max	4	6	3
Rejection		dB max	42	44	46
Spurious for $ t - T_0 > .9T_0$		dB max	-46	-42	-48
Gain		dB min	-28	-28	-28
Return Loss		dB min	10		
DC Power	15V @ 100mA, 5V @ 50mA, Oven -15V @ 600mA (2 A Max.)				

Notes

Switched Filter Module. Filters are maintained at constant temperature for stable operation.

System Block Diagram

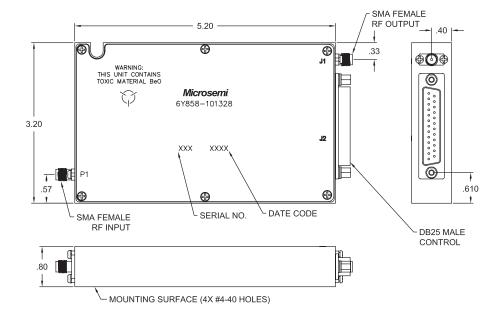




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Package Outline



DB25 PINOUT

PIN FUNCTION

1,14 -15 VDC, 600mA TYP. (2A MAX)

2,12,15 GND

3 +15 VDC, 100mA 4 EXT THERMISTOR

5 500 MHz SELECT

6 200 MHz SELECT

8 EEPROM-HOLD/

9 EEPROM-SCK

10 EEPROM-SO

13 +5 VDC, 50mA 16 EXT THERMISTOR

17 \MODE ENABLE

18 100 MHz SELECT 19 5 MHz SELECT

21 EEPROM-WP/

22 EEPROM-SI

23 EEPROM-CS/



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