



DEFENSE LOGISTICS AGENCY
LAND AND MARITIME
POST OFFICE BOX 3990
COLUMBUS, OH 43218-3990

April 19, 2012

Mr. Esam Elashmawi
Vice President and General Manager
Actel DBA Microsemi SoC Products Group
2061 Stierlin Court
Mountain View, CA 94043-4655

Dear Mr. Elashmawi:

Re: Full Certification Class Q and V for MIL-PRF-38535; FSC 5962; VQC-12-024080;
Tran

Actel DBA Microsemi SoC Products Group has demonstrated to the DLA Land and Maritime that it complies with MIL-PRF-38535, the performance specification used by the Department of Defense for monolithic integrated circuits that operate in severe environments.

This letter supersedes previous Level Q Certification/Qualification letters granted to Actel DBA Microsemi SoC Products Group, effective immediately, to reflect the current certification status of your facilities as documented on the Quality Management Program (QMP), document number C-00-QMP, revision 20, dated March 08, 2012.

In addition, the parts that are manufactured using the certified technology flows are being listed on the QML-38535. This will allow Actel DBA Microsemi SoC Products Group to mark parts with "Q" or "QML". These designators have been authorized by the Department of Defense for parts that have been produced to a QML specification, (i.e., one which allows less government oversight), the use of world-wide commercial production lines, reduced finished product testing based on statistical process controls (SPC), and other cost advantages.

Testing must be performed using the facilities and methods listed in the Laboratory Suitability letter DLA Land and Maritime-VQC-12-024081, or at facilities approved by Actel DBA Microsemi SoC Products Group Technical Review Board using its MIL-PRF-38535 Quality Management Program Plan.

This certification is subject to the conditions in DoD 4120.24-M, Defense Standardization Program and SD-6.

Any and all of the facilities mentioned on the enclosure are subject to an audit by the Qualifying Activity at any time. The QML manufacturer shall be responsible to make all necessary arrangement with offshore facilities to grant DLA audit team access to these facilities to perform on site audit to the MIL-PRF-38535 and MIL-STD-883 requirements. Offshore facilities are subject to all of the conditions of MIL-PRF-38535.



QPL/QML manufacturers shall notify the qualifying activity immediately after learning of a potential issuance of a GIDEP alert, problem advisory or major quality/reliability problem on their QPL/QML products. Failure to provide prior notification may be grounds for removal from QML-38535.

Finally, it is requested that the following activities be reported promptly to DLA Land and Maritime:

1. Changes to certified facilities, process flows, or approved testing subcontractors
2. Problem evaluation and a corrective action when:
 - a. A Technology Conformance Inspection (TCI) failure has been validated
 - b. The reliability of shipped parts is questionable.
3. Test optimization, including:
 - a. Implementation - paragraph J.3.12, Appendix J, MIL-PRF-38535
 - b. Changing, suspending or canceling a prior test optimization
4. Additions or deletions of parts in the QML-38535
5. Change of company QML contact or other key QML personnel

This certification is valid until terminated by written notice from the qualifying activity. If warranted, it may be withdrawn by this Agency at any time.

If you have any questions, please contact Mr. Vinh V Tran at (614) 692-0606.

Sincerely,



for JOSEPH GEMPERLINE
Chief
Sourcing and Qualification Division

Enclosure

cc:

DLA Land and Maritime-VQC (Michael Grammens)

DLA Land and Maritime-VQC (Scott Thomas)

DLA Land and Maritime-VQ (Alberta Petruskevich)

Actel DBA Microsemi SoC Products Group – (Mrs. Lisa Konigsberg)

Actel DBA Microsemi SoC Products Group – (Mr. Tejpal Sahota)

OPERATION	FACILITY/LOCATION	Technologies & Product Families	FLOW
Design	Actel DBA Microsemi SoC Products Group 2061 Stierlin Court Mountain View, CA 94043	CMOS, ONO and M2M Antifuse, Flash based Field Programmable Gate Arrays (FPGA).	Q and V
Wafer Fabrication	Matsushita Electronics Corporation (MEC) 1 Koutari Yakemachi, Nagaokakyo City, Kyoto Japan	0.6u, 0.8u, and 1.0 u CMOS/Antifuse FPGA, wafer probe, E-test/PCM test	Q
	Global Foundries (GFM) (formerly Charter Semiconductor Manufacturing Inc. (CSM)) 60 Woodlands, Industrial Park D, Street 2, Singapore 738406	0.35u, 0.45u, and 0.6u CMOS/Antifuse FPGA, wafer probe, E-test/PCM test	Q
	Open Silicon 490 N. McCarthy Blvd, suite 220, Milpitas, CA 95035	Customer Service Liaison for Global Foundries (GFM)	Q
	United Microelectronics Corp. (UMC) No. 3, Li-Hsin 2 nd road, Science based Industrial Park, Hsinchu City, Taiwan, Republic of China	0.15u, 0.22u, and 0.25u CMOS/Antifuse FPGA wafer probe, E-test/PCM test	Q and V
Assembly and Test	Kyocera America, Inc. 8611 Balboa Avenue, San Diego, CA 92123	Hermetic package assembly for Ceramic Quad Flatpak (CQFP), Land Grid Array (LGA), and Pin Grid Array (CPGA) packages	Q and V
	Amkor Technology, site P1; Muntinlupa City, Philippines	Ceramic package and selected screen tests (no burn-in) Hermetic package assembly for Ceramic Quad Flatpak (CQFP) and Pin Grid Array (CPGA) packages	Q
	Six Sigma 905 Montague Expressway, Milpitas, CA 95035	Solder column attach Solderability (dipping, testing, inspection)	Q and V
Electrical Test	Actel DBA Microsemi SoC Products Group 2061 Stierlin Court Mountain View, CA 94043	Electrical test	Q and V

<u>OPERATION</u>	<u>FACILITY/LOCATION</u>	<u>Technologies & Product Families</u>	<u>FLOW</u>
Misc.	Actel DBA Microsemi SoC Products Group 2061 Stierlin Court Mountain View, CA 94043	Wafer Probe, Parametric test, inspection, QCI coverage tracking	Q and V