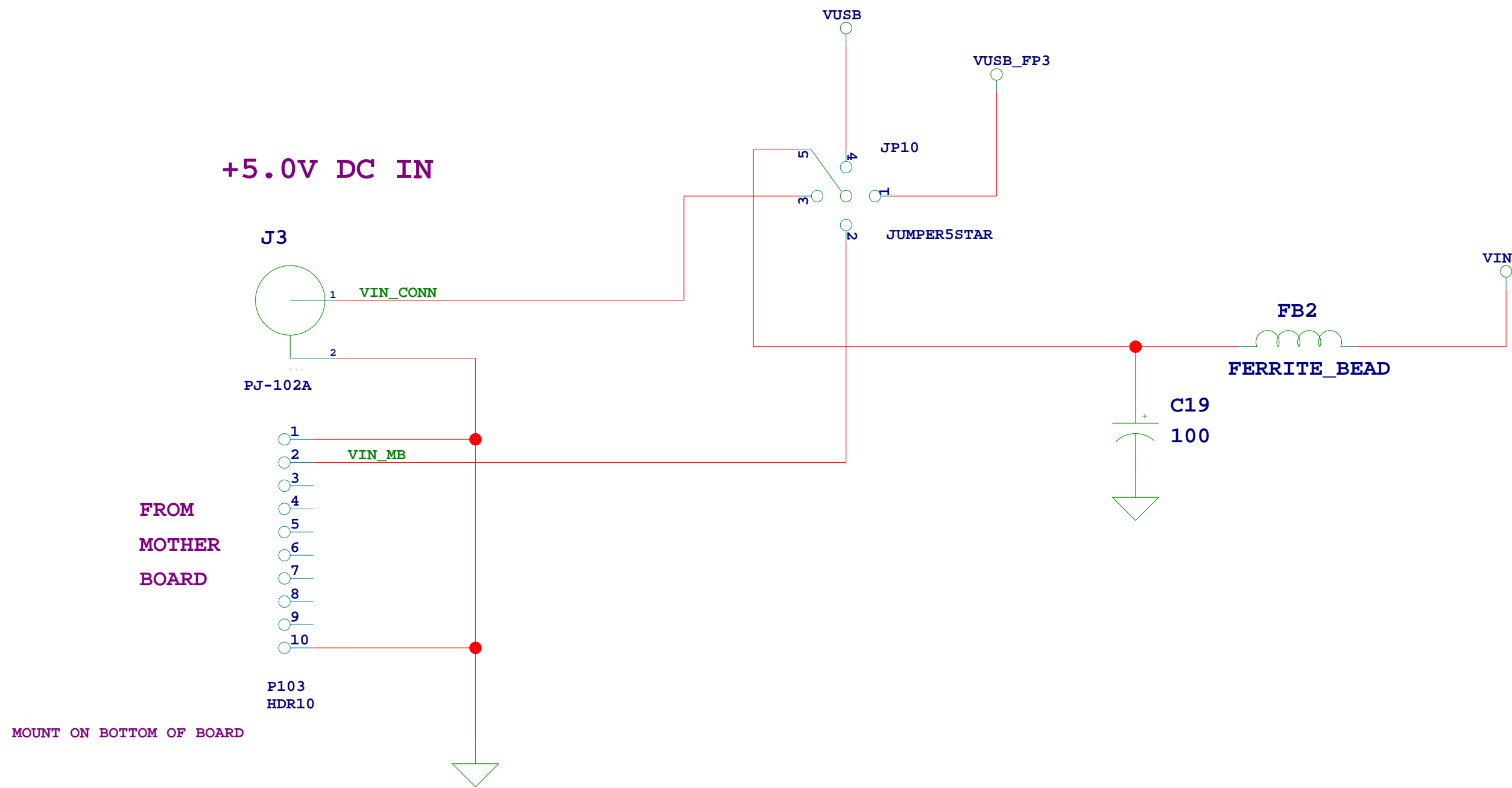
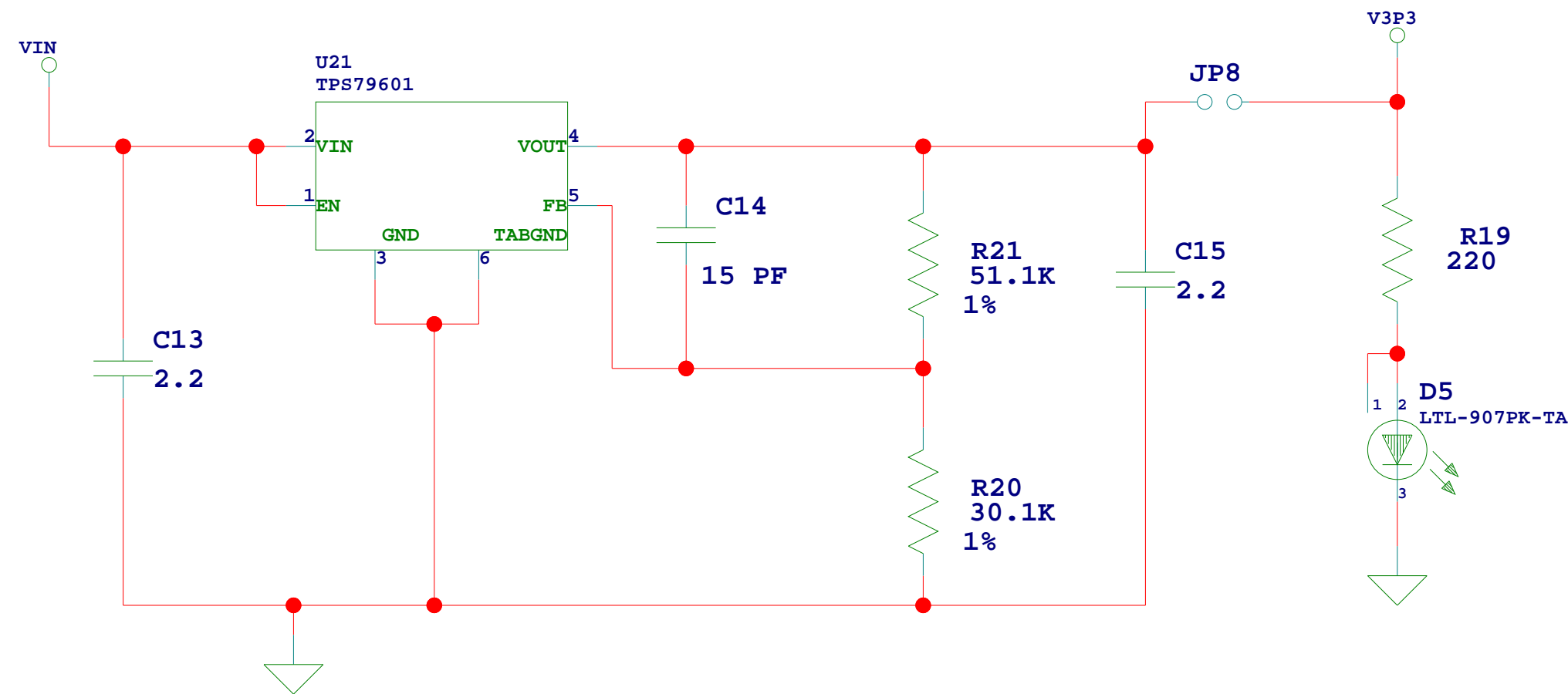


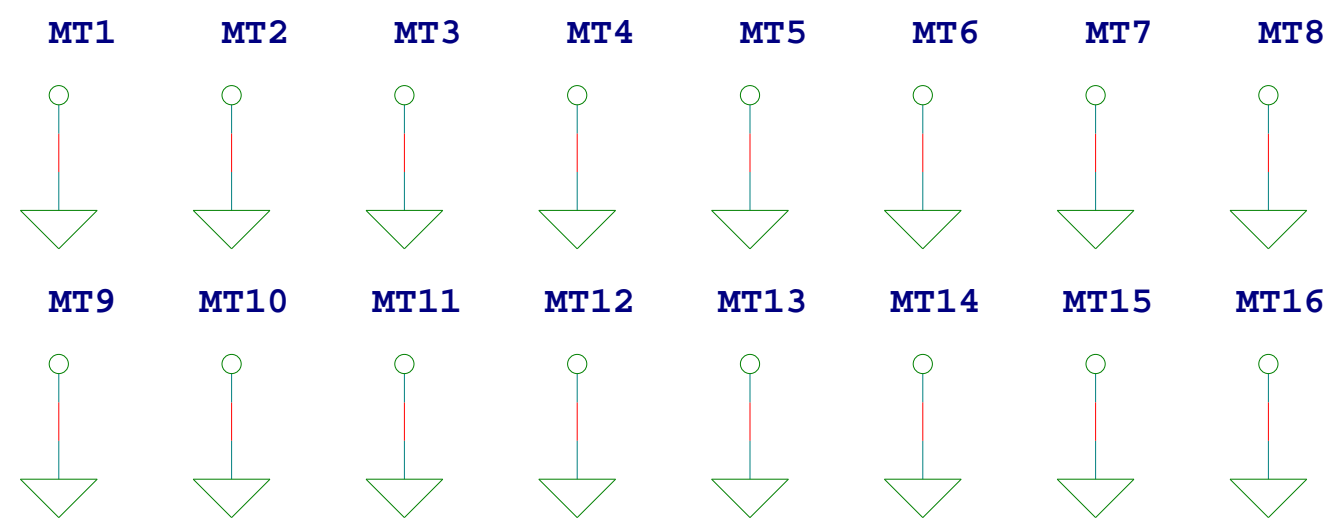
+5.0V DC IN



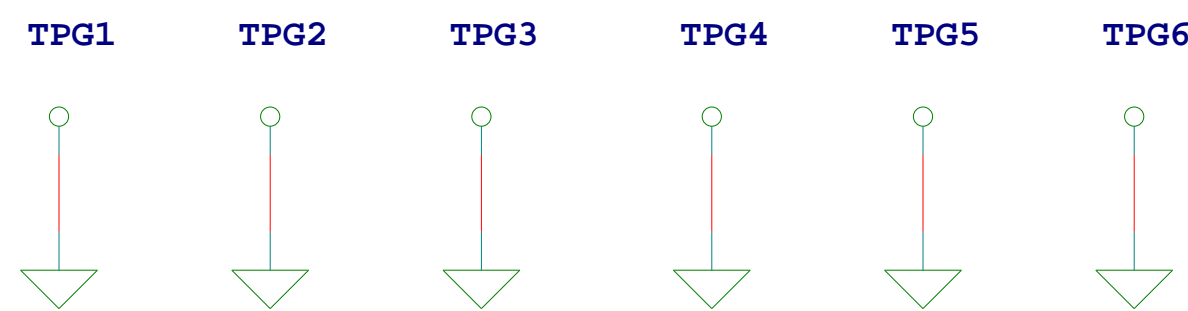
3.3V I/O



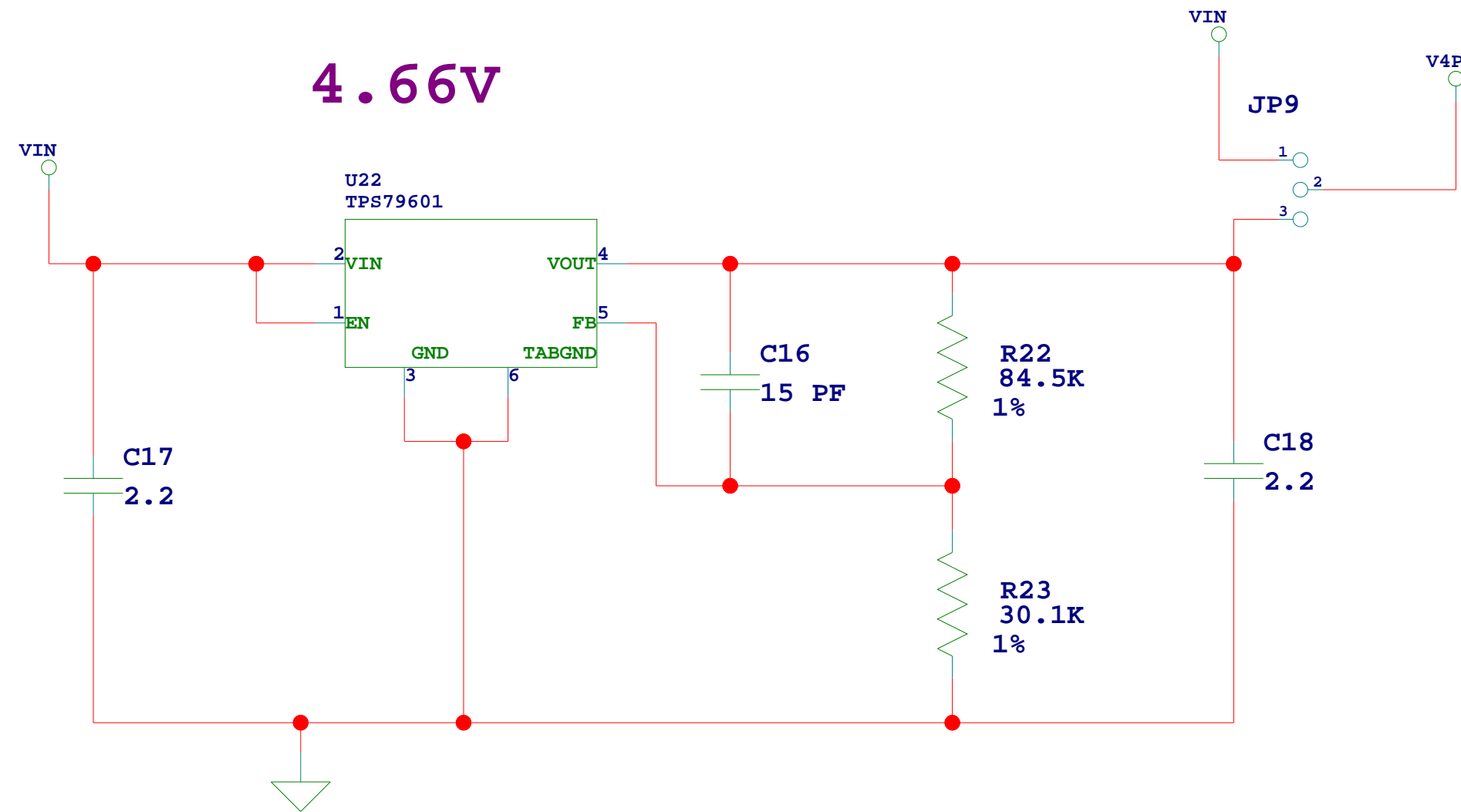
MOUNTING HOLES



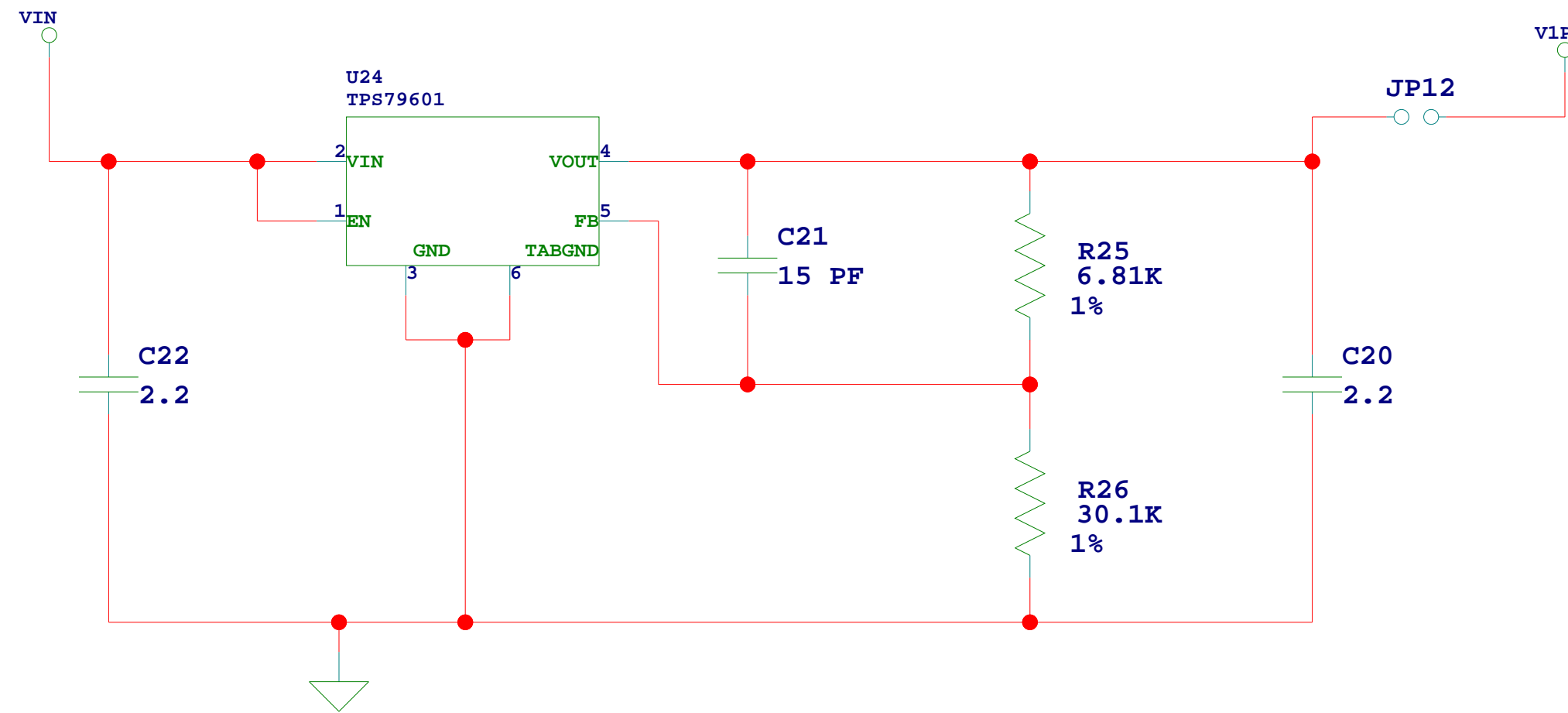
GROUND TESTPOINTS



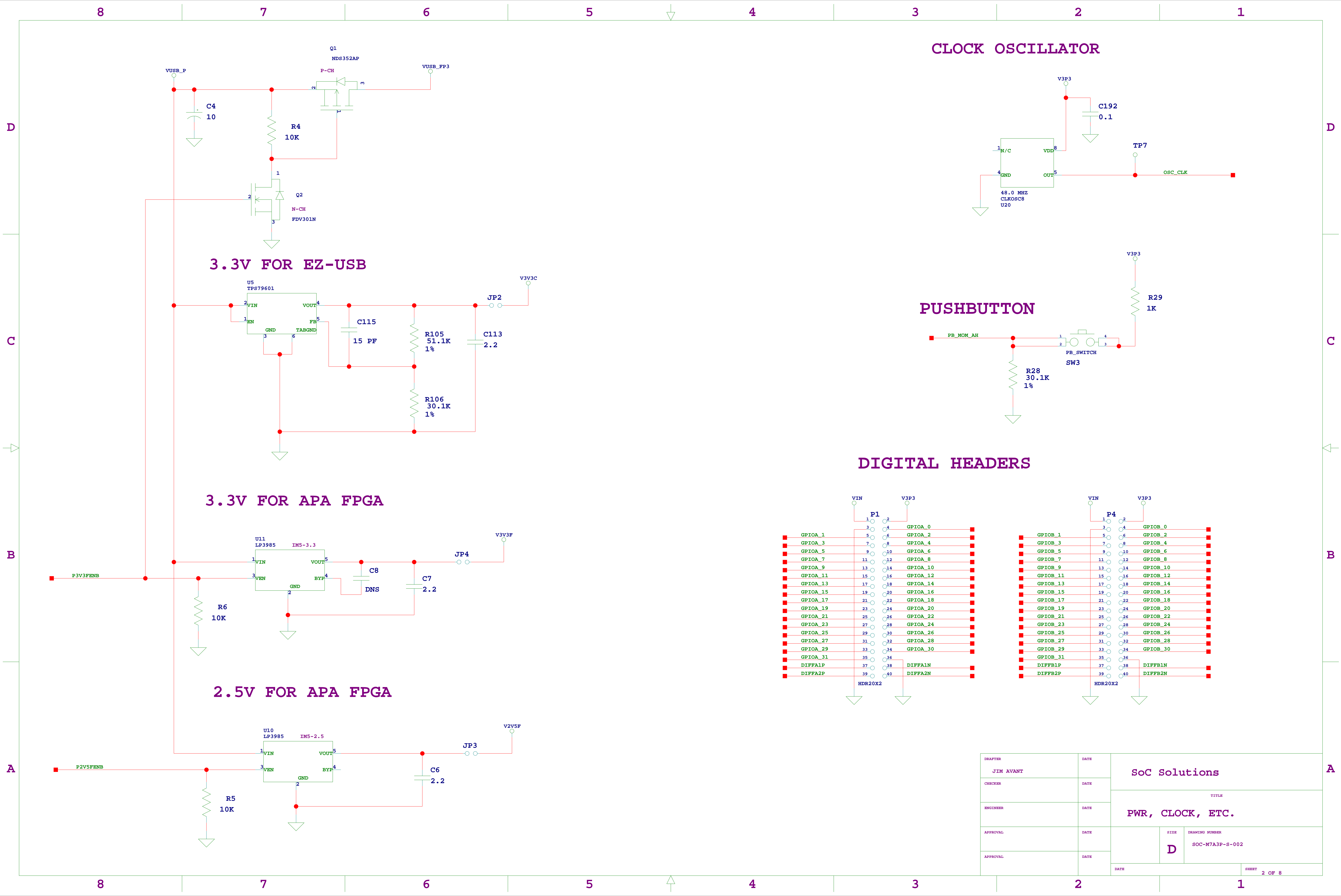
4.66V



1.5V CORE



DRAFTER	DATE	SoC Solutions		
JIM AVANT	DATE			
CHECKER	DATE	POWER SUPPLIES		
ENGINEER	DATE			
APPROVAL	DATE	D	SIZE	DRAWING NUMBER
APPROVAL	DATE			SOC-M7A3P-S-002
DATE		SHEET		1 OF 8



DRAFTER	DATE	SoC Solutions			
JIM AVANT					
CHECKER	DATE	TITLE			
		PWR, CLOCK, ETC.			
ENGINEER	DATE	D	DRAWING NUMBER		
APPROVAL	DATE		SOC-M7A3P-S-002		
APPROVAL	DATE				
		DATE	SHEET 2 OF 8		

D

C

B

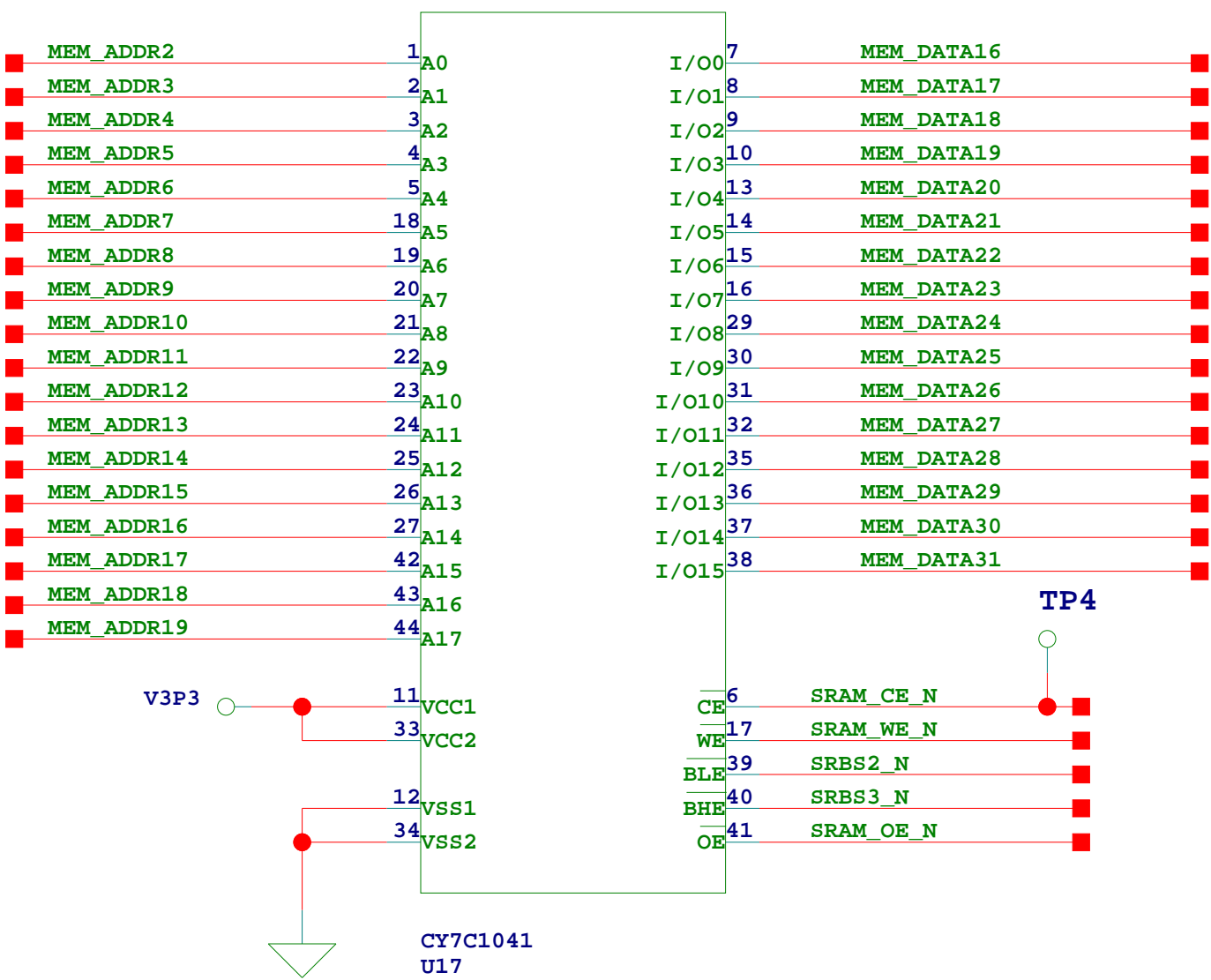
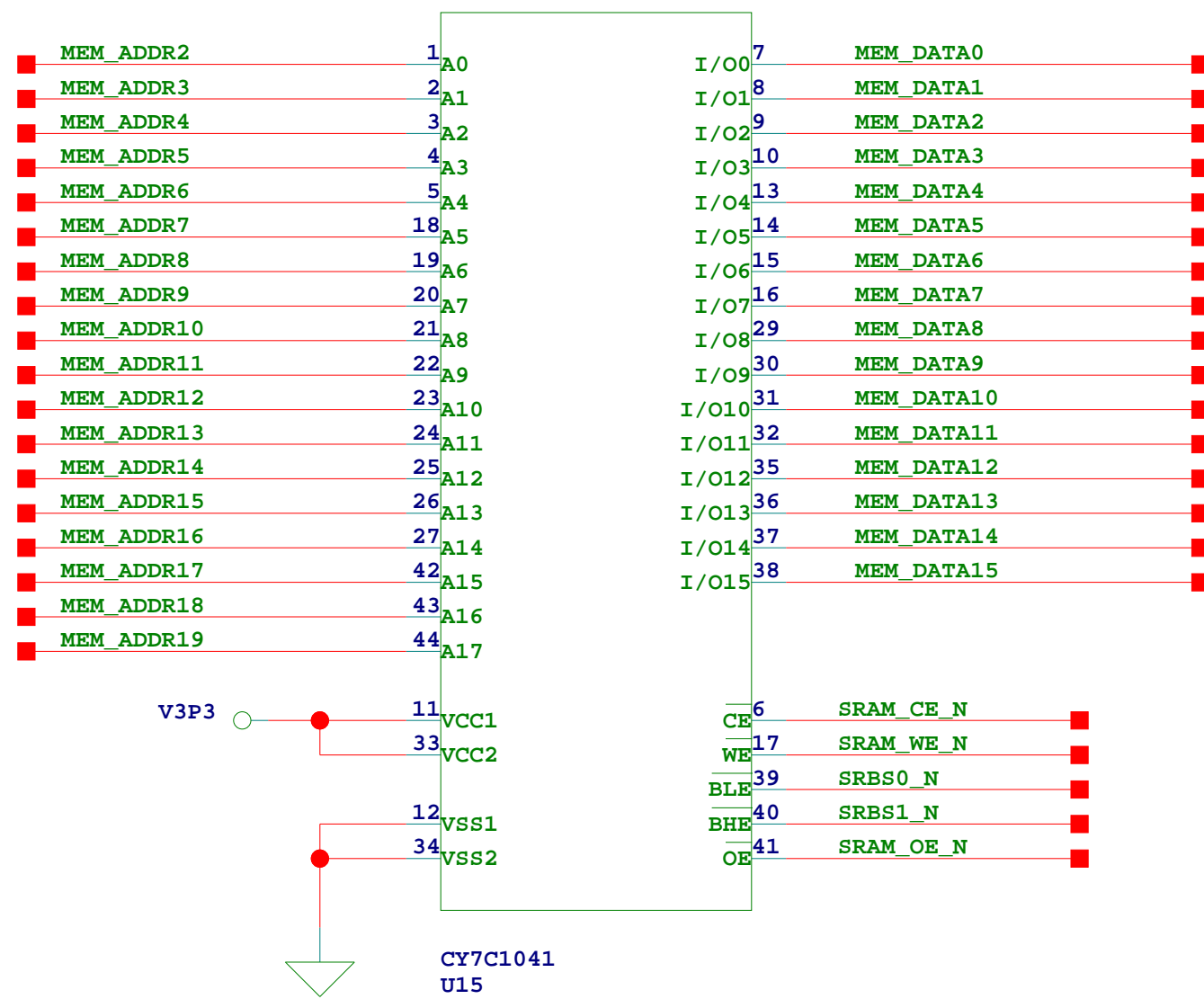
A

D

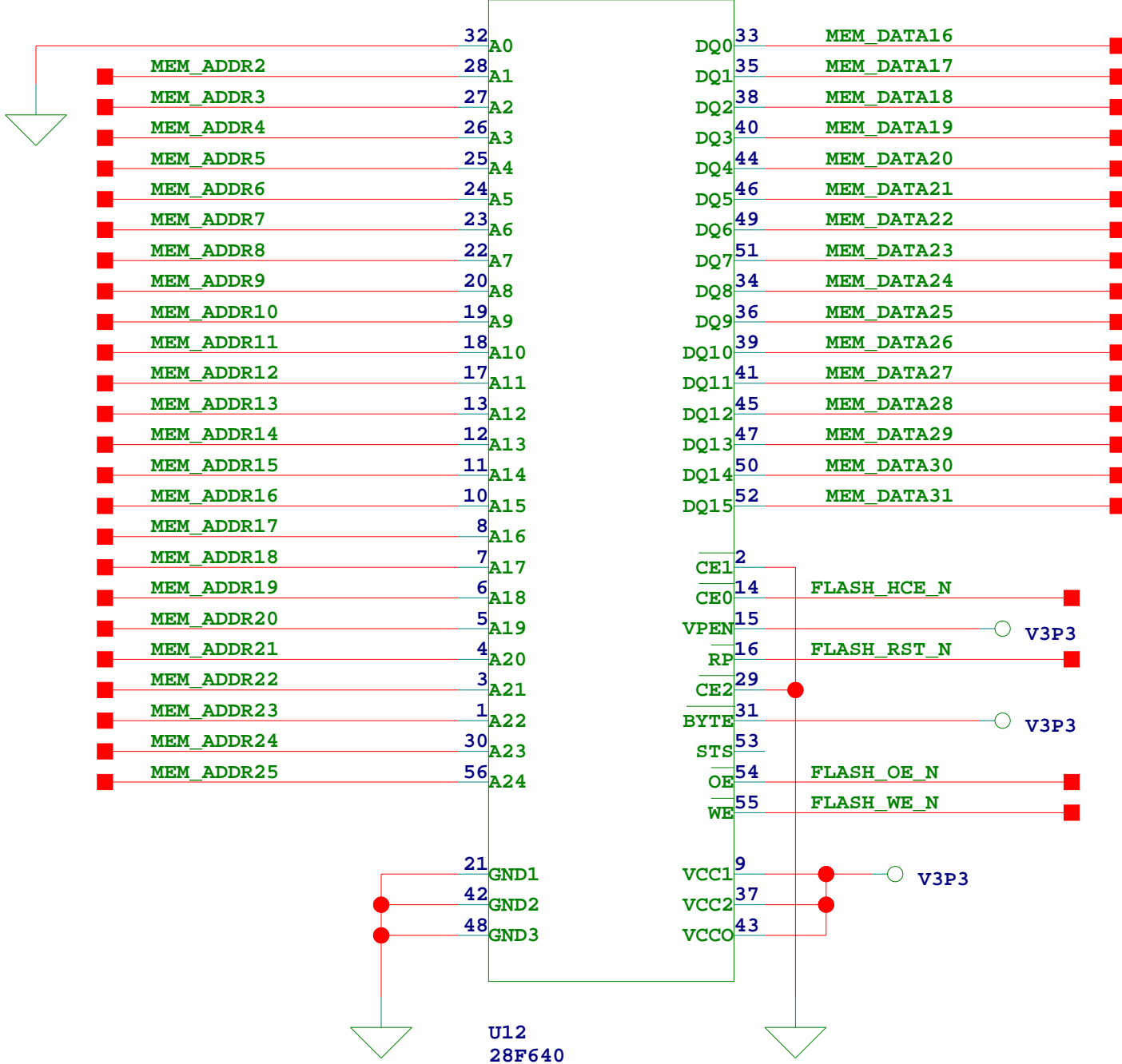
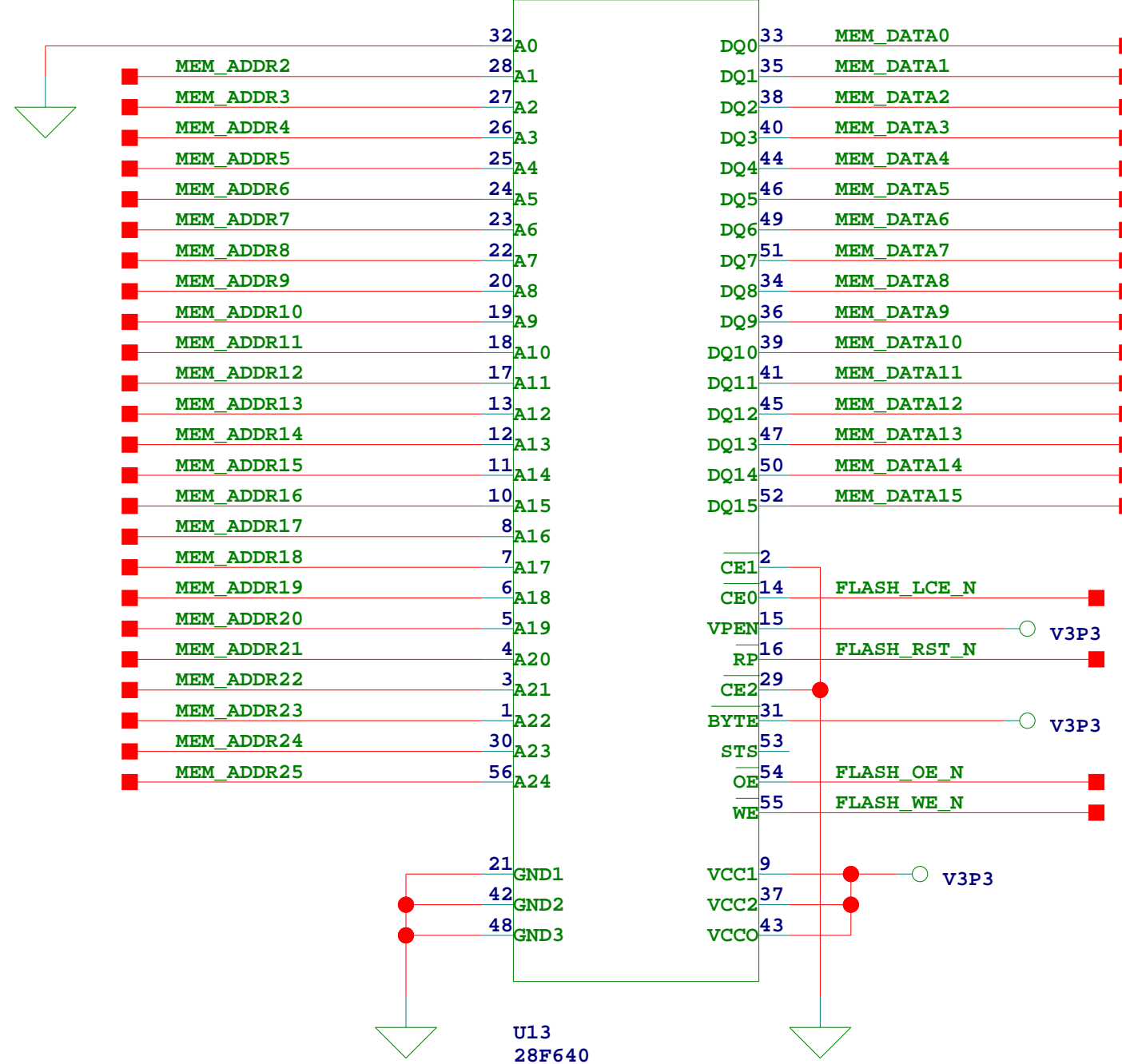
C

B

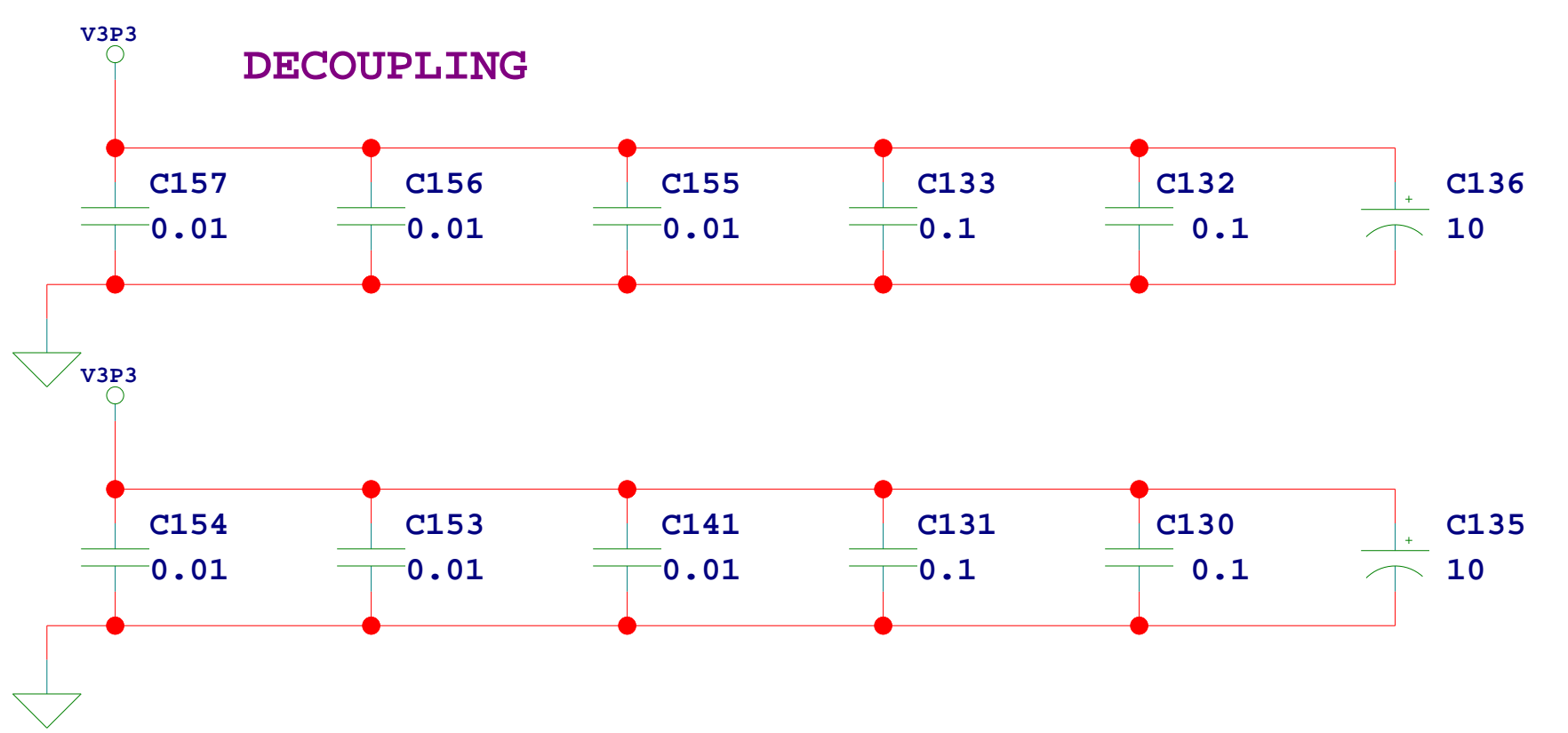
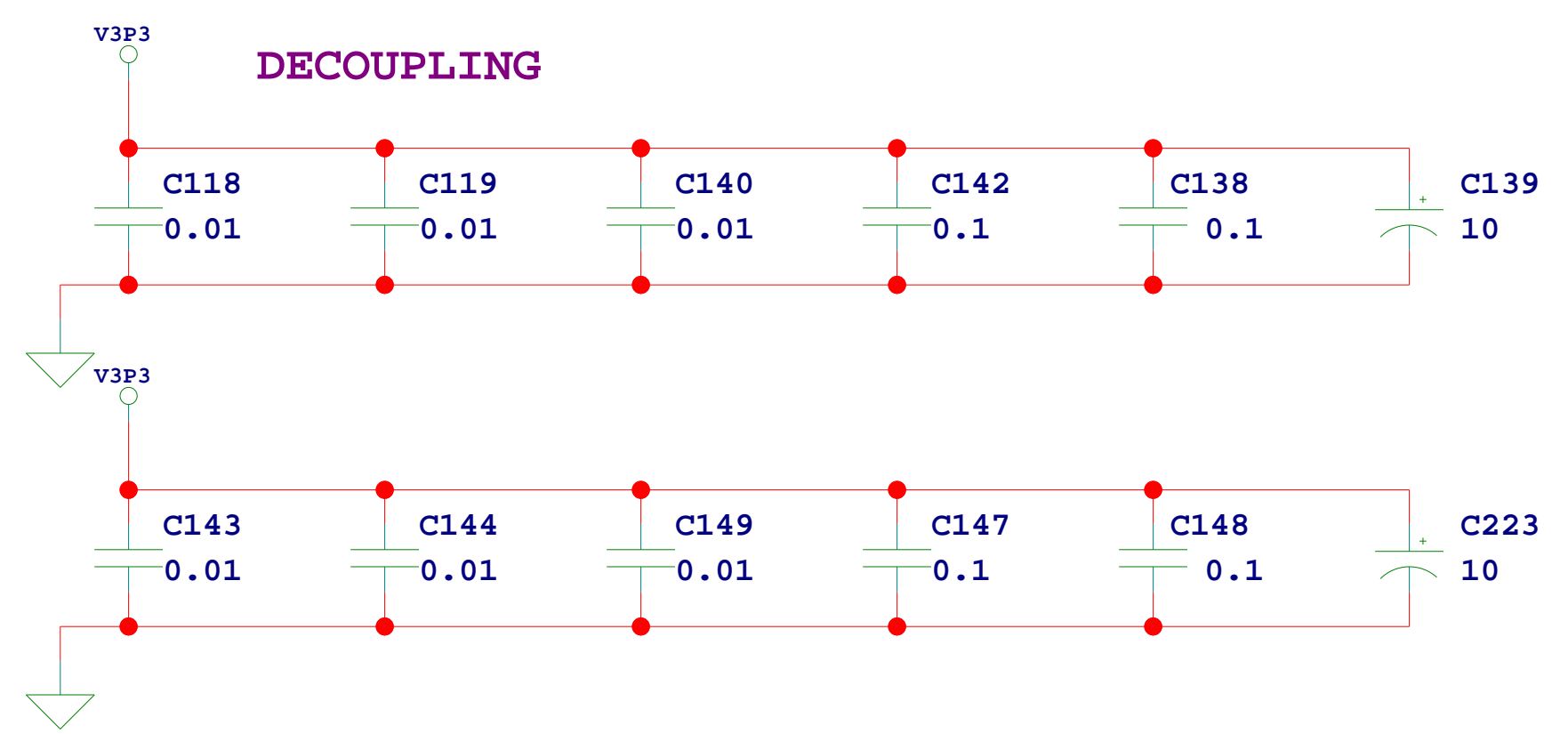
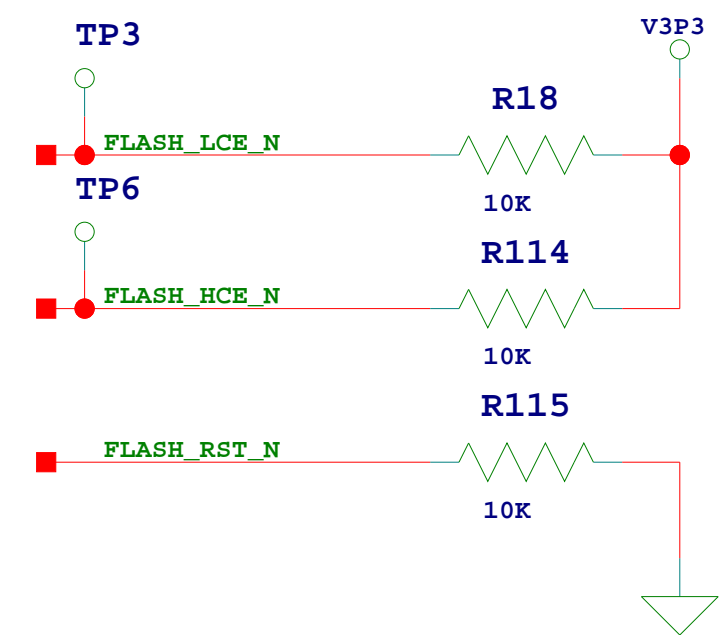
A



SRAM
1MB
256Kx32



FLASH
16MB
4Mx32



DRAFTER	DATE	SoC Solutions		
JIM AVANT	DATE			
CHECKER	DATE	TITLE		
ENGINEER	DATE	SRAM & FLASH		
APPROVAL	DATE	SIZE D	DRAWING NUMBER SOC-M7A3P-S-002	
APPROVAL	DATE		SHEET 3 OF 8	

D

C

B

A

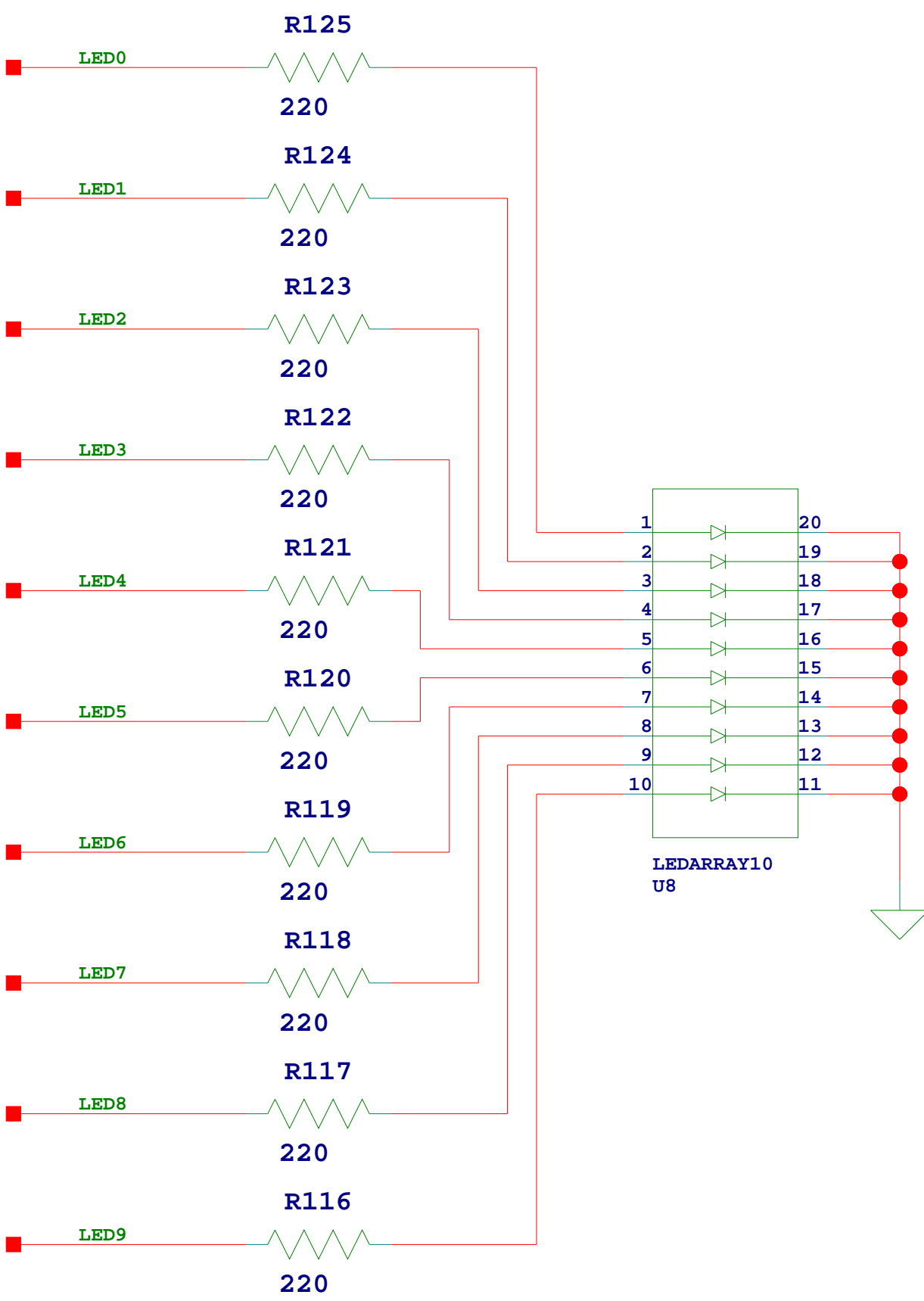
D

C

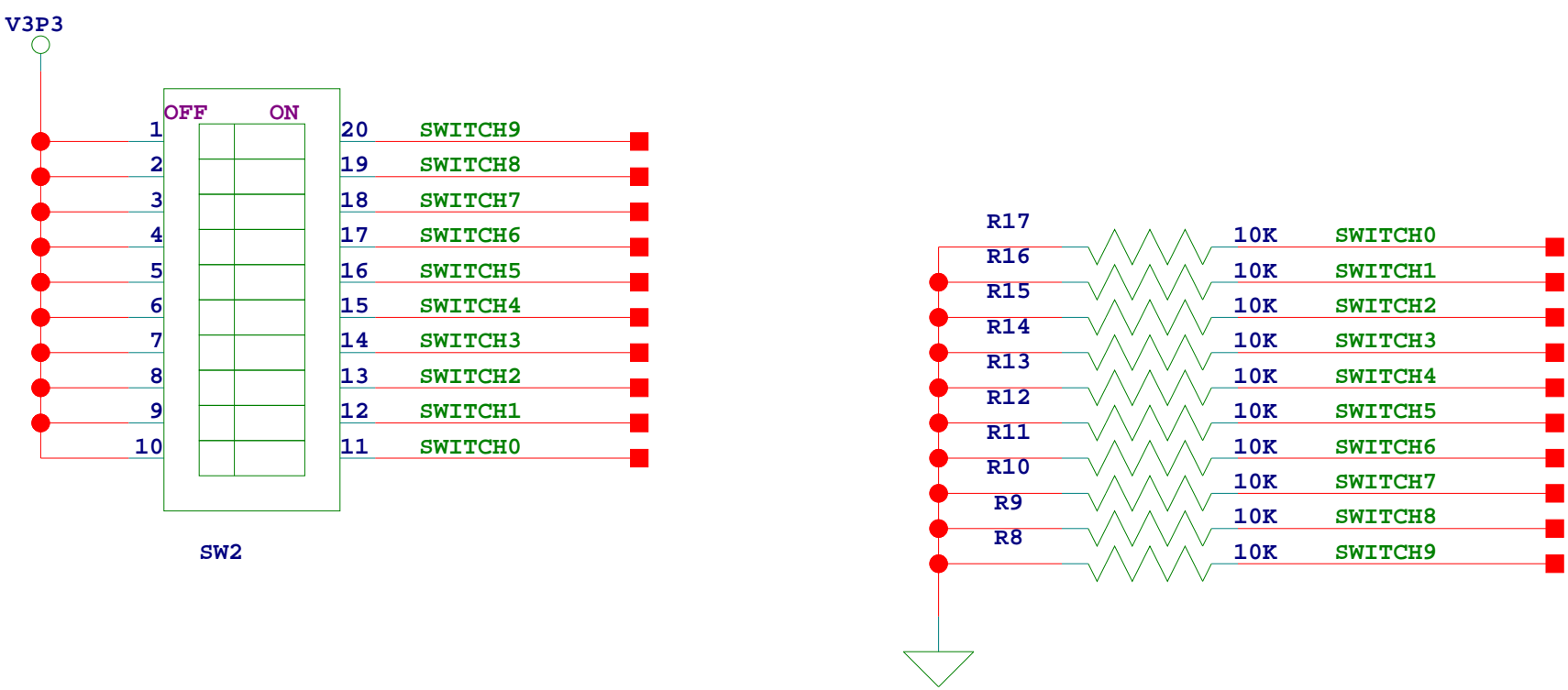
B

A

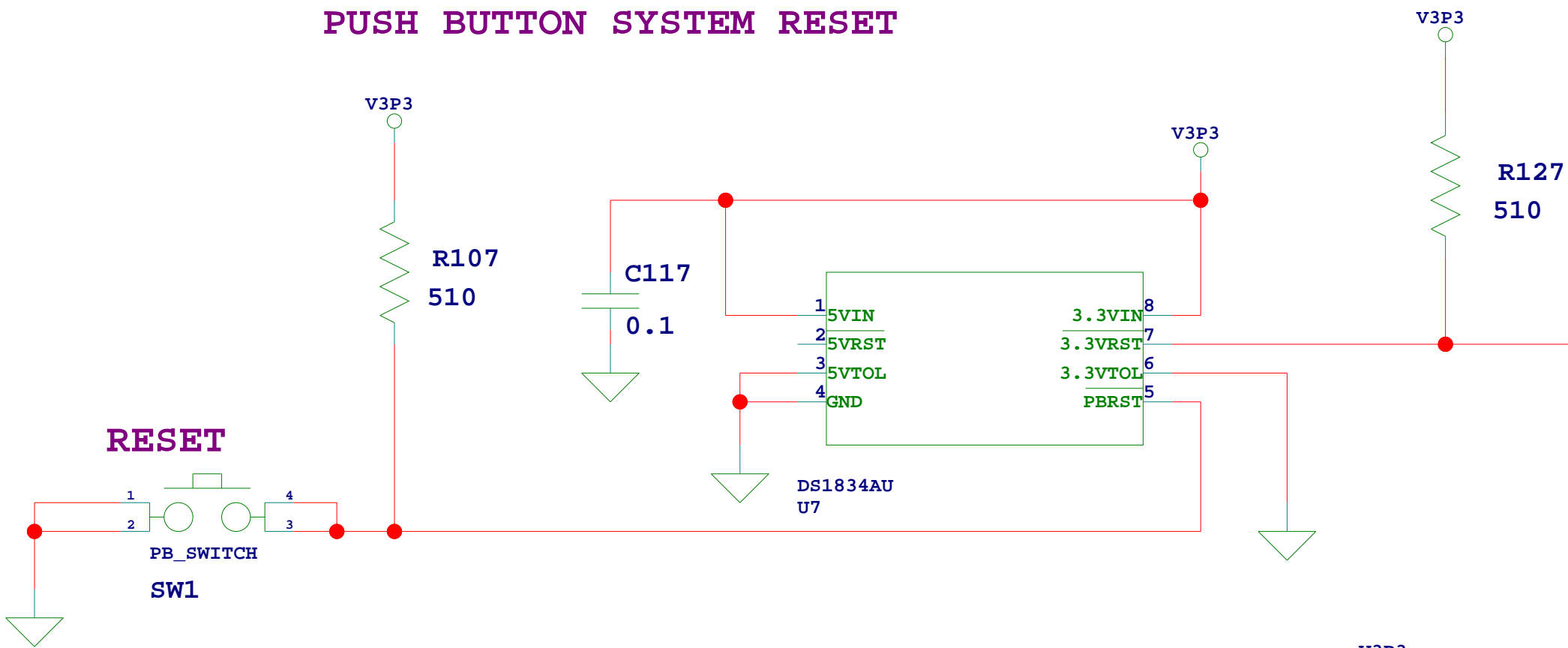
TEST LEDS



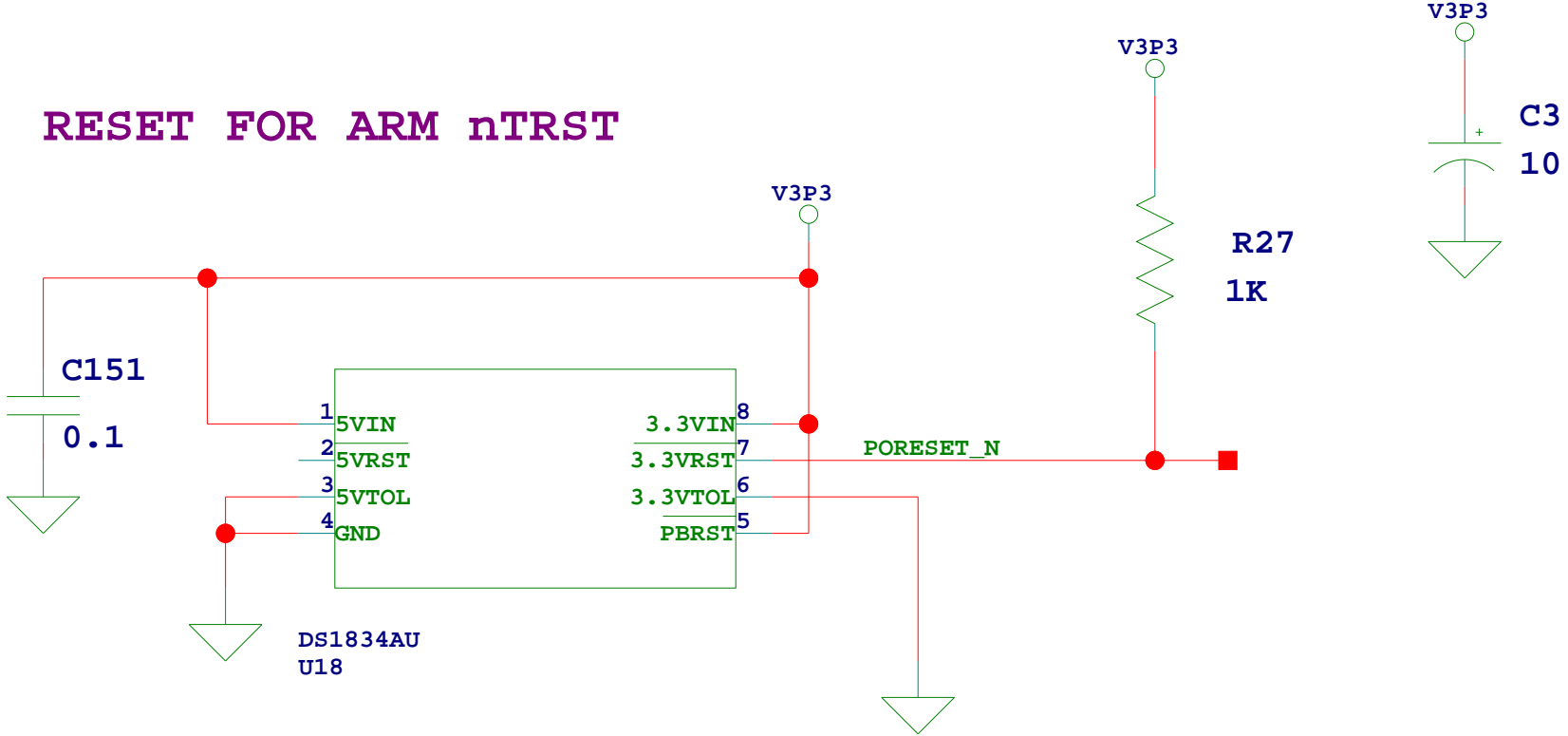
TEST SWITCHES



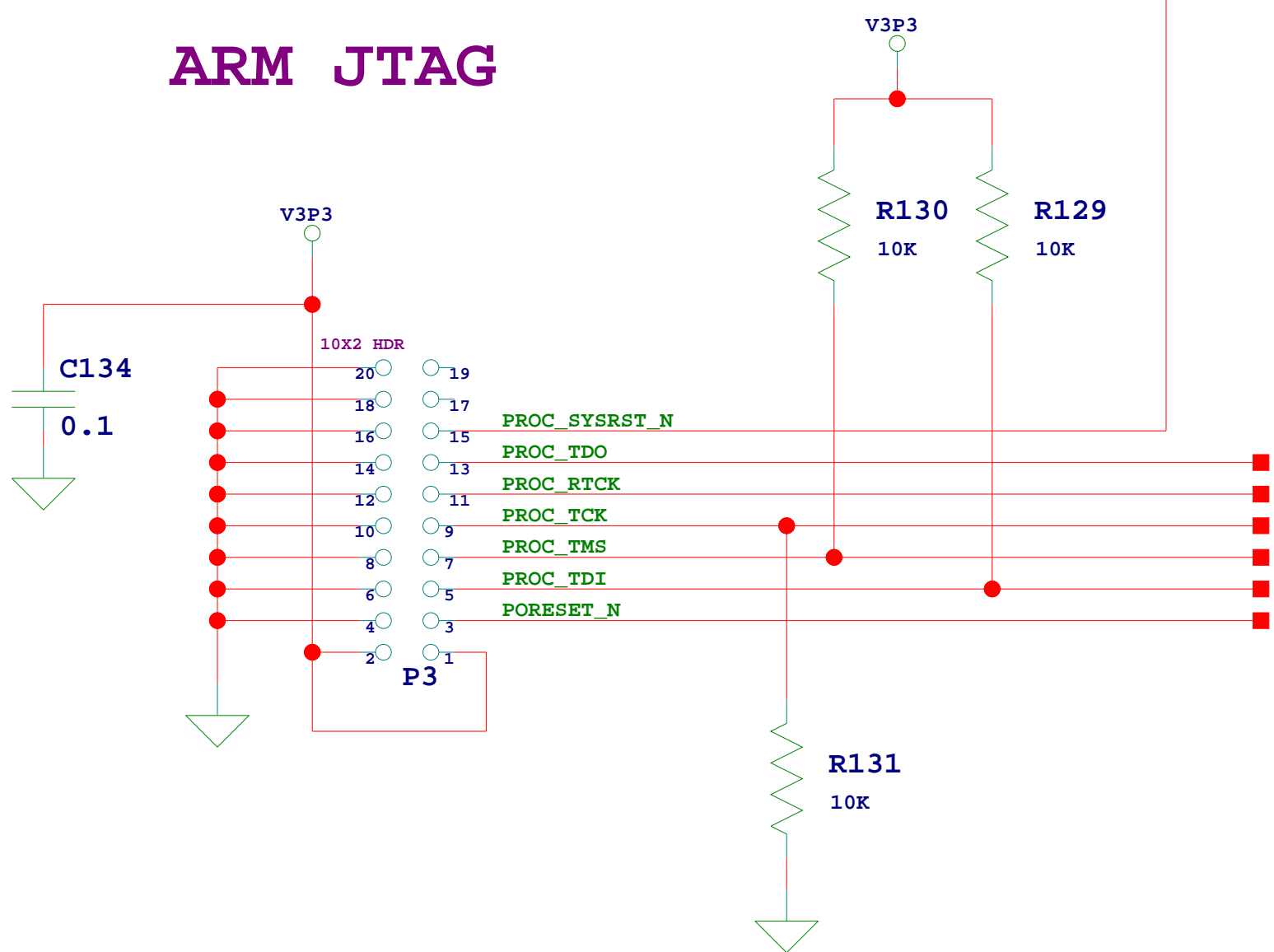
PUSH BUTTON SYSTEM RESET



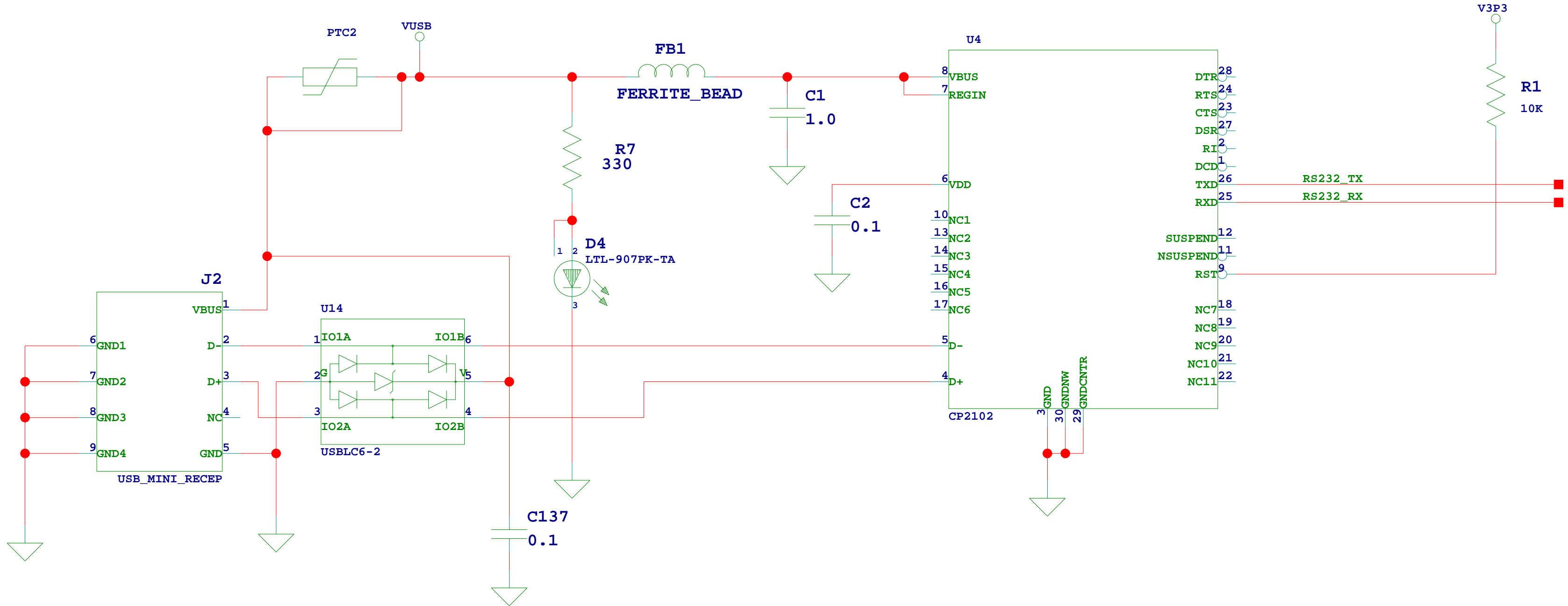
RESET FOR ARM nTRST



ARM JTAG

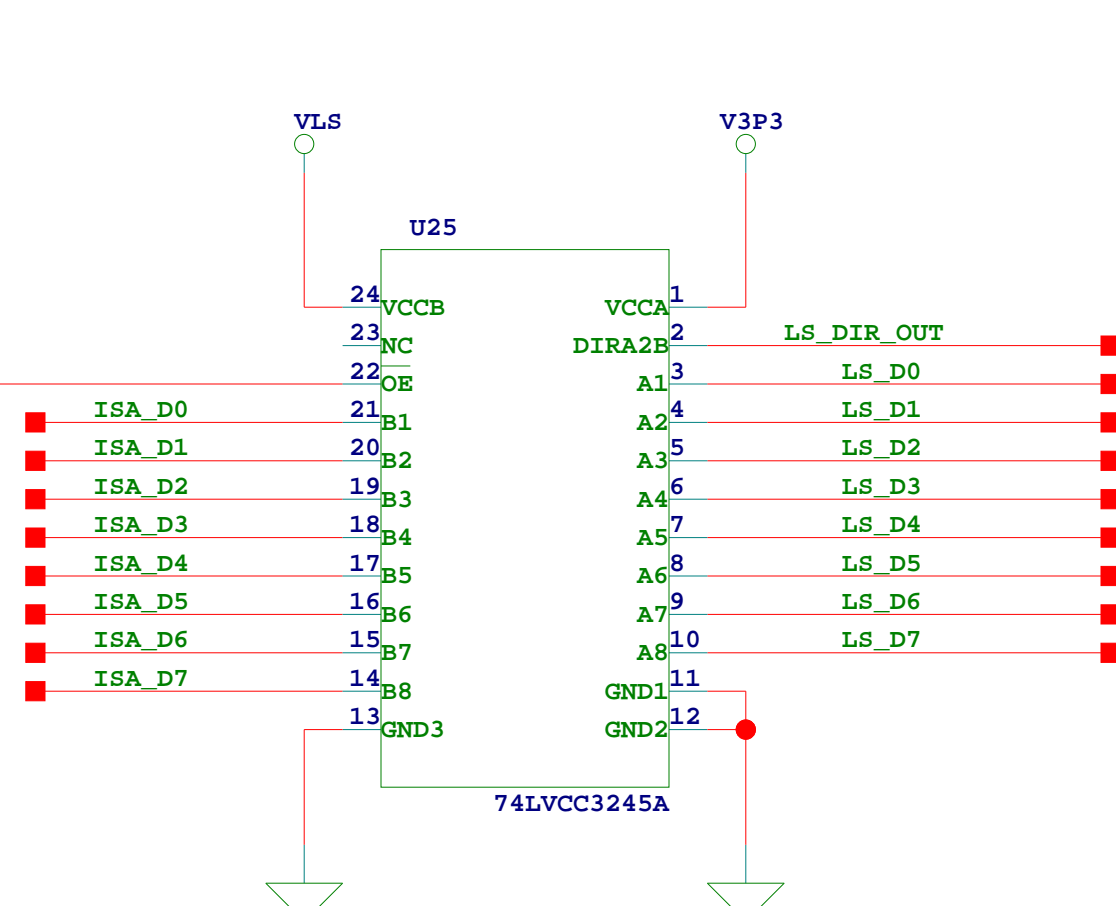
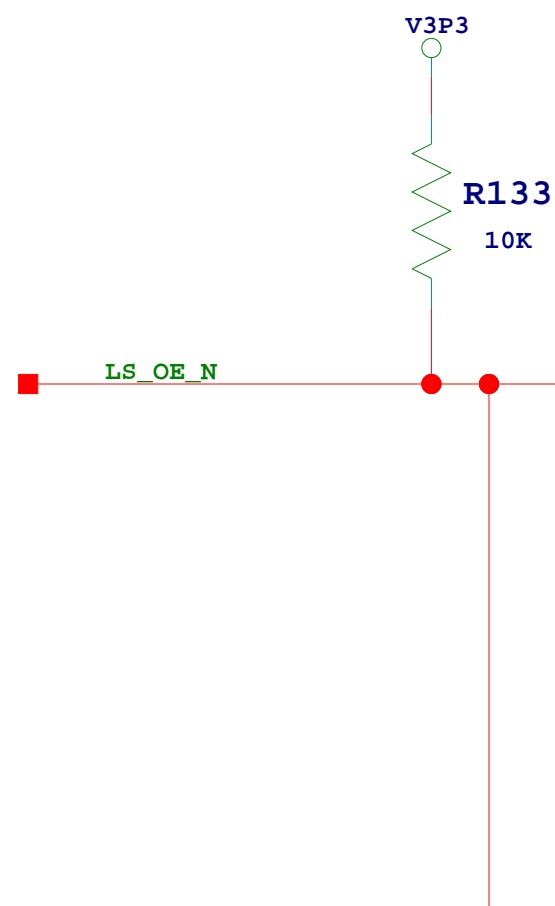
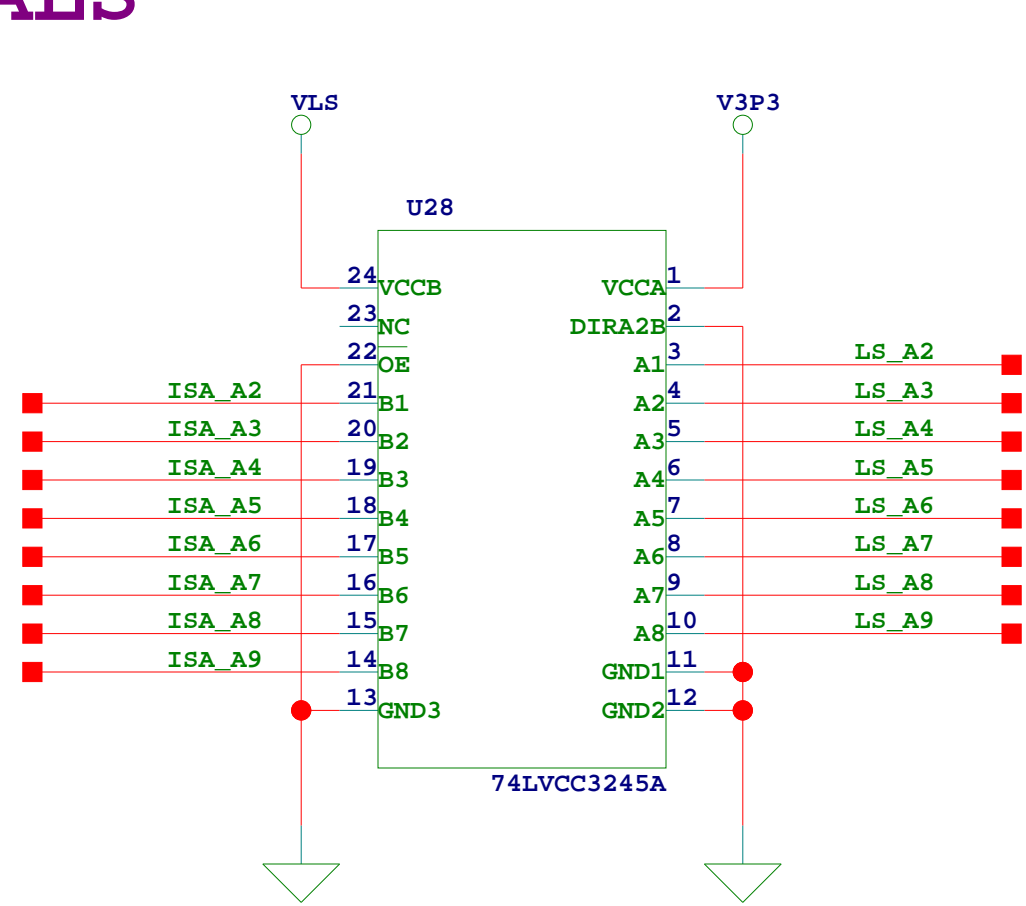
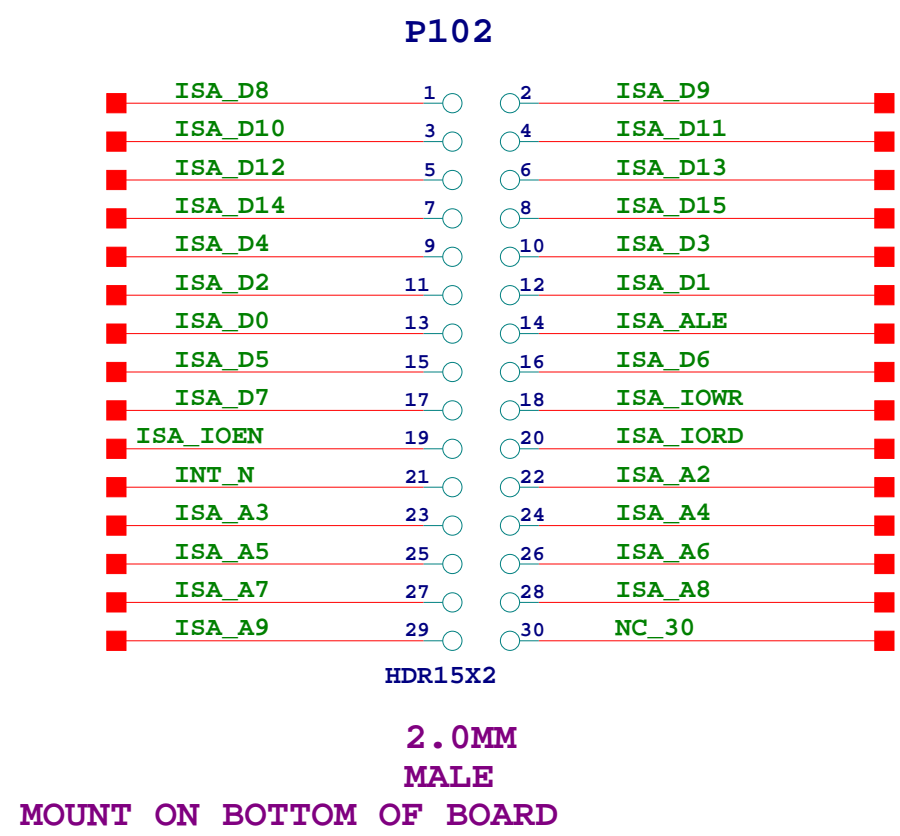


USB

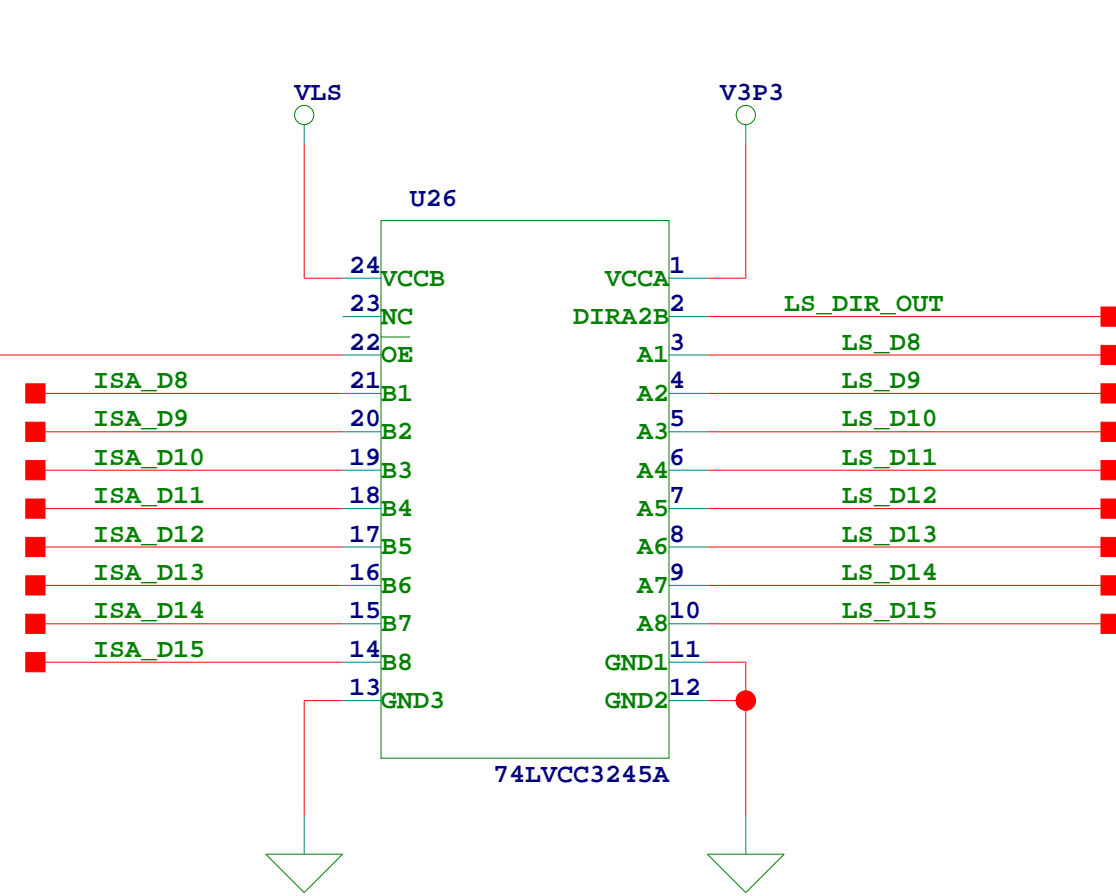
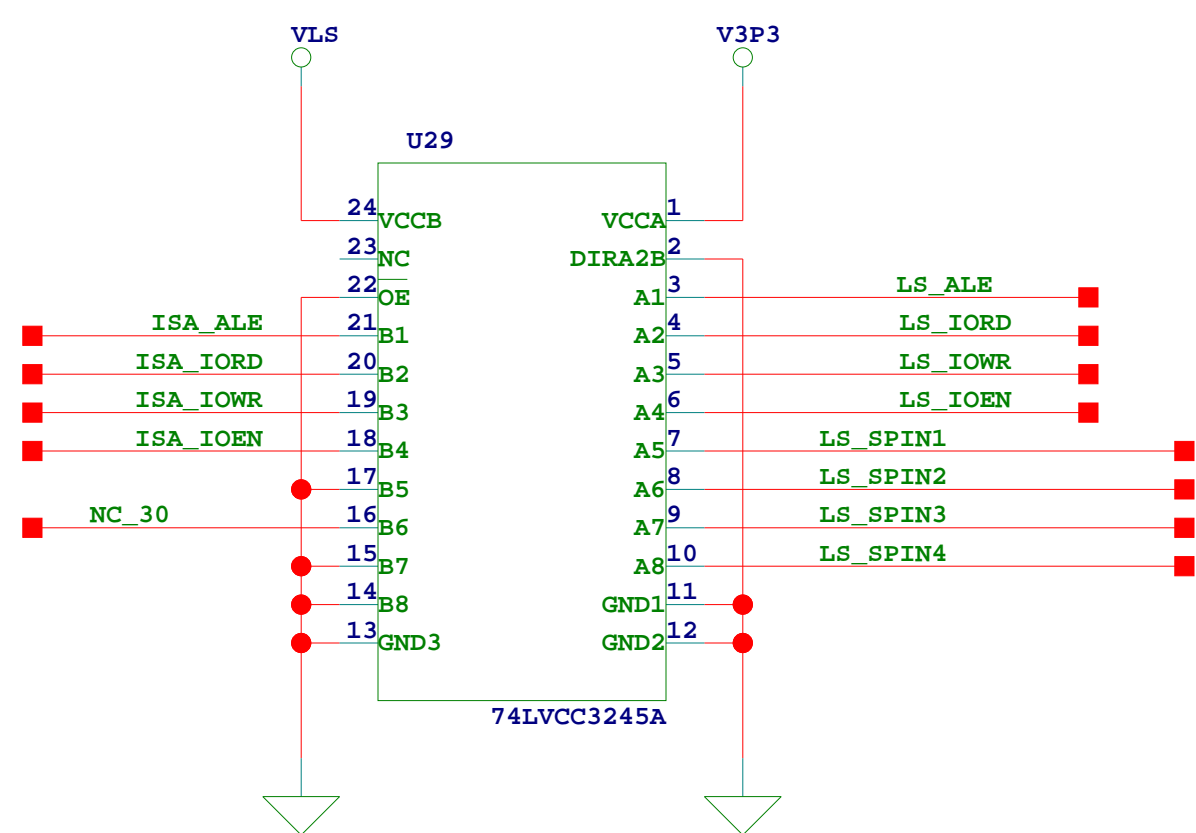
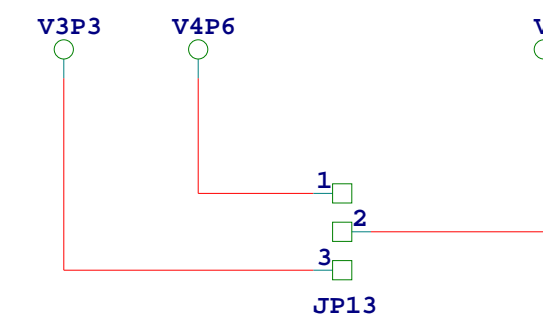


DRAFTER	DATE	SoC Solutions			
JIM AVANT	DATE				
CHECKER	DATE	TITLE			
ENGINEER	DATE				
APPROVAL	DATE	SIZE	DRAWING NUMBER		
APPROVAL	DATE				
DATE		SHEET		4 OF 8	

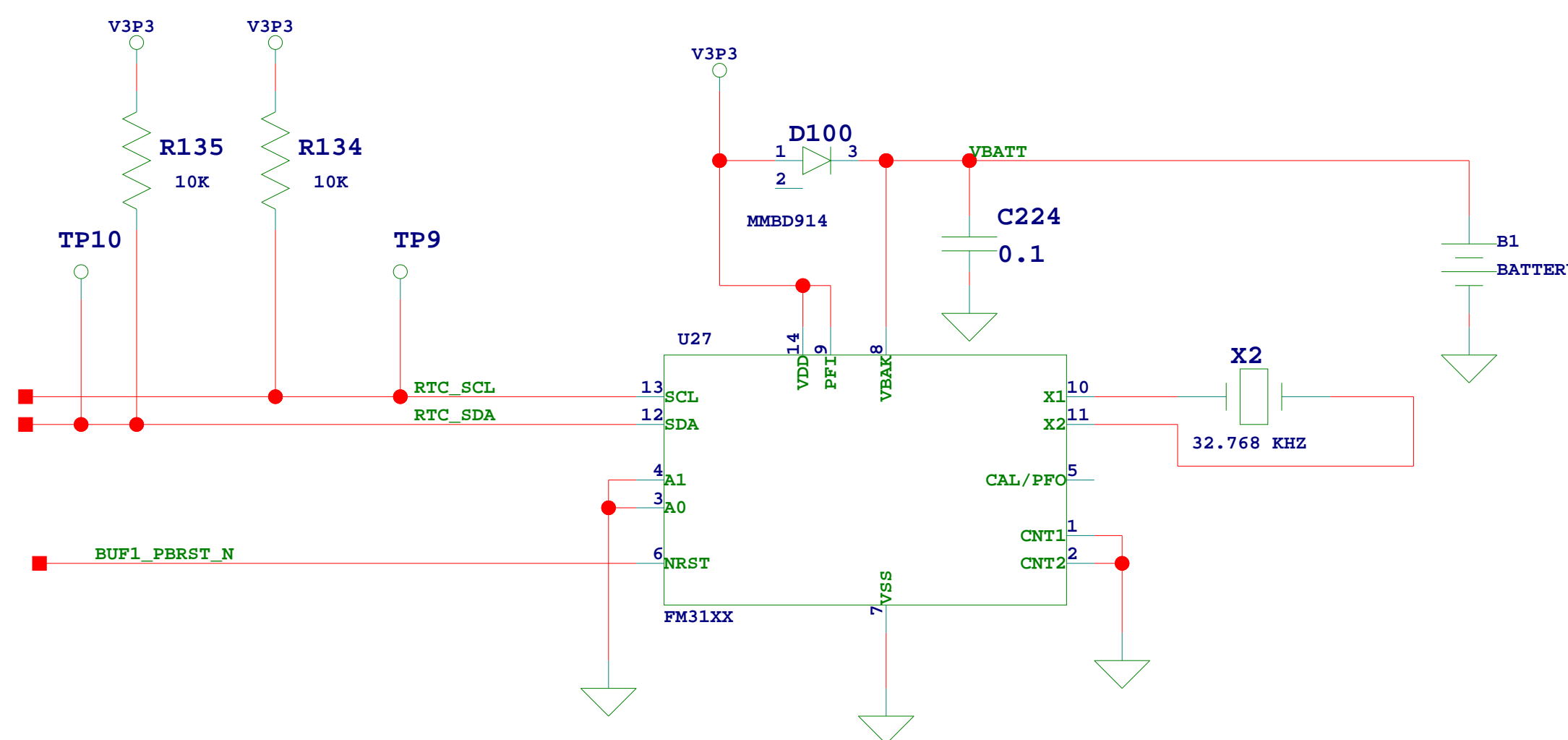
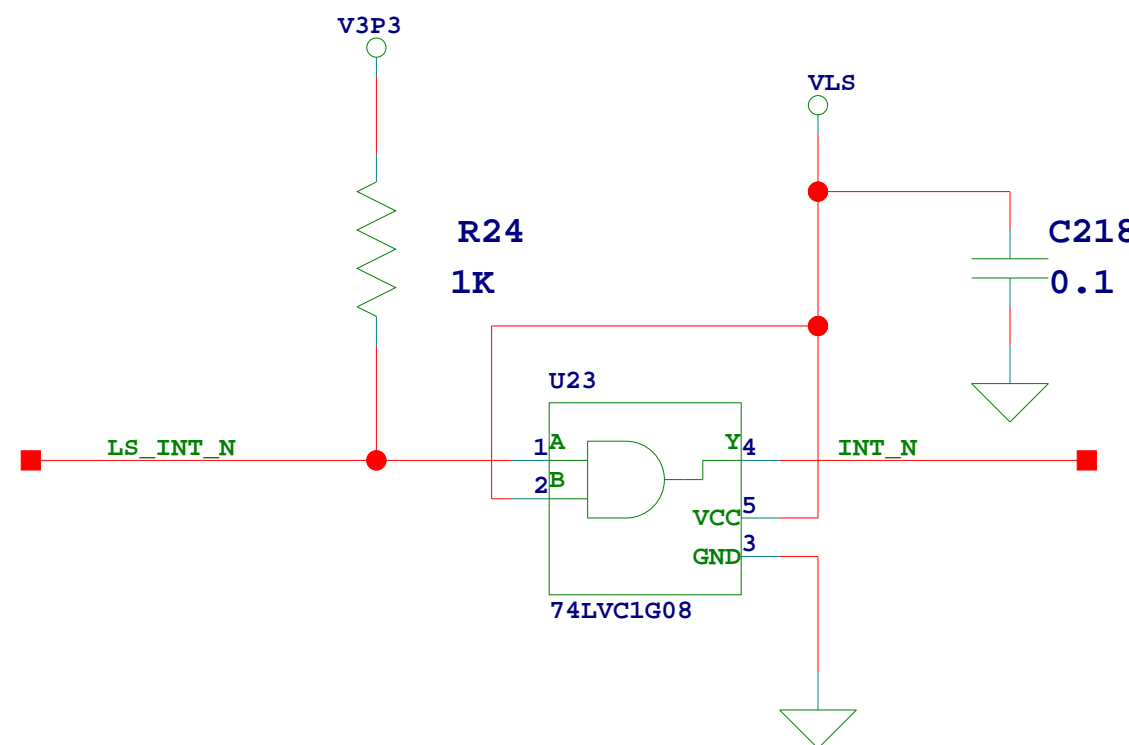
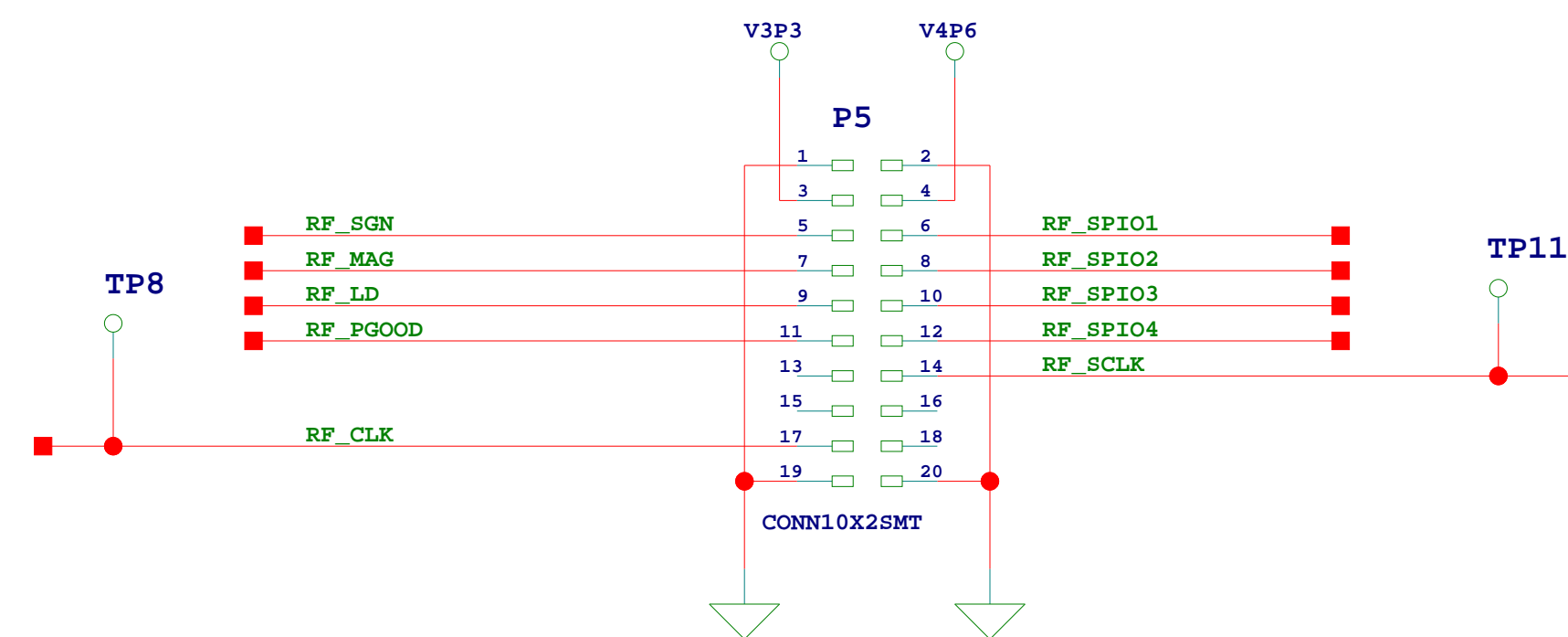
PC INTERFACE SIGNALS



LEVEL SHIFTER VOLTAGE SELECT

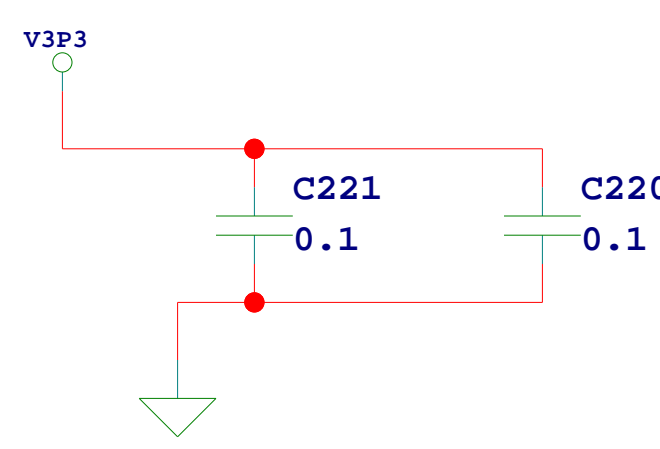
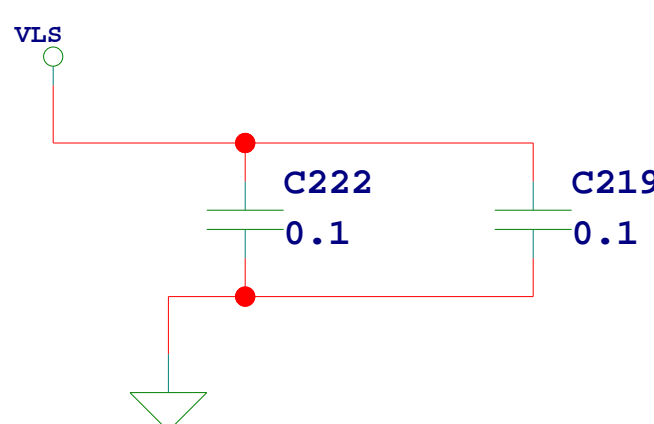


RF INTERFACE

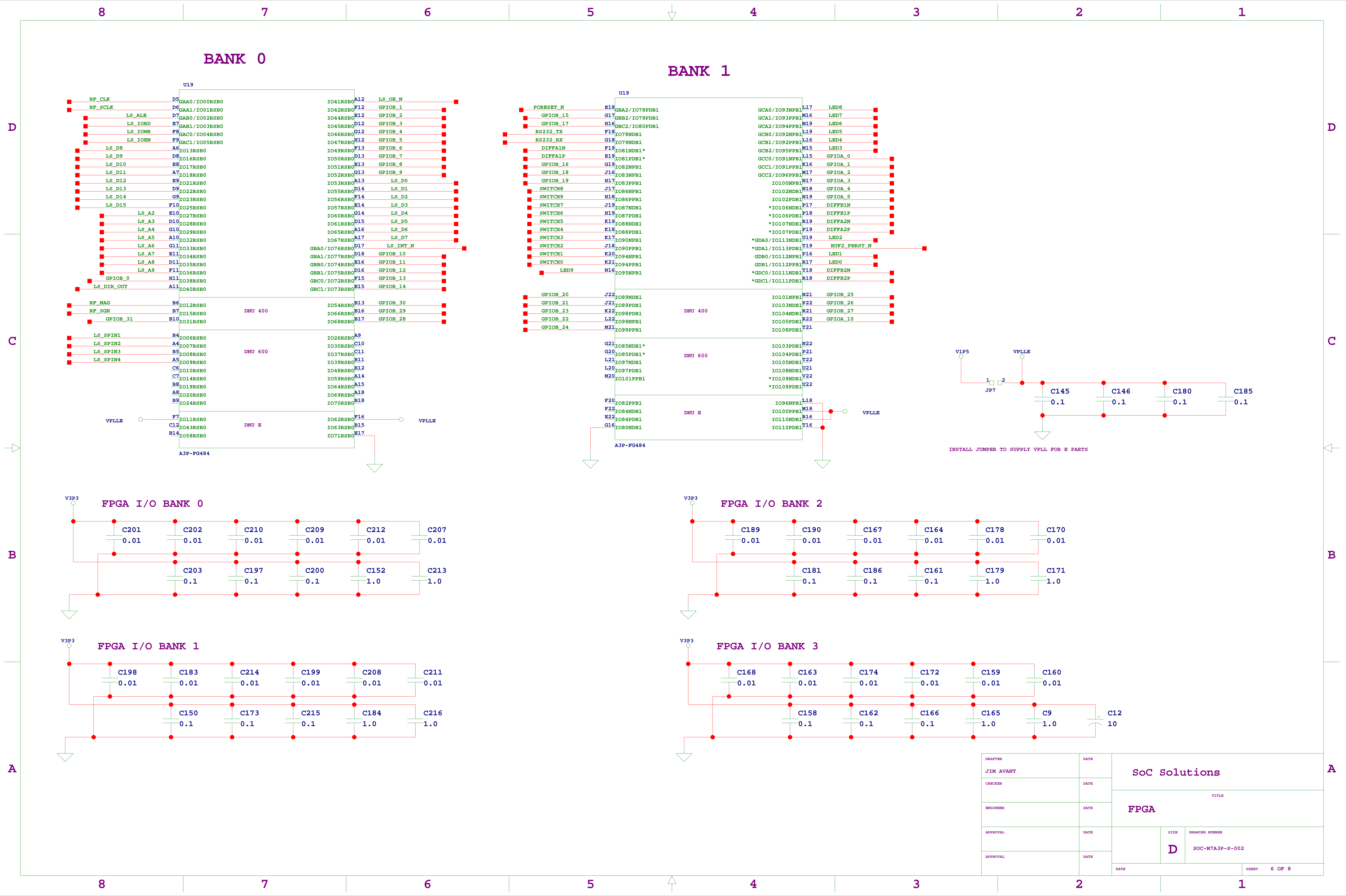


REAL-TIME CLOCK

NOTE: COMPONENTS ON THIS SHEET ARE NOT
NORMALLY STUFFED. FOR PC-BASED GPS APPLICATION
STUFF THIS SHEET AND ADD PCI OR ISA BOARD
AND GPS RF BOARD AVAILABLE FROM SOC SOLUTIONS.



DRAFTER	DATE	SoC Solutions			
JIM AVANT	DATE	TITLE			
CHECKER	DATE	GPS APP. SUPPORT			
ENGINEER	DATE	SIZE			
APPROVAL	DATE	DRAWING NUMBER			
APPROVAL	DATE	SHEET			
DATE		SHEET		5 OF 8	



D

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A

POWER & GND

BANK 2

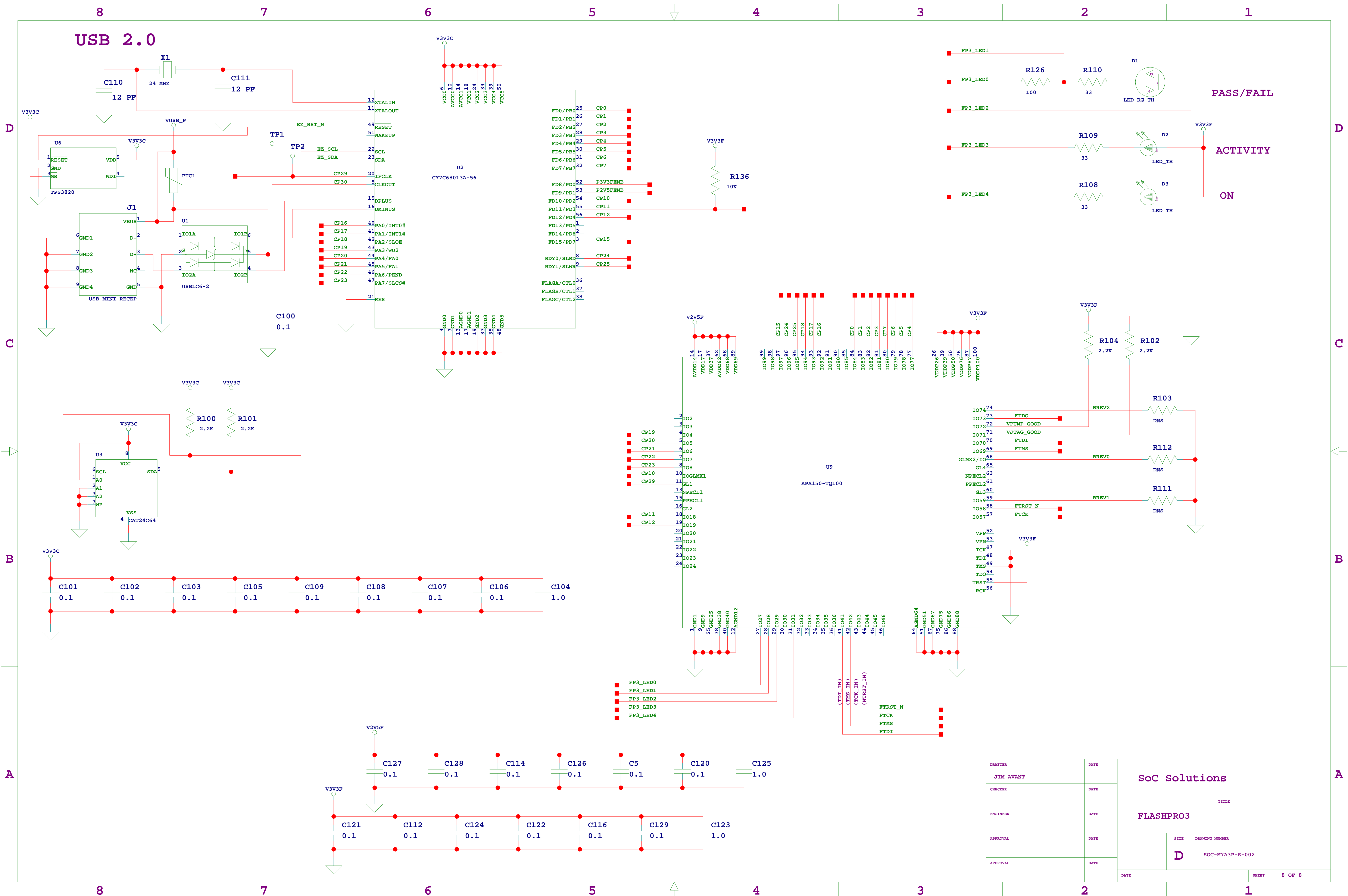
BANK 3

NC

JTAG

FPGA CORE

DRAFTER	DATE	SoC Solutions			
JIM AVANT	DATE				
CHECKER	DATE	FPGA			
ENGINEER	DATE				
APPROVAL	DATE	D	SIZE	DRAWING NUMBER	
APPROVAL	DATE			SOC-M7A3P-S-002	
		DATE	SHEET		7 OF 8



DRAFTER	DATE	SoC Solutions		
JIM AVANT				
CHECKER	DATE	TITLE		
ENGINEER	DATE	FLASHPRO3		
APPROVAL	DATE	SIZE	DRAWING NUMBER	
APPROVAL	DATE	D	SOC-M7A3P-S-002	
DATE		SHEET		8 OF 8