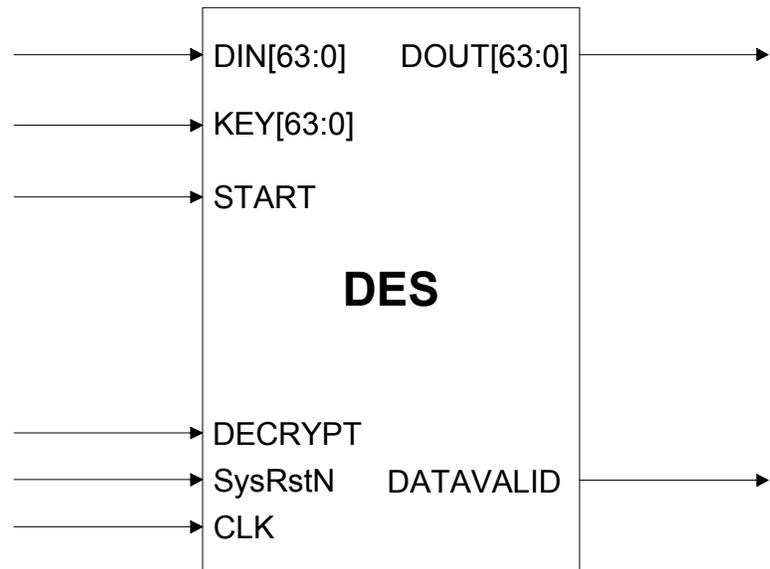


Features

- FIPS 46-3 Standard Compliant
- Encryption/Decryption performed in 16 cycles
- 56 bit security
- For use in FPGA or ASIC designs
- Verilog IP Core

Applications

- Secure video
- Electronic Funds Transfer
- Encrypted Storage Data
- Secure communications



Overview

The **IPC-DES** is a complete implementation of the Data Encryption Standard (DES) documented in the U.S. Government publication FIPS 46-3.

The **IPC-DES** is a block cipher, working on 64 bits of data at a time. Key length is 56 bits. Encoding and decoding operations are performed in only 16 cycles.

The **IPC-DES** can be used in wired, wireless secure data applications, and as a building block for Triple DES.

IP Package

The **IPC-DES** package includes fully tested and verified Verilog source. The **IPC-DES** can also be delivered as an FPGA Netlist for Xilinx, Altera and Actel FPGAs.