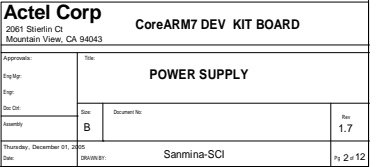


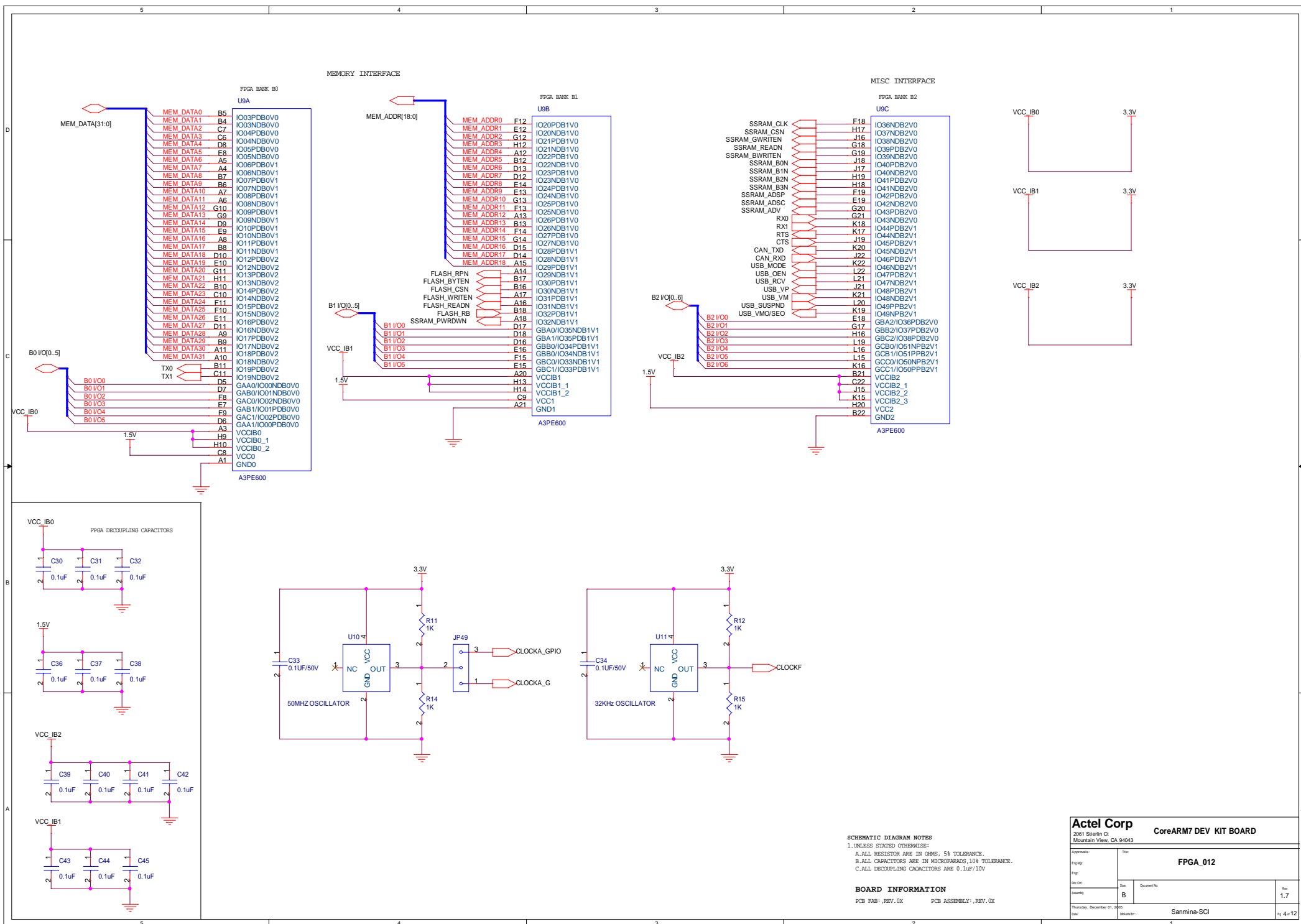
Revision History					
Date	ECO#	Description	Sheet	Initiater	Approval
28/06/05	0.1	Capture the full schematic	1 to 13	Actel	
29/06/05	0.2	Capture the full schematic	1 to 13	Actel	
01/07/05	0.3	Capture the full schematic	1 to 13	Actel	
04/07/05	0.4	Updated all correction by checking the netlist	1 to 12	Actel	
07/07/05	0.5	Corrected the switch and oscillator	2,4	Actel	
07/08/05	0.6	Up dated the manufacturer and Manufacturer part number	1 to 12	Actel	
07/12/05	0.7	Up dated the manufacturer and Manufacturer part number	1 to 12	Actel	
07/21/05	0.8	Changed the schematic on pages	2,8,9,11	Actel	
07/25/05	0.9	Changed the schematic on pages	2,11,12	Actel	
11/15/05	1.1	Changed schematics on pages	1 to 12	Actel	
11/17/05	1.2	Changed Asynchronous SRAM by Synchrnous SRAM	3	Actel	
11/17/05	1.3	Changed the values of R84 and R85	10	Actel	
11/21/05	1.4	Changed schematics on pages	1 to 12	Actel	
11/23/05	1.5	Changed schematics on pages	1 to 12	Actel	
11/28/05	1.6	Added R101 back in schematics	6	Actel	
12/01/05	1.7	Updated the Part numbers	1 to 12	Actel	

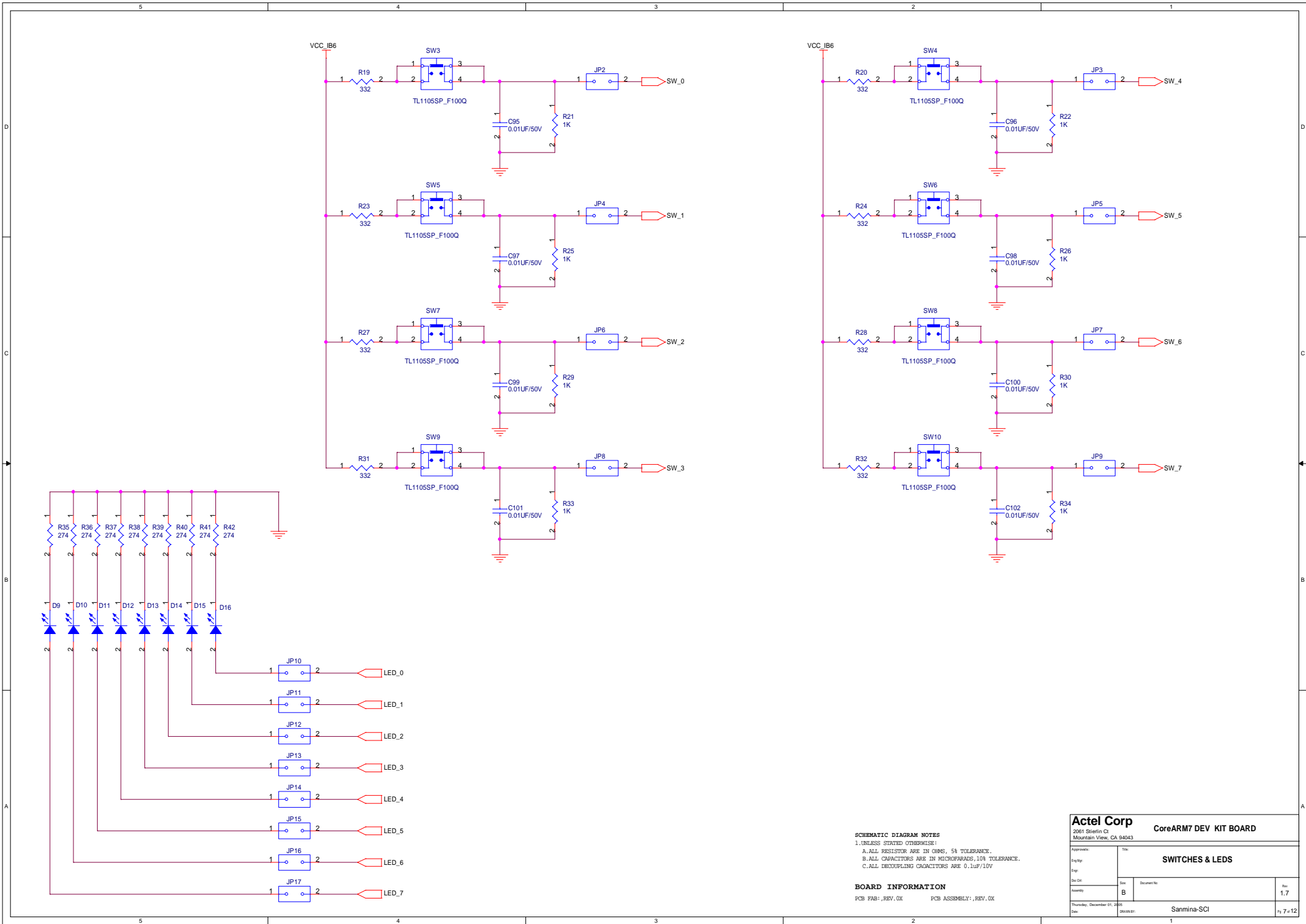
CoreARM7 DEV KIT BOARD

SCHEMATIC DIAGRAM NOTES
1.UNLESS STATED OTHERWISE:
A.ALL RESISTOR ARE IN OHMS, 5% TOLERANCE.
B.ALL CAPACITORS ARE IN MICROFARADS,10% TOLERANCE.
BOARD INFORMATION
PCB FAB: ,REV.0X PCB ASSEMBLY: ,REV.0X

Actel Corp 2061 Shiloh Ct Mountain View, CA 94043		CoreARM7 DEV KIT BOARD	
Appreviate:		Title:	
Eng/Ver:		FRONT PAGE	
Date:		Date:	
Assemble:		Rev:	
B		1.7	
Thursday, December 01, 2005		Sanmina-SCI	
Date:		pg 1 of 12	



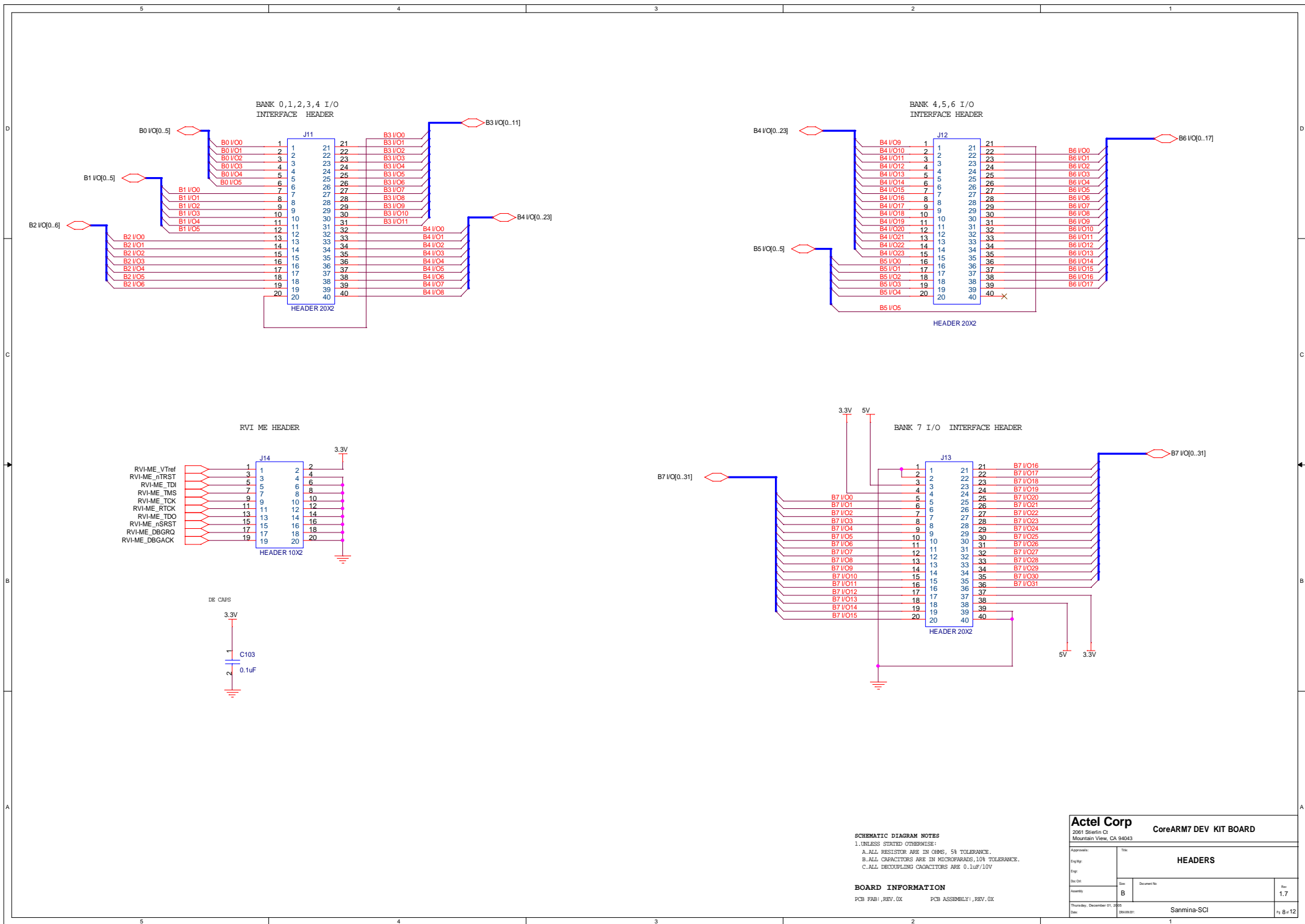




SCHEMATIC DIAGRAM NOTES
1. UNLESS STATED OTHERWISE:
A. ALL RESISTORS ARE IN OHMS, 5% TOLERANCE.
B. ALL CAPACITORS ARE IN MICROFARADS, 10% TOLERANCE.
C. ALL DECOUPLING CAPACITORS ARE 0.1uF/10V

BOARD INFORMATION
PCB FAB: ,REV. 0X PCB ASSEMBLY: ,REV. 0X

Appendix		Title	
Eng/Rev		SWITCHES & LEADS	
Date Del		Rev	
Assembly		B	
Thursday, December 01, 2005		Sanmina-SCI	
Date		1	



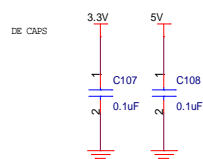
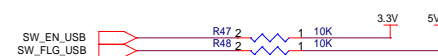
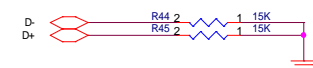
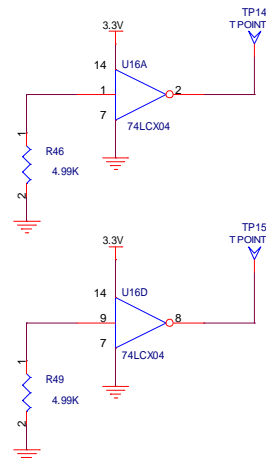
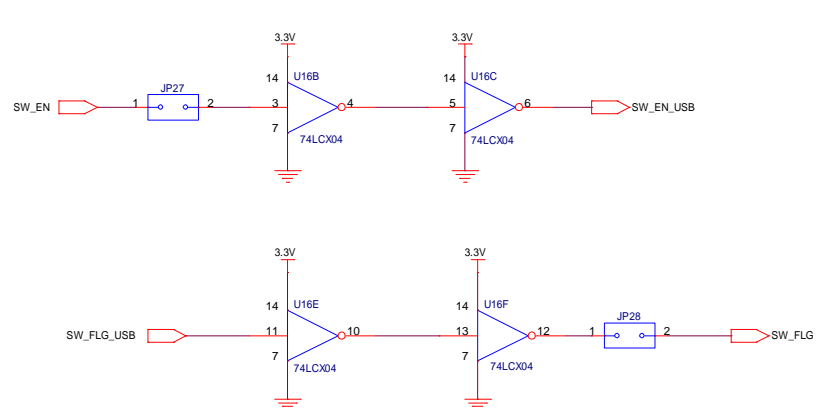
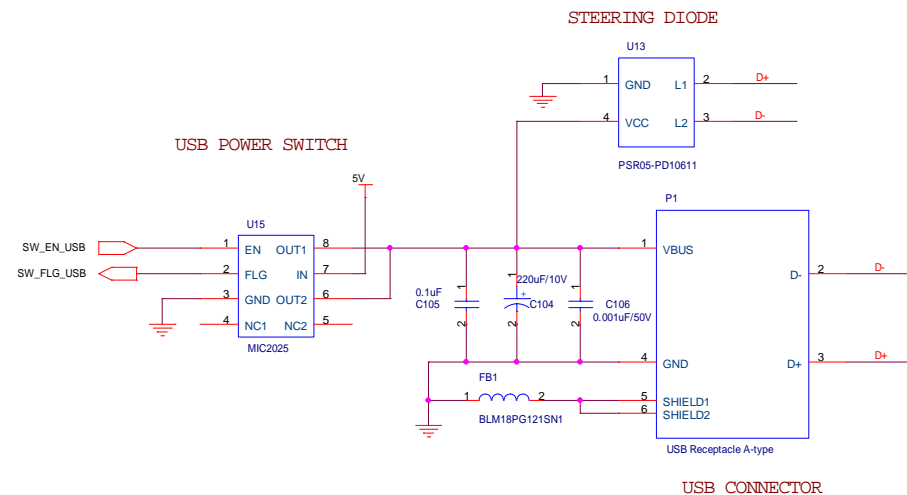
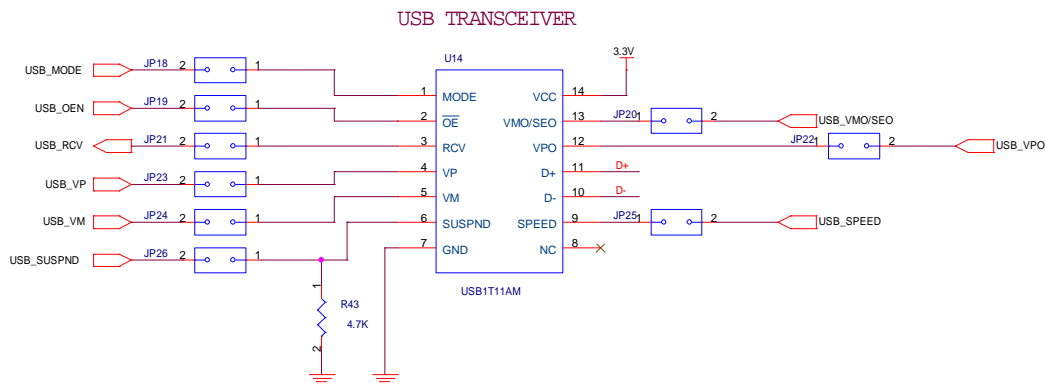
SCHEMATIC DIAGRAM NOTES

1. UNLESS STATED OTHERWISE:
- A. ALL RESISTORS ARE IN OHMS, 5% TOLERANCE.
- B. ALL CAPACITORS ARE IN MICROFARADS, 10% TOLERANCE.
- C. ALL DECOUPLING CAPACITORS ARE 0.1uF/10V

BOARD INFORMATION

PCB FAB: ,REV. 0X PCB ASSEMBLY: ,REV. 0X

Actel Corp		CoreARM7 DEV KIT BOARD	
2061 Shiloh Ct. Mountain View, CA 94043			
Appendix:	Title		
Eng/Ver:	HEADERS		
Doc Ctrl:	Doc:	Document No:	Rev
Assembly:	B		1.7
Thursday, December 01, 2005		Sanmina-SCI	
Date:	09/04/07	Pg 8 of 12	



SCHEMATIC DIAGRAM NOTES

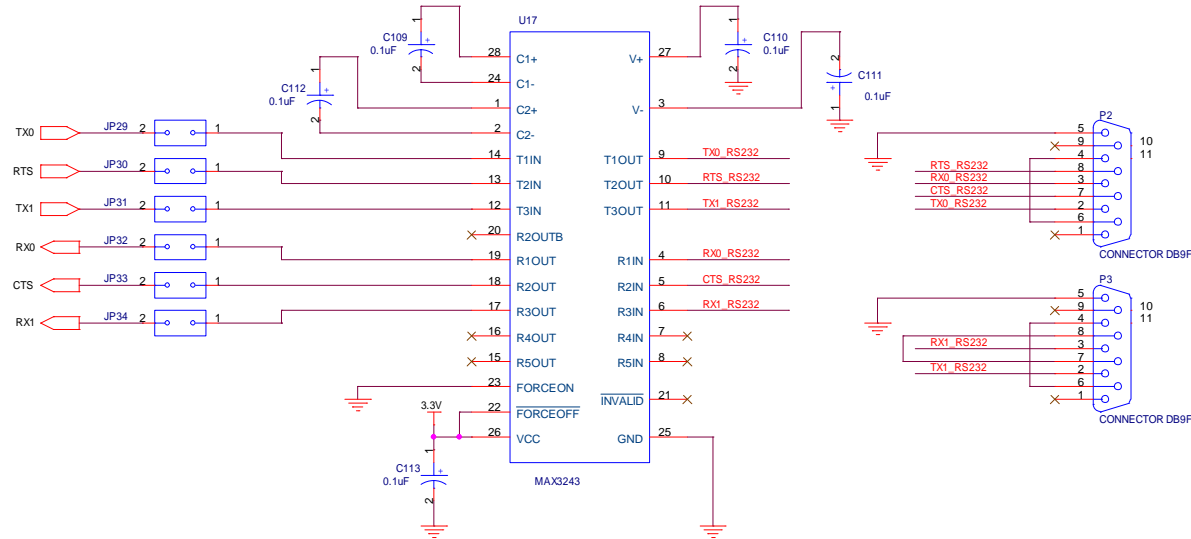
1. UNLESS STATED OTHERWISE:
 A. ALL RESISTORS ARE IN OHMS, 5% TOLERANCE.
 B. ALL CAPACITORS ARE IN MICROFARADS, 10% TOLERANCE.
 C. ALL DECOUPLING CAPACITORS ARE 0.1uF/10V

BOARD INFORMATION

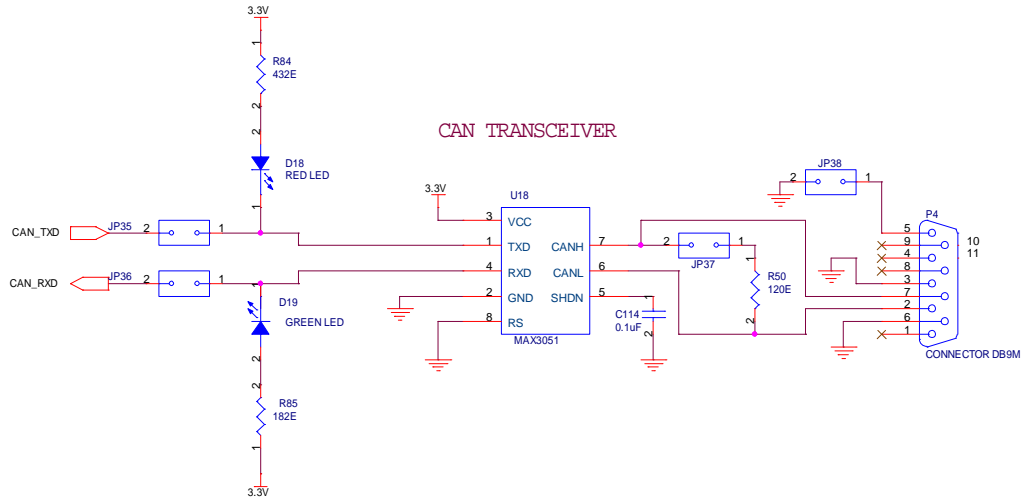
PCB FAB: REV. 0X PCB ASSEMBLY: REV. 0X

Actel Corp		CoreARM7 DEV KIT BOARD	
2061 Sterlin Ct Mountain View, CA 94043			
Appreviate:	Title: USB		
Eng Mgr:			
Eng:			
Doc Ctrl:	Size:	Document No:	Rev:
Assembly:	B		1.7
Thursday, December 01, 2005		Sanmina-SCI	
Date:	2005/08/01	Pg 9 of 12	

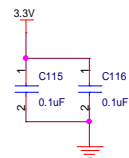
RS-232 TRANSCEIVER



CAN TRANSCEIVER



DECOUPLING CAPACITORS



SCHEMATIC DIAGRAM NOTES

- UNLESS STATED OTHERWISE:
 - ALL RESISTORS ARE IN OHMS, 5% TOLERANCE.
 - ALL CAPACITORS ARE IN MICROFARADS, 10% TOLERANCE.
 - ALL DECOUPLING CAPACITORS ARE 0.1uF/10V

BOARD INFORMATION

PCB FAB: ,REV.0X PCB ASSEMBLY: ,REV.0X

Actel Corp		CoreARM7 DEV KIT BOARD	
2061 Shiloh Ct Mountain View, CA 94043			
Appendix:	Title:	CAN_RS-232_TRANSCEIVER	
Eng/Ver:	Rev:	Document No:	Rev:
Doc Ctrl:	B		1.7
Assembly:			
Thursday, December 01, 2005	Sanmina-SCI	pg10 of 12	
Date:	30/09/07		

