Second Thursdays

May 14 - Webinar 13: Two Bare-Metal Applications on PolarFire[®] SoC

- June 11 Webinar 14: The PolarFire SoC Icicle Kit Model in Renode
- July 9 Webinar 15: Linux[®] on Renode
- Aug. 13 Webinar 16: Building Applications for Linux on PolarFire SoC
- Sep. 10 Webinar 17: Real-Time (AMP Mode) on PolarFire SoC

Thank you for joining early, our presentation will start on the hour



Getting Started with the RISC-V Based PolarFire[®] SoC FPGA Webinar Series Session 13 Two Bare-Metal Applications on PolarFire[®] SoC



A Leading Provider of Smart, Connected and Secure Embedded Control Solutions



Hugh Breslin, Design Engineer Thursday May. 14, 2020

Supporting Content

www.microsemi.com/Mi-V "Renode Webinar Series"



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Webinar 1 (May 2): Discover Renode for PolarFire™ SoC Design and Debug

In this introductory session, we will provide you with an overview of SoftConsole 6.0 with Renode™ integration. We will introduce you to the Renode development framework and provide an overview of the platform and its features. You will also learn about the PolarFire™ SoC architecture and how to use Renode to develop your application.

Webinar 1: Discover Renode for PolarFire[®] SoC Design and Debug

Webinar 2: How to Get Started with Renode for PolarFire SoC

Webinar 3: Learn to Debug a Bare-Metal PolarFire SoC Application with Renode

Webinar 4: Tips and Tricks for Even Easier PolarFire SoC Debug with Renode

Webinar 5: Add and Debug PolarFire SoC Models with Renode

Webinar 6: Add and Debug Pre-Existing Model in PolarFire SoC

Webinar 7: How to Write Custom Models

Webinar 8: What's New in SoftConsole v6.2

Webinar 9: Getting Started with PolarFire SoC

Webinar 10: Introduction to the PolarFire SoC Bare-Metal Library

Webinar 11: Handling Binaries

Webinar 12: Simple Peripheral as Software Stimulus



Agenda

- Hart Software Services
- HSS Boot Image
- Creating the HSS Boot Image
- Building the HSS





- HSS is an application that runs on the E51
- It uses a superloop monitor to operate
- U54s can request the E51 to perform certain tasks / services on their behalf
- Features a Machine-Mode soft interrupt handler
 - Allows the E51 to send messages to the U54s
 - Named "SSMB" Secure Software Message Bus
- Release scheduled for June through GitHub



• Services available include:

- Boot
- Crypto
- DDR
- FlashFreeze
- Goto
- Ipi_poll
- Opensbi
- Powermode
- Qspi
- Sgdma
- Spi
- UART
- Watchdog
- Y-Modem



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4000 HSS_Init(): Vendor: 0, Arch: 0, MISA: ACIM

4000 HSS_DDRInit(): Initializing DDR... 4000 HSS_DDR_Train(): running DDR training on hart 0... 4000 HSS_TinyCLI_Parser(): Press a key to enter CLI, ESC to skip 4100 HSS_TinyCLI_Parser(): Timeout in 5 seconds 254200 HSS_TinyCLI_Parser(): CLI check timeout 254200 HSS_RegistryInit(): Initializing Registry... 254200 IPI_QueuesInit(): Initializing IPI Queues (68136 bytes @ 0x000000000000000000 256900 HSS_BootInit(): Checking memory... 257000 HSS_BootInit(): Initializing Permissions... 258100 HSS_BootInit(): Initializing Clocks and IO... 258300 HSS_BootInit(): Initializing Boot Image.. 258300 HSS_BootInit(): Preparing to decompress to DDR... <u>258300 HSS_Decompress()</u>: Compressed Image is missing magic value (00000402 vs C08B8 258300 HSS_BootInit(): decompressed 0 bytes... 258400 HSS_BootInit(): Boot Image NULL, ignoring 258400 HSS_Init(): HSS_BootInit() returned 0 258400 HSS_OpenSBIInit(): Initializing OpenSBI 258400 HSS_Init(): HSS_OpenSBIInit() returned 0 258400 HSS_Init(): Init finished... 258500 RunStateMachines(): uart_service::init -> uart_service::state1 258500 RunStateMachines(): ddr_service::Init -> ddr_service::Idle 258600 RunStateMachines(): opensbi_service::init -> opensbi_service::idle 258600 RunStateMachines(): loop 1 took 14090 ticks (max 14090 ticks) 55871500 RunStateMachines(): ddr_service::Idle -> ddr_service::Retrain 55871500 HSS_DDR_Train(): running DDR training on hart 0... 55871600 RunStateMachines(): ddr_service::Retrain -> ddr_service::Idle 🛐 1 2 3 4 🗖 🙋 🔲 📩 hart-soft...eb2919963 🛛 🔚 gterminal - 4 windows 🝅 vms

Initialise DDR

Configure system Initialize and decompress **boot image**





• The HSS Boot Image is the result of merging two binaries



• This is accomplished using the included bin2chunks tool

 bin2chunks will extract and compile the data from both images into one in a fixed structure



- The image contains:
 - A header
 - A table of initialized boot chunks (code and data)
 - A table of BSS and zero-init chunks
 - The data itself





31 30 29 28 27 26 25 24 23 22 21 20 19 18 17 16 15 14 13 12 11 10 9 8 7 6 5 4 3 2 1 0

struct HSS_BootImage header
[
struct HSS_BootChunkDesc bootChunksTable[]
[
struct HSS_BootZIChunkDesc ziChunksTable[]
char chunkPayloads[]

uint32_t magic	A magic value at the start of the boot image to identify the file as a valid image.
$size_t$ headerLength	The length of the header in bytes.
<pre>size_t chunkTableOffset</pre>	The offset in bytes to the start of the chunk table, starting from the
	first byte of the magic value.
<pre>size_t ziChunkTableOffset</pre>	The offset in bytes to the start of the ZI chunk table, starting from
-	the first byte of the magic value.
<pre>uintptr_t entryPoint</pre>	The entry point address for a particular hart to jump to once boot
	has completed.
<pre>uint8_t privMode</pre>	The privilege mode to start that particular hart in.
$size_t$ numChunks	The number of chunks specific to that particular hart.
$size_t$ firstChunk	The location of the first chunk for that particular hart in the chunks
	table.
$size_t$ lastChunk	The location of the last chunk for that particular hart in the chunks
	table.
char name[]	A filename for a particular hart's boot image.
char bootSetName[]	A name to describe the collection of all hart boot images.
$size_t$ bootImageLength	The overall length of the boot image, starting at the first byte of the
	magic value.
uint_8 hash[]	The hash digest of the boot image.
uint_8 ecdsaSig[]	The signature of the boot image.
- 0	



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31 30 29 28 27 26 25 24 23 22 21 20 19 18 17 16 15 14 13 12 11 10 9 8 7 6 5 4 3 2 1 0

struct HSS_BootImage header

struct HSS_BootChunkDesc bootChunksTable[]

struct HSS_BootZIChunkDesc ziChunksTable[]

char chunkPayloads[]

 $31 \ 30 \ 29 \ 28 \ 27 \ 26 \ 25 \ 24 \ 23 \ 22 \ 21 \ 20 \ 19 \ 18 \ 17 \ 16 \ 15 \ 14 \ 13 \ 12 \ 11 \ 10 \ 9 \ 8 \ 7 \ 6 \ 5 \ 4 \ 3 \ 2 \ 1 \ 0$

enum HSSHartId owner

uintptr_t loadAddr

 $uintptr_t$ execAddr

size_t size

uint32_t crc32

and the second second process of the second se

enum HSSHartId owner	Indicates which hart owns this chunk. This is used to ensure that chunks
	for a particular hart are only downloaded to memory regions permitted by
	PMP settings for that hart.
$uintptr_t$ loadAddr	The offset from the first byte of magic where the chunked data will be in
	memory at load time.
$uintptr_t$ execAddr	The address where the chunked lives in memory at execution time.
<i>size_t</i> size	The size of the chunk.
<i>uint32_t</i> crc	A CRC32 of the chunk.



31 30 29 28 27 26 25 24 23 22 21 20 19 18 17 16 15 14 13 12 11 10 9 8 7 6 5 4 3 2 1 0

struct HSS_BootImage header

struct HSS_BootChunkDesc bootChunksTable[]

struct HSS_BootZIChunkDesc ziChunksTable[]

char chunkPayloads[]

$31 \ 30 \ 29 \ 28 \ 27 \ 26 \ 25 \ 24 \ 23 \ 22 \ 21 \ 20 \ 19 \ 18 \ 17 \ 16 \ 15 \ 14 \ 13 \ 12 \ 11 \ 10 \ 9 \ 8 \ 7 \ 6 \ 5 \ 4 \ 3 \ 2 \ 1 \ 0$

enum HSSHartId owner

uintptr_t execAddr

size_t size

enum HSSHartId owner	Indicates which hart owns this chunk. This is used to ensure that chunks for a particular hart are only downloaded to memory regions permitted by PMP softings for that hart
<pre>uintptr_t execAddr size_t size</pre>	The address where the chunked lives in memory at execution time. The size of the chunk.





hart-software-services

- ---- baremetal
- drivers (legacy)
- └── polarfire-soc-bare-metal-library (subtree)
- ---- compression
- ---- debug (helper routines for function profiling)
- include
- ---- init (system initialization)
- misc
- services (software service state machines)
- ---- boot
- crypto
- ├— ddr
- ---- flashfreeze
- ├--- goto
- ├— ipi_poll
- ├— opensbi
- powermode
- ├— qspi
- ├— sgdma
- ├— spi
- ├— uart
- ---- wdog
- └— ymodem
- ---- ssmb (secure software message bus)
- └— ipi
- └── thirdparty
- ----- fastlz (fast lossless compression library)
- ----- opensbi (RISC-V OpenSBI)
- └── riscv-pk (RISC-V Proxy Kernel legacy)

<mark>— tools</mark>

<mark>— bin2chunks</mark>







Bin2chunks takes 12 arguments:

- 1. U54_1 entry point
- 2. U54_2 entry point
- 3. U54_3 entry point
- 4. U54_4 entry point
- 5. Chunk limit (4096)
- 6. Output file name
- 7. Owner of payload 1
- 8. Payload 1 name
- 9. Memory address to place payload 1
- 10. Owner of payload 2
- 11. Payload 2 name
- 12. Memory address to place payload 2



\$_	miv@mi-pc: ~/Downloads/ALOE_HSS	- 0
File Actions Edit View Help		
miv@mi-pc: ~/Downloads/ALOE_HSS		
<pre>niv@mi-pc:~/Downloads/ALOE_HSS\$./bin2chunks 0x</pre>	C4000000 0xC4000000 0x84000000 0x84000000 4096 hss_boot.bin 1 payload1.bin 0xC4000000 3 payload2.bin 0x8	34000000

- 1. U54_1 entry point is 0xC4000000
- 2. U54_2 entry point is 0xC4000000
- 3. U54_3 entry point is 0x84000000
- 4. U54_4 entry point is 0x84000000
- 5. Chunk limit (4096)
- 6. hss_bootImage.bin is the output file name
- 7. 1 => U54_1 is the owner of the following binary
- 8. payload1.bin is the binary name
- 9. 0xC4000000 is where payload1 will be placed
- 10. 3 => U54_3 is the owner of the following binary
- 11. payload2.bin is the binary name
- 12. 0x84000000 is where payload2 will be placed



s_	miv@mi-pc: ~/Downloads/ALOE_HSS/tools/bin2chunks	- e x
File Actions Edit View Help		
miv@mi-pc: ~/Downloads/ALOE	HSS/tools/bin2chunks 🛛 🛞	
<pre>miv@mi-pc:~/Downloads/ALOE_ entryPoint[0] set to c40000 entryPoint[1] set to c40000 entryPoint[2] set to 840000 entryPoint[3] set to 840000 chunkSize set to 4096 output file set to >>hss_bo - processing image 1 - hart owner is >>1<< - input file is >>payload1 execAddr[0] set to c4000000 - binSize[0] is 1616 - processing image 2 - hart owner is >>3<< - input file is >>payload2 execAddr[1] set to 84000000 - binSize[1] is 1616 Boot Image Size: Boot Image Padded Size:</pre>	HSS/tools/bin2chunks\$./bin2chunks 0xC4000000 0xC40000000 0x840000000 4096 hss_boot.bin 1 payload1.bin 0xC40000000 3 payload2.bin 0x84000 00	900
Number of Chunks: Chunk Table Size: Chunk Table Padded Size:	3 120 120	
Number of ZI Chunks: ZI Chunk Table Size: ZI Chunk Table Padded Size:	1 24 24	
./bin2chunks: 9888 bytes wr ./bin2chunks: headerCrc is miv@mi-pc:~/Downloads/ALOE_	itten to >>hss_boot.bin<< 0x3D022642 HSS/tools/bin2chunks \$	



Building the HSS



Building the HSS

- Create your HSS boot image
- Place the resulting image in your board folder



• Build the HSS



Building the HSS

miv@mi-pc: ~/Downloads/hss ...arfiresoc-merge@3beb2919963 🛞 miv@mi-pc:~/Downloads/hss_/hart-software-services-aloevera-polarfiresoc-merge@3beb2919963\$ make MACHINE=icicle Makefile:69: BOARD not specified ICICLE selected rules.mk:97: Not enabling -flto as stack protector enabled baremetal/polarfire-soc-bare-metal-library/src/platform/mpfs hal/mss mutex.d MAKEDEP MAKEDEP crt.d MAKEDEP misc/stack guard.d services/qspi/qspi_api.d MAKEDEP MAKEDEP baremetal/svsreq.d MAKEDEP baremetal/polarfire-soc-bare-metal-library/src/platform/mpfs hal/mss stubs.d baremetal/polarfire-soc-bare-metal-library/src/platform/mpfs_hal/mss_util.d MAKEDEP baremetal/polarfire-soc-bare-metal-library/src/platform/mpfs hal/nwc/mss sgmii.d MAKEDEP baremetal/polarfire-soc-bare-metal-library/src/platform/mpfs_hal/nwc/mss_pll.d MAKEDEP baremetal/polarfire-soc-bare-metal-library/src/platform/mpfs hal/nwc/mss io.d MAKEDEP MAKEDEP baremetal/polarfire-soc-bare-metal-library/src/platform/mpfs_hal/nwc/mss_ddr.d baremetal/polarfire-soc-bare-metal-library/src/platform/mpfs_hal/nwc/mss_nwc_init.d MAKEDEP MAKEDEP baremetal/drivers/mss_watchdog/mss_watchdog.d baremetal/polarfire-soc-bare-metal-library/src/platform/drivers/mss mmuart/mss uart.d MAKEDEP MAKEDEP baremetal/polarfire-soc-bare-metal-library/src/platform/drivers/mss gspi/mss gspi.d MAKEDEP baremetal/polarfire-soc-bare-metal-library/src/platform/drivers/mss pdma/mss pdma.d MAKEDEP thirdparty/opensbi/lib/sbi/sbi_string.d MAKEDEP misc/hss_tinycli.d MAKEDEP misc/hss_progress.d MAKEDEP misc/hss_memcpy_via_pdma.d misc/hss crc32.d MAKEDEP CC build/lib/utils/libfdt/fdt_rw.o CC build/lib/utils/libfdt/fdt_sw.o CC build/lib/utils/libfdt/fdt_strerror.o CC build/lib/utils/libfdt/fdt empty tree.o build/lib/utils/irqchip/plic.o CC AR build/lib/libsbiutils.a LD hss.elf ΝM hss.svm BIN hss.bin HEX hss.hex data bss dec hex filename text 1944 107516 186014 2d69e hss.elf 76554 miv@mi-pc:~/Downloads/hss_/hart-software-services-aloevera-polarfiresoc-merge@3beb2919963\$



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Thank you!

Any questions?



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